SPEC CFP95rate Results

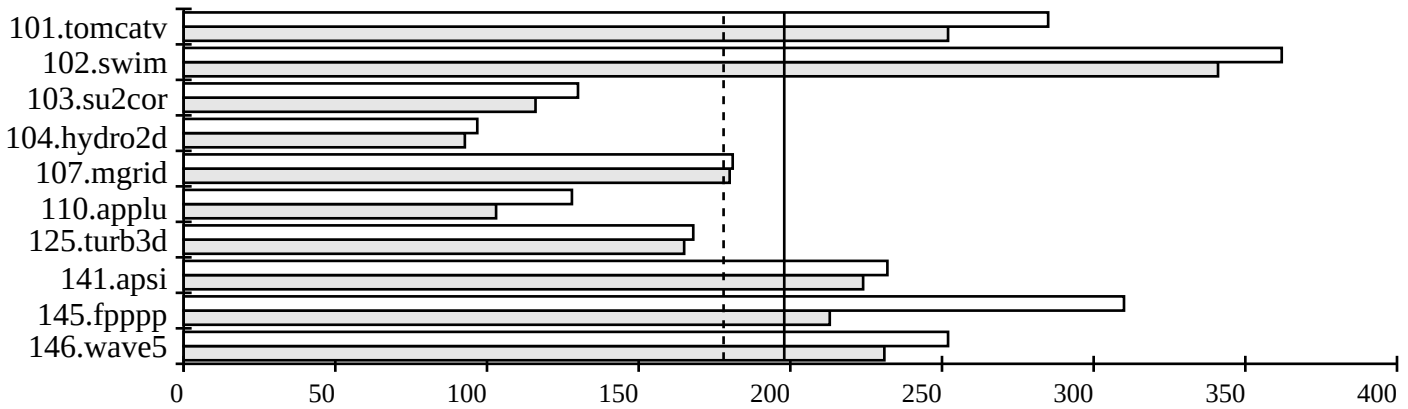
©Copyright 1995, Standard Performance Evaluation Corporation

Sun Microsystems
Ultra 2 Model 1400

SPECfp_rate95 = 198

SPECfp_rate_base95 = 178

SPEC license # 6	Tested By: Sun, Palo Alto	Test Date: Apr-99	Hardware Avail: May-99	Software Avail: Feb-99



Hardware/Software Configuration for:
Ultra 2 Model 1400

Hardware

Model Name:	Ultra 2 Model 1400
CPU:	400 MHz UltraSPARC-II
FPU:	Integrated
Number of CPU(s):	1
Primary Cache:	16KBI+16KBD on chip
Secondary Cache:	2MB(I+D) off chip
Other Cache:	None
Memory:	512MB
Disk Subsystem:	2 x 9.1GB (7200 RPM)
Other Hardware:	Fast Ethernet System clock frequency: 100 MHz

Software

Operating System:	Solaris 2.6 Hardware 3/98
Compiler:	Sun WorkShop FORTRAN 77 5.0
File System:	UFS
System State:	Single User
Kernel Extensions:	consistent_coloring=1

Benchmark # and Name	Base Copies	Base Run Time	Base SPEC Ratio	Copies	Run Time	SPEC Ratio
101.tomcatv	2	264	252	2	234	285
102.swim	2	454	341	2	428	362
103.su2cor	2	217	116	2	194	130
104.hydro2d	2	466	92.7	2	446	96.8
107.mgrid	2	249	180	2	248	181
110.applu	2	384	103	2	308	128
125.turb3d	2	447	165	2	439	168
141.apsi	2	169	224	2	163	232
145.fpppp	2	810	213	2	558	310
146.wave5	2	234	231	2	214	252
SPECfp_rate_base95 (G. Mean)			178			
SPECfp_rate95 (G. Mean)						198

Notes/Tuning Information

```
P1= -xprofile=collect:‘pwd‘/../feedback; P2= -xprofile=use:‘pwd‘/../feedback; FOPTION= -fast -xarch=v8plusa -s
BCOPY = -Qoption iropt -O4+bcopy; INV = -Qoption iropt -O4+invccexp; BMR = -Qoption iropt -O4+bmerge
Baseline: -fast -xarch=v8plusa -fsimple=2 -xprefetch
101.tomcatv: -fast -xarch=v8plusa -xpad=local:23 -xprefetch -Qoption iropt -O4+data_access
102.swim:    -fast -xarch=v8plusa -xpad=common:15 -xprefetch -fsimple=2
103.su2cor:  ${FOPTION} -xvector -Qoption cg -Qms_pipe+nfll=5 -fsimple=2 ${BCOPY}
103.su2cor:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2}
104.hydro2d: ${FOPTION} -xprefetch -Qoption iropt -O4+pde -fsimple=2
104.hydro2d: PASS1= ${P1}; PASS2= ${P2}
107.mgrid:   ${FOPTION} -xprefetch -fsimple=2 ${BCOPY}
107.mgrid:   PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2}
110.applu:   -fast -xarch=v8plusa -Qoption iropt -O4+scalarrep+ddint+perfect+loop_dist+pde+algassoc,-Mt6000,-Mr20000,-Ma140,-ldstr -fsimple=2
125.turb3d:  ${FOPTION} -stackvar -xpad -fsimple=2 -Qoption iropt -O4+bcopy ${INV} -xvector=yes
125.turb3d:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2}
141.apsi:    ${FOPTION} -fsimple=2 -stackvar  -Qoption iropt -Si36  ${INV} ${BMR} -Qoption iropt -O4+algassoc
141.apsi:    PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2}
145.fpppp:   -fast -xarch=v8plusa -xO5 -xsafe=mem -fsimple=2 -nodepend -Qoption iropt -O4+algassoc -Qoption iropt -reroll=1
145.fpppp:   PASS1= ${P1}; PASS2= ${P2}
146.wave5:   ${FOPTION} -stackvar -fsimple=2 -Qoption iropt -O4+unroll ${BCOPY}
Shell limits: # ulimit -n 1024  or % limit descriptors 1024
Config file: ultrabest.cfg
```

For More Information Contact:
SPEC
10754 Ambassador Drive, Suite 201
Manassas, VA 20109
(703) 331-0180
info@spec.org
http://www.spec.org/

--

Prepared By: gnana