SPEC CFP95 Results

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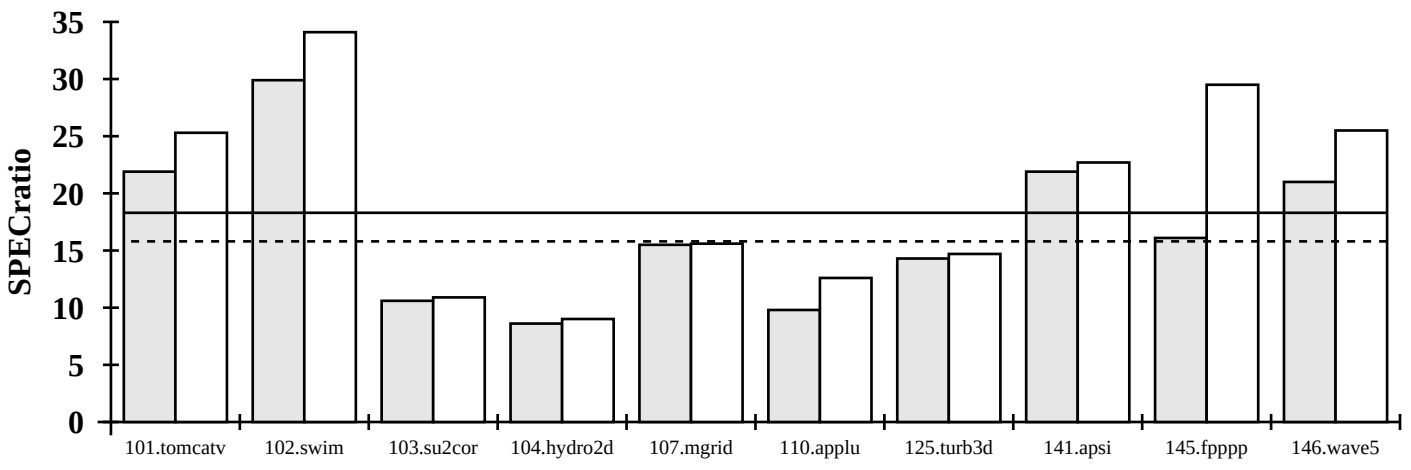
Sun Microsystems
Ultra 5 Model 333

SPECfp95 = 18.3

SPECfp_base95 = 15.8

SPEC license # 6	Tested By: Sun, Palo Alto	Test Date: Sep-98	Hardware Avail: Nov-98	Software Avail: Oct-98



Hardware/Software Configuration for:
Ultra 5 Model 333

Hardware

Model Name:	Ultra 5 Model 333
CPU:	333 MHz UltraSPARC-IIi
FPU:	Integrated
Number of CPU(s):	1
Primary Cache:	16KBI+16KBD on chip
Secondary Cache:	2MB(I+D) off chip
Other Cache:	None
Memory:	512MB
Disk Subsystem:	1 x 9.1GB
Other Hardware:	Fast Ethernet System clock frequency: 111 MHz

Software

Operating System:	Solaris 2.6 3/98
Compiler:	Sun f77 SF775.0 EFT1 (8/98)
File System:	UFS
System State:	Single User
Kernel Extensions:	consistent_coloring=1

Benchmark # and Name	Reference Time	Base Run Time	Base SPEC Ratio	Run Time	SPEC Ratio
101.tomcatv	3700	169	21.9	146	25.3
102.swim	8600	287	29.9	252	34.1
103.su2cor	1400	132	10.6	128	10.9
104.hydro2d	2400	279	8.61	267	9.01
107.mgrid	2500	161	15.5	160	15.6
110.applu	2200	225	9.80	174	12.6
125.turb3d	4100	288	14.3	278	14.7
141.apsi	2100	95.7	21.9	92	22.7
145.fpppp	9600	598	16.1	325	29.5
146.wave5	3000	143	21.0	117	25.5
SPECfp_base95 (G. Mean)			15.8		
		SPECfp95 (G. Mean)			18.3

Notes/Tuning Information

```
P1= -xprofile=collect:‘pwd‘/../feedback
P2= -xprofile=use:‘pwd‘/../feedback
BCOPY=-Qoption iropt -O4+bcopy; INV=-Qoption iropt -O4+invccexp;
Baseline: -fast -xarch=v8plusa -fsimple=2 -xprefetch
Peak:
101.tomcatv: -fast -xarch=v8plusa -xpad=local:23 -xprefetch -Qoption iropt -O4+data_access
102.swim:    -fast -xarch=v8plusa -xpad=common:15 -xprefetch -fsimple=2
103.su2cor:  -fast -xarch=v8plusa -xvector -Qoption cg -Qms_pipe+nfll=5 -fsimple=2  ${BCOPY}
103.su2cor:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2}
104.hydro2d: -fast -xarch=v8plusa -xprefetch -Qoption iropt -O4+pde -fsimple=2
104.hydro2d: PASS1= ${P1}; PASS2= ${P2}
107.mgrid:   -fast -xarch=v8plusa -xprefetch -fsimple=2  ${BCOPY}
107.mgrid:   PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2}
110.applu:   -fast -xarch=v8plusa -Qoption iropt -O4+scalarrep+ddint+perfect+loop_dist+pde+algassoc,-Mt6000,-Mr20000,-Ma140,-ldstr -fsimple=2
125.turb3d:  -fast -xarch=v8plusa -stackvar -xpad -fsimple=2 -Qoption iropt -O4+bcopy ${INV} -xvector=yes
125.turb3d:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2}
141.apsi:    -fast -xarch=v8plusa -xchip=ultra3 -fsimple=2 -stackvar  -Qoption iropt -Si36  ${INV} -Qoption iropt -O4+algassoc
141.apsi:    PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2}
145.fpppp:   -fast -xarch=v8plusa -xO5 -xsafe=mem -fsimple=2 -nodepend -Qoption iropt -O4+algassoc -Qoption iropt -reroll=1
145.fpppp:   PASS1= ${P1}; PASS2= ${P2}
146.wave5:   -fast -xarch=v8plusa  -stackvar -fsimple=2 -Qoption iropt -O4+unroll ${BCOPY}
Shell limits: # ulimit -n 1024  or % limit descriptors 1024
Config file: ultra5_fp.cfg
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