SPEC CFP95 Results

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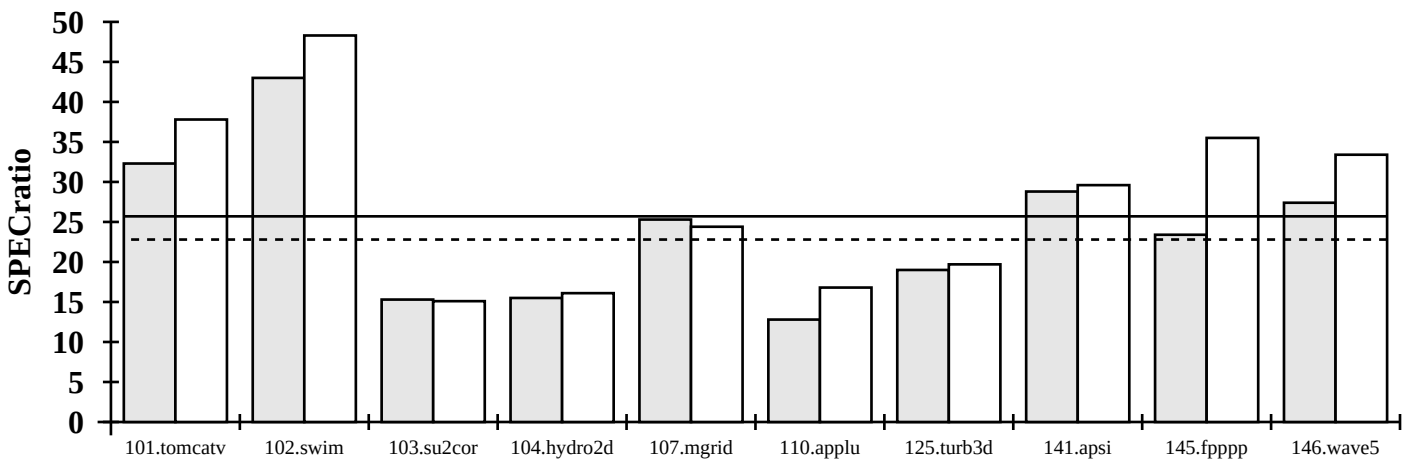
Sun Microsystems
Sun Enterprise 450

SPECfp95 = 25.7

SPECfp_base95 = 22.8

SPEC license # 6 | Tested By: Sun, Palo Alto | Test Date: Oct-98 | Hardware Avail: Nov-98 | Software Avail: Feb-99



Hardware/Software Configuration for:
Sun Enterprise 450

Hardware

Model Name:	Sun Enterprise 450
CPU:	400 MHz UltraSPARC-II
FPU:	Integrated
Number of CPU(s):	1
Primary Cache:	16KBI+16KBD on chip
Secondary Cache:	4MB (I+D) off chip
Other Cache:	None
Memory:	512MB
Disk Subsystem:	2 x 4.2GB
Other Hardware:	Fast Ethernet

Software

Operating System:	Solaris 7 (kernel/unix)
Compiler:	Sun f77 5.0 Early Access
File System:	UFS
System State:	Single User
Kernel Extensions:	consistent_coloring=1

Benchmark # and Name	Reference Time	Base Run Time	Base SPEC Ratio	Run Time	SPEC Ratio
101.tomcatv	3700	114	32.3	98	37.8
102.swim	8600	200	43.0	178	48.3
103.su2cor	1400	91.4	15.3	92	15.1
104.hydro2d	2400	155	15.5	149	16.1
107.mgrid	2500	98.8	25.3	102	24.4
110.applu	2200	172	12.8	131	16.8
125.turb3d	4100	216	19.0	208	19.7
141.apsi	2100	73.0	28.8	71	29.6
145.fpppp	9600	410	23.4	270	35.5
146.wave5	3000	110	27.4	90	33.4
SPECfp_base95 (G. Mean)			22.8		
			SPECfp95 (G. Mean)		25.7

Notes/Tuning Information

```
P1= -xprofile=collect:‘pwd‘/../feedback; P2= -xprofile=use:‘pwd‘/../feedback
BCOPY=-Qoption iropt -O4+bcopy; INV=-Qoption iropt -O4+invccexp
Baseline:    -fast -xarch=v8plusa -fsimple=2 -xprefetch
101.tomcatv: -fast -xarch=v8plusa -xpad=local:23 -xprefetch -Qoption iropt -O4+data_access
102.swim:    -fast -xarch=v8plusa -xpad=common:15 -xprefetch -fsimple=2
103.su2cor:  -fast -xarch=v8plusa -xvector -Qoption cg -Qms_pipe+nfll=5 -fsimple=2  ${BCOPY}
103.su2cor:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
104.hydro2d: -fast -xarch=v8plusa -xprefetch -Qoption iropt -O4+pde -fsimple=2
104.hydro2d:  PASS1= ${P1}; PASS2= ${P2};
107.mgrid:   -fast -xarch=v8plusa -xprefetch -fsimple=2  ${BCOPY}
107.mgrid:   PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
110.applu:   -fast -xarch=v8plusa -Qoption iropt -O4+scalarrep+ddint+perfect+loop_dist+pde+algassoc,-Mt6000,-Mr20000,-Ma140,-ldstr -fsimple=2
125.turb3d:  -fast -xarch=v8plusa -stackvar -xpad -fsimple=2 -Qoption iropt -04+bcopy ${INV} -xvector=yes
125.turb3d:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
141.apsi:    -fast -xarch=v8plusa -xchip=ultra3 -fsimple=2 -stackvar  -Qoption iropt -Si36
141.apsi: ${INV} -Qoption iropt -O4+algassoc; PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
145.fpppp:   -fast -xarch=v8plusa -xO5 -xsafe=mem -fsimple=2 -nodepend -Qoption iropt -O4+algassoc -Qoption iropt -reroll=1
145.fpppp:   PASS1= ${P1}; PASS2= ${P2};
146.wave5:   -fast -xarch=v8plusa  -stackvar -fsimple=2 -Qoption iropt -O4+unroll ${BCOPY}
Shell limits: # ulimit -n 1024  or % limit descriptors 1024
Config file: E450_DT.pub.int.fp_2.cfg
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