SPEC CINT95rate Results

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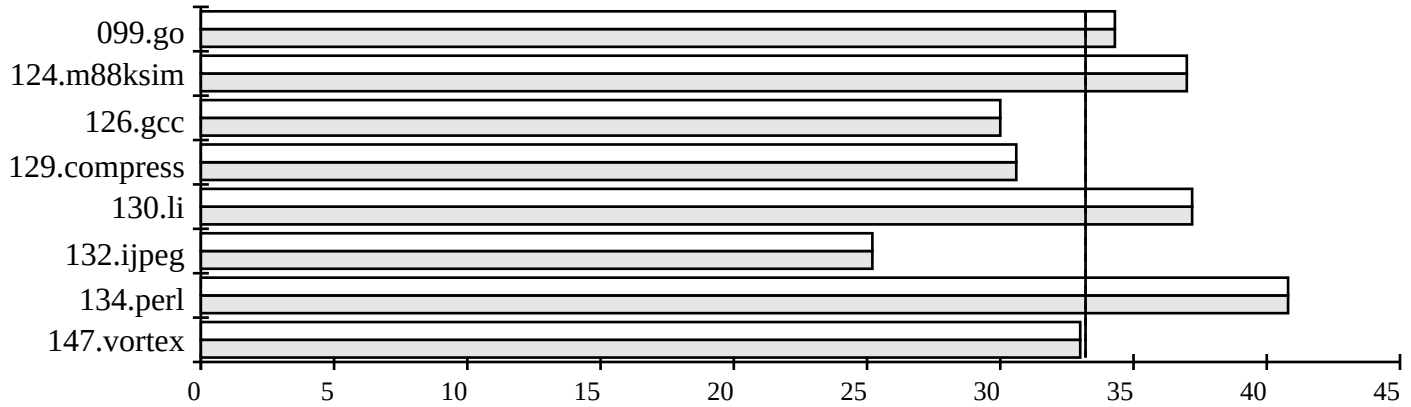
Siemens Nixdorf Informationssysteme PRIMERGY-550

SPECint_rate95 =	33.2
SPECint_rate_base95 =	33.2

SPEC license # 22	Tested By: SNI, Augsburg / Germany	Test Date: Feb-96	Hardware Avail: Mar-96	Software Avail: Feb-96



Hardware/Software Configuration for: PRIMERGY-550

Hardware

Model Name:	PRIMERGY-550
CPU:	133 MHz Pentium
FPU:	Integrated
Number of CPU(s):	1
Primary Cache:	8KBI+8KBD on chip
Secondary Cache:	512KB(I+D) off chip
Other Cache:	None
Memory:	128MB
Disk Subsystem:	2 x 2 GB
Other Hardware:	Mylex DAC960 Controller

Software

Operating System:	Unixware 2.1
Compiler:	Intel Reference Compiler 2.2 Beta
File System:	UFS,VXFS
System State:	Single User

Benchmark # and Name	Base Copies	Base Run Time	Base SPEC Ratio	Copies	Run Time	SPEC Ratio
099.go	1	1208	34.3	1	1208	34.3
124.m88ksim	1	462	37.0	1	462	37.0
126.gcc	1	510	30.0	1	510	30.0
129.compress	1	530	30.6	1	530	30.6
130.li	1	459	37.2	1	459	37.2
132.ijpeg	1	858	25.2	1	858	25.2
134.perl	1	419	40.8	1	419	40.8
147.vortex	1	737	33.0	1	737	33.0
SPECint_rate_base95 (G. Mean)			33.2			
SPECint_rate95 (G. Mean)						33.2

Notes/Tuning Information

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Compiled by Intel
Base and peak flags are the same and use Feedback Directed Optimization
Pass1: -tp p5 -ipo -prof_gen -ircdb_dir /tmp/IRCDB -dn
Pass2: -tp p5 -ipo -prof_use -ircdb_dir /tmp/IRCDB -dn
-ircdb_dir is a location flag and not an optimization flag
Portability: 124: -DSYSV -DLEHOST 130, 134, 147: -lm  132: -DSYSV  126: -lm -lc -L/usr/ucblib -lucb -lmalloc
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