SPEC CFP95 Results

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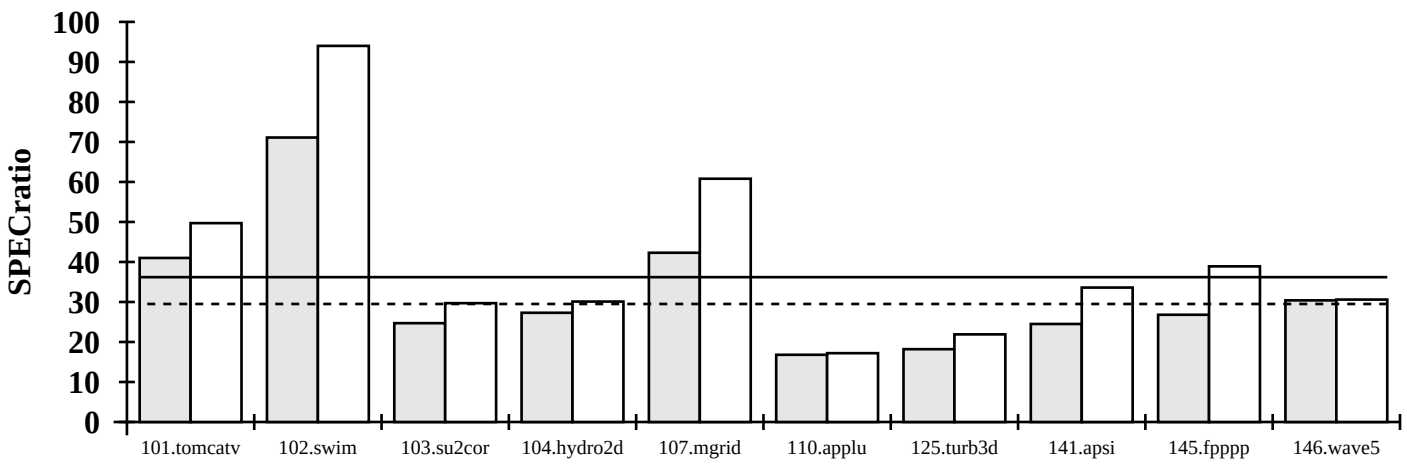
Sun Microsystems
Ultra 80 Model 2450

SPECfp95 = 36.2

SPECfp_base95 = 29.5

SPEC license # 6 | Tested By: Sun, Palo Alto | Test Date: Dec-1999 | Hardware Avail: Nov-1999 | Software Avail: May-1999



Hardware/Software Configuration for:
Ultra 80 Model 2450

Hardware

Model Name:	Ultra 80 Model 2450
CPU:	450 MHz UltraSPARC-II
FPU:	Integrated
Number of CPU(s):	2
Primary Cache:	16KBI+16KBD on chip
Secondary Cache:	4MB(I+D) off chip
Other Cache:	None
Memory:	512MB
Disk Subsystem:	2 x 18GB
Other Hardware:	Fast Ethernet System clock frequency: 113 MHz

Software

Operating System:	Solaris 7 SU2 (64-bit)
Compiler:	Sun Workshop FORTRAN 77 5.0
File System:	UFS
System State:	Single User
Kernel Extensions:	

Benchmark # and Name	Reference Time	Base Run Time	Base SPEC Ratio	Run Time	SPEC Ratio
101.tomcatv	3700	90.3	41.0	74	49.7
102.swim	8600	121	71.1	92	94.0
103.su2cor	1400	56.6	24.7	47	29.7
104.hydro2d	2400	87.9	27.3	80	30.1
107.mgrid	2500	59.1	42.3	41	60.8
110.applu	2200	131	16.8	128	17.2
125.turb3d	4100	225	18.2	187	21.9
141.apsi	2100	85.9	24.5	63	33.6
145.fpppp	9600	359	26.8	247	38.9
146.wave5	3000	98.8	30.4	98	30.6
SPECfp_base95 (G. Mean)			29.5		
		SPECfp95 (G. Mean)			36.2

Notes/Tuning Information

```
P1= -xprofile=collect:‘pwd‘/../feedback
P2= -xprofile=use:‘pwd‘/../feedback
BCOPY= -Qoption iropt -O4+bcopy; INV = -Qoption iropt -O4+invccexp; BMR = -Qoption iropt -O4+bmerge
Baseline: -fast -xarch=v8plusa -xparallel
Baseline: PASS1= ${P1}; PASS2= ${P2};
101.tomcatv: -fast -xarch=v8plusa -s -xpad=local:23 -xprefetch -xparallel -xreduction -Qoption iropt -O4+data_access
102.swim:    -fast -xarch=v8plusa -s -xpad=common:15 -xprefetch -xparallel
103.su2cor:  -fast -xarch=v8plusa -s -xvector -Qoption cg -Qms_pipe+nfll=5 -fsimple=2  ${BCOPY}  -xparallel -xreduction
103.su2cor:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2}; EXTRA_LIBS= -lmvec
104.hydro2d: -fast -xarch=v8plusa -s -xprefetch -Qoption iropt -O4+pde -fsimple=2  ${BMR} -xparallel
104.hydro2d:  PASS1= ${P1}; PASS2= ${P2};
107.mgrid: -fast -xarch=v8plusa -fsimple=2 -xprefetch -xparallel
110.applu: -fast -xarch=v8plusa -s -Qoption iropt -O4+scalarrep+ddint+perfect+loop_dist+algassoc,-Mt6000,-Mr20000,-Ma140,-ldstr -xparallel
125.turb3d:  -fast -xarch=v8plusa -s -stackvar -xpad -fsimple=2 ${INV}
125.turb3d:  PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
141.apsi:    -fast -xarch=v8plusa -s -xchip=ultra2  -fsimple=2 -xvector -stackvar  -Qoption iropt -Si36  ${BMR} ${INV}
141.apsi (cont):    -Qoption iropt -O4+algassoc
141.apsi:    PASS1= -xO4 ${P1}; PASS2= -xO5 -xsafe=mem ${P2};
145.fpppp:   -fast -xarch=v8plusa -s -xO5 -xsafe=mem -fsimple=2 -nodepend -Qoption iropt -O4+algassoc -Qoption iropt -reroll=1
145.fpppp:   PASS1= ${P1}; PASS2= ${P2};
146.wave5:   -fast -xarch=v8plusa  -s -stackvar -fsimple=2 -Qoption iropt -O4+unroll ${BCOPY}  -xparallel
Config file: ultra80fpmp.cfg
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