



# CINT2000 Result

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**Hewlett-Packard Company**  
ProLiant BL480c (1.86 GHz, Intel Xeon processor E5320)

SPECint\_rate2000 = **78.0**  
SPECint\_rate\_base2000 = **77.7**

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Feb-2007 Hardware Avail: Jan-2007 Software Avail: Nov-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	4	125	52.0	4	125	52.0
175.vpr	4	114	56.9	4	112	57.8
176.gcc	4	58.8	86.7	4	58.8	86.7
181.mcf	4	126	66.4	4	124	67.6
186.crafty	4	58.9	78.7	4	59.0	78.6
197.parser	4	129	64.6	4	129	64.7
252.eon	4	53.6	112	4	53.5	113
253.perlbnk	4	83.3	100	4	82.9	101
254.gap	4	78.1	65.3	4	77.7	65.7
255.vortex	4	67.1	131	4	67.1	131
256.bzip2	4	122	57.3	4	122	57.3
300.twolf	4	139	100	4	139	100

### Hardware

CPU: Intel Xeon processor E5320 (1.86 GHz, 2x4 MB L2 shared, 1066 MHz bus)  
CPU MHz: 1860  
FPU: Integrated  
CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip  
CPU(s) orderable: 1,2 chips  
Parallel: No  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 8 MB I+D on chip per chip (4 MB shared per 2 cores)  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 16 GB (8x2 GB PC2-5300F CL5)  
Disk Subsystem: 1x72 GB 10 K SAS  
Other Hardware:

### Software

Operating System: Windows Server 2003 Enterprise x64 Edition SP1  
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20061103Z)  
Microsoft Visual Studio .NET 2003 (v7.1.3088, for libraries)  
MicroQuill Smartheap Library 8.0  
File System: NTFS  
System State: Default

## Notes/Tuning Information

+FDO: PASS1=-Qprof\_gen PASS2=-Qprof\_use

Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib

Base tuning for C++ programs: -fast -Qcxx\_features +FDO ONESTEP=yes

Portability flags:

176.gcc: -Dalloca=\_alloca /F10000000

186.crafty: -DNT\_i386

253.perlbnk: -DSPEC\_CPU2000\_NTOS -DPERL DLL /MT

254.gap: -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_HAS\_MALLOC\_PROTO

Peak tuning:

164.gzip: -fast +FDO ONESTEP=yes

175.vpr: -fast +FDO ONESTEP=yes

176.gcc: basepeak=1

181.mcf: -fast +FDO

186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

197.parser: -fast +FDO ONESTEP=yes

252.eon: -fast +FDO

253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

254.gap: -fast +FDO ONESTEP=yes

255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib



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## Notes/Tuning Information (Continued)

```
256.bzip2:    basepeak=1
300.twolf:    -fast          +FDO          shlw32M.lib
BIOS Configuration Notes
Power Regulator set to Static High Performance Mode
Adjacent Sector Prefetch disabled
Other Configuration Notes
The start /b /wait /affinity command is used to bind CPU(s) to processes.
```