



# CINT2000 Result

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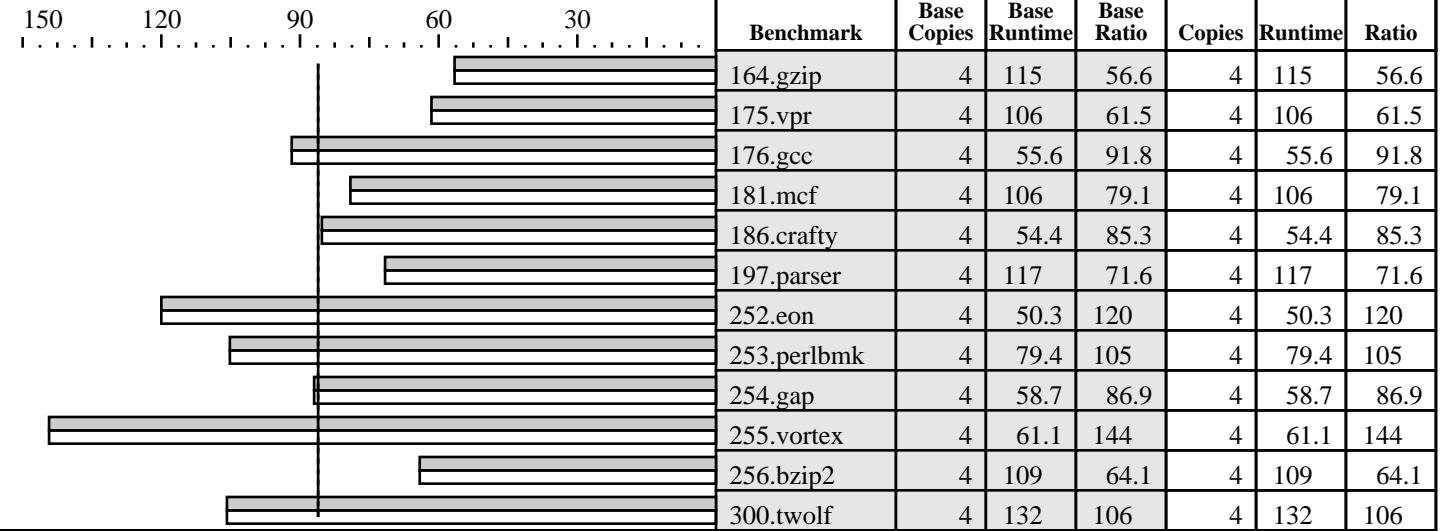
## Itautec

Servidor Itautec MX201 (Intel Xeon 5130 processor)

SPECint\_rate2000 = 86.1

SPECint\_rate\_base2000 = 86.1

SPEC license #: 9001 | Tested by: Itautec | Test date: Oct-2006 | Hardware Avail: Jul-2006 | Software Avail: May-2006



### Hardware

CPU: Intel Xeon 5130 processor (2.00GHz, 4MB L2 shared, 1333MHz system bus)  
CPU MHz: 2000  
FPU: Integrated  
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
CPU(s) orderable: 1,2  
Parallel: No  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 4096KB(I+D) on chip, per chip, shared  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 8x1GB 533MHz ECC CL4 FB-DIMM DDR2 SDRAM  
Disk Subsystem: 1 x 73GB SCSI 10000 RPM  
Other Hardware: None

### Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)  
Compiler: Intel C++ Compiler 9.1  
Build 20060519Z (for 32-bit applications),  
Microsoft Visual Studio .NET 7.0.9466 (for libraries),  
MicroQuill SmartHeap for SMP Library 8.0  
File System: NTFS  
System State: Default

## Notes/Tuning Information

### PORTABILITY FLAGS

176.gcc: -Dalloca=\_alloca /F10000000  
186.crafy: -DNT\_i386  
253.perlbnk: -DSPEC\_CPU2000\_NTOS -DPERLDLL /MT  
254.gap: -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_HAS\_MALLOC\_PROTO

### GENERAL

+FDO: PASS1=-Qprof\_gen PASS2=-Qprof\_use

### Base tuning flags

for C programs: -fast +FDO sh1SMPMt.lib  
for C++ programs: -fast -Qcxx\_features +FDO

### EXTRA LIBRARIES

MicroQuill SmartHeap for SMP Library 8.0 available from  
<http://www.microquill.com/>