



CFP2000 Result

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Acer Incorporated
Acer Altos R520 (3.2 GHz Intel Xeon Processor 5060)

SPECfp_rate2000 = **60.0**
SPECfp_rate_base2000 = **60.0**

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: May-2006 | Hardware Avail: Nov-2006 | Software Avail: Dec-2005

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	4	81.4	91.2	4	81.4	91.2
171.swim	4	293	49.1	4	293	49.1
172.mgrid	4	189	44.3	4	189	44.3
173.applu	4	209	46.5	4	209	46.5
177.mesa	4	84.6	76.7	4	84.6	76.7
178.galgel	4	106	127	4	106	127
179.art	4	72.2	167	4	72.2	167
183.equake	4	136	44.5	4	136	44.5
187.facerec	4	136	64.7	4	136	64.7
188.amp	4	189	53.9	4	189	53.9
189.lucas	4	200	46.3	4	200	46.3
191.fma3d	4	201	48.5	4	201	48.5
200.sixtrack	4	172	29.6	4	172	29.6
301.apsi	4	236	51.0	4	236	51.0

Hardware

CPU: Intel Xeon Processor 5060 (3.2 GHz, 2x2MB L2, 1066MHz system bus)
CPU MHz: 3200
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip(Hyper-Threading Technology disabled)
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 12K(I) micro-ops + 16KB(D) on chip, per core
Secondary Cache: 2048KB(I+D) on chip, per core
L3 Cache: N/A
Other Cache: N/A
Memory: 8 x 2048MB ECC FB-DIMM DDR2-667
Disk Subsystem: 1 x 73GB 10000RPM SAS HDD
Other Hardware:

Software

Operating System: Red Hat Enterprise Linux 4 Advanced Server Update 3 EM64T (Kernel 2.6.9-34.EL.smp)
Compiler: Intel C++ Compiler 9.0 Build 20051201 for EM64T
Intel Fortran Compiler 9.0 Build 20051201 for EM64T
File System: ext3
System State: Runlevel 3

Notes/Tuning Information

GENERAL

ONESTEP=yes for all benchmarks
+FDO : PASS1= -prof_gen PASS2= -prof_use

PORTABILITY FLAGS

-DSPEC_CPU2000_LP64 applied to all benchmarks
178.galgel: -FI for fixed-format Fortran

BASE TUNING

Baseline optimizations for C and Fortran: -fast +FDO

PEAK TUNING

basepeak=yes set for all benchmarks

Enable hardware prefetching and adjacent cache line prefetch BIOS setting

This result was measured on the Acer Alto R720.

The Altos R520 and Altos R720 are electronically equivalent.