



# CINT2000 Result

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**Hewlett-Packard Company**  
ProLiant BL480c (1.60 GHz, Intel Xeon processor 5110)

SPECint\_rate2000 = **71.2**  
SPECint\_rate\_base2000 = **70.9**

SPEC license #: 3 | Tested by: Hewlett-Packard Company | Test date: Aug-2006 | Hardware Avail: Jun-2006 | Software Avail: May-2006

| Benchmark   | Base Copies | Base Runtime | Base Ratio | Copies | Runtime | Ratio |
|-------------|-------------|--------------|------------|--------|---------|-------|
| 164.gzip    | 4           | 144          | 45.1       | 4      | 144     | 45.0  |
| 175.vpr     | 4           | 127          | 51.1       | 4      | 122     | 53.4  |
| 176.gcc     | 4           | 63.2         | 80.8       | 4      | 63.2    | 80.8  |
| 181.mcf     | 4           | 114          | 73.2       | 4      | 114     | 73.1  |
| 186.crafty  | 4           | 68.4         | 67.8       | 4      | 68.5    | 67.8  |
| 197.parser  | 4           | 145          | 57.6       | 4      | 145     | 57.7  |
| 252.eon     | 4           | 62.3         | 96.9       | 4      | 62.4    | 96.7  |
| 253.perlbnk | 4           | 95.9         | 87.1       | 4      | 95.5    | 87.4  |
| 254.gap     | 4           | 72.5         | 70.4       | 4      | 72.0    | 70.9  |
| 255.vortex  | 4           | 74.5         | 118        | 4      | 74.6    | 118   |
| 256.bzip2   | 4           | 136          | 51.0       | 4      | 136     | 51.0  |
| 300.twolf   | 4           | 164          | 85.0       | 4      | 164     | 84.9  |

### Hardware

CPU: Intel Xeon processor 5110 (1.60 GHz, 4 MB L2 shared, 1066 MHz bus)  
CPU MHz: 1600  
FPU: Integrated  
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
CPU(s) orderable: 1,2 chips  
Parallel: No  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 4 MB I+D on chip per chip  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 16 GB (8x2048 MB PC2-5300F)  
Disk Subsystem: 2x36 GB 10 K SAS  
Other Hardware:

### Software

Operating System: Windows Server 2003 Enterprise SP1  
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)  
Microsoft Visual Studio .NET 7.0.9466 (for libraries)  
MicroQuill Smartheap Library 7.0  
File System: NTFS  
System State: Default

## Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO ONESTEP=yes
Portability flags:
 176.gcc: -Dalloca=_alloca /F10000000
 186.crafty: -DNT_i386
 253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
 254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: -fast +FDO ONESTEP=yes
175.vpr: -fast +FDO ONESTEP=yes
176.gcc: basepeak=1
181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib
186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
197.parser: -fast +FDO ONESTEP=yes
252.eon: -fast +FDO
253.perlbnk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib
254.gap: -fast +FDO ONESTEP=yes
255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib
256.bzip2: basepeak=1
```



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## Notes/Tuning Information (Continued)

300.twolf: -fast +FDO shlw32M.lib  
BIOS Configuration Notes  
Power Regulator set to Static High Performance Mode  
Adjacent Sector Prefetch Disabled