



# CFP2000 Result

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## Fujitsu Siemens Computers

PRIMERGY TX600 S3, Intel Xeon processor 7041, 3.0 GHz

SPECfp\_rate2000 = 63.3

SPECfp\_rate\_base2000 = 63.3

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Jun-2006 | Hardware Avail: May-2006 | Software Avail: Jan-2006

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	8	197	75.3	8	197	75.3
171.swim	8	802	35.9	8	802	35.9
172.mgrid	8	494	33.8	8	494	33.8
173.applu	8	546	35.7	8	546	35.7
177.mesa	8	97.2	134	8	97.2	134
178.galgel	8	165	163	8	165	163
179.art	8	176	137	8	176	137
183.quake	8	359	33.6	8	359	33.6
187.facerec	8	235	74.9	8	235	74.9
188.amp	8	240	85.0	8	240	85.0
189.lucas	8	450	41.3	8	450	41.3
191.fma3d	8	446	43.7	8	446	43.7
200.sixtrack	8	191	53.4	8	191	53.4
301.apsi	8	294	82.1	8	294	82.1

### Hardware

CPU: Intel Xeon processor 7041 (3.0 GHz, 2x2MB L2, 800 MHz system bus)  
 CPU MHz: 3000  
 FPU: Integrated  
 CPU(s) enabled: 8 cores, 4 chips, 2 cores/chip (Hyper-Threading Technology disabled)  
 CPU(s) orderable: 1,2,4  
 Parallel: No  
 Primary Cache: 12k micro-ops I + 16KBD on chip, per core  
 Secondary Cache: 2048KB(I+D) on chip, per core  
 L3 Cache: N/A  
 Other Cache: N/A  
 Memory: 16x1GB DDRII-RAM PC2-3200R (CAS 3-3-3)  
 Disk Subsystem: Fujitsu MAS3367NC (SCSI, 15krpm, 36GB)  
 Other Hardware: none

### Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 9 with SP3  
 Kernel 2.6.5-7.244-smp on an x86\_64  
 Compiler: Intel C++ and Fortran Compiler 9.0 for EM64T  
 Build 20060120 (for 64-bit applications)  
 File System: ext3  
 System State: Multi-user run level 3

## Notes/Tuning Information

### GENERAL

+FDO implies feedback-directed optimization  
 PASS1: -prof\_gen PASS2: -prof\_use

### Optimization flags

ONESTEP=yes set for all benchmarks

### Portability flags

-DSPEC\_CPU2000\_LP64 applied to all benchmarks  
 178.galgel: -FI for fixed-format Fortran

### Base tuning flags

for Fortran and C programs: -fast +FDO

### Peak tuning flags

same as baseline (basepeak=true set globally)



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## Notes/Tuning Information (Continued)

The system bus runs at 800 MHz

This result was measured with 64-bit binaries using the 64-bit version of the operating system.

BIOS Configuration:

Hardware Prefetch = Enable, Adjacent Sector Prefetch = Disable

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