



# CINT2000 Result

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**Hewlett-Packard Company**  
ProLiant ML350 G5 (3.0GHz, Intel Xeon processor 5160)

SPECint2000 = **3025**  
SPECint\_base2000 = **3015**

SPEC license #: 3 Tested by: Hewlett-Packard Company Test date: Jun-2006 Hardware Avail: Jun-2006 Software Avail: May-2006

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	
164.gzip	1400	76.0	1843	76.0	1842	
175.vpr	1400	65.1	2152	63.0	2221	
176.gcc	1100	36.2	3038	36.2	3038	
181.mcf	1800	41.1	4384	41.1	4384	
186.crafty	1000	36.3	2753	36.3	2751	
197.parser	1800	73.3	2457	73.0	2467	
252.eon	1300	33.1	3925	33.2	3915	
253.perlbmk	1800	50.0	3599	49.7	3620	
254.gap	1100	38.2	2879	38.1	2888	
255.vortex	1900	38.1	4982	38.2	4979	
256.bzip2	1500	66.2	2266	66.2	2266	
300.twolf	3000	87.2	3440	87.2	3440	

### Hardware

CPU: Intel Xeon processor 5160 (3.0GHz, 4MB L2 shared, 1333MHz bus)  
CPU MHz: 3000  
FPU: Integrated  
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
CPU(s) orderable: 1,2 chips  
Parallel: No  
Primary Cache: 32KBI + 32KBD (on chip) per core  
Secondary Cache: 4096KB(I+D) (on chip) shared  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 4x2048MB PC2-5300F  
Disk Subsystem: 1x146GB 15K SAS  
Other Hardware:

### Software

Operating System: Windows Server 2003 Enterprise SP1  
Compiler: Intel C++ Compiler for 32-bit applications, (Version 9.1 Build 20060323Z)  
Microsoft Visual Studio .NET 7.0.9466 (for libraries)  
MicroQuill Smartheap Library 7.0  
File System: NTFS  
System State: Default

## Notes/Tuning Information

+FDO: PASS1=-Qprof\_gen PASS2=-Qprof\_use

Base tuning for C programs: -fast +FDO ONESTEP=yes shlw32M.lib

Base tuning for C++ programs: -fast -Qcxx\_features +FDO ONESTEP=yes

Portability flags:

176.gcc: -Dalloca=\_alloca /F10000000

186.crafty: -DNT\_i386

253.perlbmk: -DSPEC\_CPU2000\_NTOS -DPERLDLL /MT

254.gap: -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_HAS\_MALLOC\_PROTO

Peak tuning:

164.gzip: -fast +FDO ONESTEP=yes

175.vpr: -fast +FDO ONESTEP=yes

176.gcc: basepeak=1

181.mcf: -fast +FDO ONESTEP=yes shlw32M.lib

186.crafty: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

197.parser: -fast +FDO ONESTEP=yes

252.eon: -fast +FDO

253.perlbmk: -fast -Oa +FDO ONESTEP=yes shlw32M.lib

254.gap: -fast +FDO ONESTEP=yes

255.vortex: -fast +FDO ONESTEP=yes shlw32M.lib



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## Notes/Tuning Information (Continued)

256.bzip2: basepeak=1  
300.twolf: -fast +FDO shlw32M.lib  
BIOS Configuration Notes  
Power Regulator set to Static High Performance Mode