



# CINT2000 Result

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## Acer Incorporated

Acer Altos R510 Mk2 (3.8 GHz Intel Xeon processor, 2MB L2, 800MHz system bus)

## SPECint\_rate2000 = 40.5

## SPECint\_rate\_base2000 = 40.4

SPEC license #: 97 | Tested by: Acer Incorporated | Test date: Dec-2005 | Hardware Avail: Jan-2006 | Software Avail: Apr-2005

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
164.gzip	2	113	28.7	2	113	28.7
175.vpr	2	118	27.5	2	116	28.0
176.gcc	2	51.9	49.2	2	51.9	49.2
181.mcf	2	113	37.0	2	113	37.0
186.crafty	2	71.1	32.6	2	71.1	32.6
197.parser	2	117	35.6	2	117	35.7
252.eon	2	54.7	55.2	2	54.8	55.0
253.perlbnk	2	79.4	52.6	2	79.5	52.6
254.gap	2	62.2	41.0	2	62.2	41.0
255.vortex	2	63.7	69.2	2	63.7	69.2
256.bzip2	2	113	30.8	2	113	30.7
300.twolf	2	155	45.0	2	155	45.0

### Hardware

CPU: Intel Xeon (3.8 GHz, 2MB L2, 800MHz system bus)  
 CPU MHz: 3800  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 2 chips, 1 core/chip(Hyper-Threading Technology Disabled)  
 CPU(s) orderable: 1,2  
 Parallel: No  
 Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip  
 Secondary Cache: 2048KB(I+D) on chip  
 L3 Cache: N/A  
 Other Cache: N/A  
 Memory: 6 x 512MB ECC DDR2-400 SDRAM  
 Disk Subsystem: 1 x 160GB 7200RPM SATA HDD  
 Other Hardware:

### Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)  
 Compiler: Intel C++ Compiler 9.0 Build 20050430Z for IA32  
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)  
 shlw32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/  
 File System: NTFS  
 System State: Default

## Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlw32M.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
252.eon: Approved stdcpp src.alt used
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: basepeak=yes
175.vpr: -fast -Qansi_alias +FDO
176.gcc: basepeak=yes
181.mcf: basepeak=yes
186.crafty: basepeak=yes
197.parser: -fast -Qansi_alias +FDO
252.eon: -fast +FDO
253.perlbnk: -fast -Qansi_alias +FDO shlw32M.lib
254.gap: basepeak=yes
255.vortex: basepeak=yes
```



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## Notes/Tuning Information (Continued)

256.bzip2: -fast -Oa -Qunroll1 +FDO

300.twolf: -fast +FDO shlw32M.lib

Enable hardware prefetching and adjacent cache line prefetch BIOS setting

Product description located at <http://www.acer.com>