



CINT2000 Result

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Advanced Micro Devices
TYAN Tomcat K8E (S2865), AMD Opteron (TM) 154

SPECint2000 = 1940
SPECint_base2000 = 1708

SPEC license #: 49 Tested by: AMD, Austin, Texas Test date: Dec-2005 Hardware Avail: Aug-2005 Software Avail: Apr-2004

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	1000 2000 3000 4000			
164.gzip	1400	109	1284	90.7	1543	[Bar chart showing ratio 1543]			
175.vpr	1400	113	1244	90.2	1552	[Bar chart showing ratio 1552]			
176.gcc	1100	76.9	1431	53.6	2052	[Bar chart showing ratio 2052]			
181.mcf	1800	155	1163	119	1516	[Bar chart showing ratio 1516]			
186.crafty	1000	52.7	1899	50.0	1998	[Bar chart showing ratio 1998]			
197.parser	1800	103	1741	104	1733	[Bar chart showing ratio 1733]			
252.eon	1300	50.1	2593	44.9	2895	[Bar chart showing ratio 2895]			
253.perlbnk	1800	87.0	2068	85.2	2112	[Bar chart showing ratio 2112]			
254.gap	1100	60.0	1833	60.0	1833	[Bar chart showing ratio 1833]			
255.vortex	1900	69.0	2753	65.9	2881	[Bar chart showing ratio 2881]			
256.bzip2	1500	102	1476	99.2	1513	[Bar chart showing ratio 1513]			
300.twolf	3000	171	1754	133	2254	[Bar chart showing ratio 2254]			

Hardware

CPU: AMD Opteron (TM) 154 (939-pin)
 CPU MHz: 2800
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip
 CPU(s) orderable: 1
 Parallel: no
 Primary Cache: 64KBI + 64KBD on chip
 Secondary Cache: 1024KB (I+D) on chip
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 2x512MB, DDR400 CL2
 Disk Subsystem: IDE, 40 GB
 Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise edition (32 bit)
 Compiler: Intel C++ 9.0 build 20050912Z for IA32
 Microsoft Visual Studio .NET 7.0.9466 (libraries)
 MicroQuill Smartheap Library 7.0
 File System: NTFS
 System State: default

Notes/Tuning Information

shlw32M.lib is the SmartHeap library V7.0 from MicroQuill (www.microquill.com)
 +FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Portability:

176.gcc: -Dalloca=_alloca /F10000000
 186.crafty: -DNT_i386
 253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
 254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Baseline C: Baseline C++:

-O3 -Qipo -Op +FDO
 -O3 -Qipo -Qcxx-features +FDO
 Peak Tuning:
 164.gzip: -O3 -Qipo -QxW +FDO
 175.vpr: -O3 -Qipo -QxW +FDO
 -Qoption,c,-ip_ninl_max_stats=2000,-ip_ninl_max_total_stats=4500
 176.gcc: -O3 -Qipo -QxW -Oi- -Qunroll3 +FDO
 181.mcf: -O3 -Qipo -QaxN +FDO
 186.crafty: -O3 -Qipo -QxW +FDO
 197.parser: -QxW +FDO -Oi- -Qipo
 252.eon: -O3 -Qipo -QxW +FDO -Qansi_alias
 -Qoption,c,-ip_ninl_max_stats=2000,-ip_ninl_max_total_stats=4500
 253.perlbnk: -O3 -Qipo -QxW +FDO



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Notes/Tuning Information (Continued)

```
254.gap:      basepeak = true
255.vortex:   -O3 -Qipo -arch:SSE +FDO -Oi- shlw32M.lib
              -Qoption,c,-ip_ninl_max_stats=2000,-ip_ninl_max_total_stats=4500
256.bzip2:   -O3 -Qipo -Qunroll2
300.twolf:   -O3 -Qipo -QxW +FDO -Qunroll3 shlw32M.lib -Qansi_alias
Corsair CMX512-3200XL memory used in Dual Channel configuration
Memory timings manually set in BIOS: CAS=2, Trcd=2, Tras=5, Trp=2
BIOS rev 3.00
The tested system can be assembled using a standard ATX case and an Antec True 550
watt EPS12V Power Supply
```