



CINT2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY RX200 S2, Intel Xeon processor 3.80GHz, 2MB L2

SPECint2000 = 1801

SPECint_base2000 = 1801

SPEC license #: 22 Tested by: Fujitsu Siemens Computers Test date: Nov-2005 Hardware Avail: Oct-2005 Software Avail: Sep-2005

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	1000 2000 3000 4000			
164.gzip	1400	111	1260	111	1260	[Bar chart showing ratio 1260]			
175.vpr	1400	113	1236	113	1236	[Bar chart showing ratio 1236]			
176.gcc	1100	50.7	2172	50.7	2172	[Bar chart showing ratio 2172]			
181.mcf	1800	92.7	1941	92.7	1941	[Bar chart showing ratio 1941]			
186.crafty	1000	71.0	1408	71.0	1408	[Bar chart showing ratio 1408]			
197.parser	1800	116	1553	116	1553	[Bar chart showing ratio 1553]			
252.eon	1300	57.8	2248	57.8	2248	[Bar chart showing ratio 2248]			
253.perlbnk	1800	80.2	2244	80.2	2244	[Bar chart showing ratio 2244]			
254.gap	1100	54.6	2014	54.6	2014	[Bar chart showing ratio 2014]			
255.vortex	1900	62.9	3021	62.9	3021	[Bar chart showing ratio 3021]			
256.bzip2	1500	110	1366	110	1366	[Bar chart showing ratio 1366]			
300.twolf	3000	155	1933	155	1933	[Bar chart showing ratio 1933]			

Hardware

CPU: Intel Xeon processor 3.80GHz, 2MB L2, 800MHz system bus
CPU MHz: 3800
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip (Hyper-Threading Technology disabled)
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 12k micro-ops I + 16KBD on chip, per core
Secondary Cache: 2048KB(I+D) on chip, per core
L3 Cache: N/A
Other Cache: N/A
Memory: 8x512 MB DDRII-RAM PC2-3200R
Disk Subsystem: Fujitsu MAS3367NC (U320, 15krpm)
Other Hardware: none

Software

Operating System: Windows Server 2003 Enterprise Edition + SP 1
Compiler: Intel C++ Compiler 9.0
Build 20050722Z (for 32-bit applications),
Microsoft Visual Studio .Net 2003
7.1.3088 (for libraries),
SmartHeap Library Version 7.4
from <http://www.microquill.com>
File System: NTFS
System State: Default

Notes/Tuning Information

GENERAL

+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use

Portability flags

176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
252.eon: Approved srcalt stdcpp is used
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

Base tuning flags

for C programs: -fast +FDO shlw32m.lib
for C++ programs: -fast -Qcxx_features +FDO shlw32m.lib

Peak tuning flags

same as baseline (basepeak=true set globally)

New 32-bit Windows tools used, approved in May-2005

The system bus runs at 800 MHz



CINT2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

PRIMERGY RX200 S2, Intel Xeon processor 3.80GHz, 2MB L2

SPECint2000 = 1801

SPECint_base2000 = 1801

SPEC license #: 22 | Tested by: Fujitsu Siemens Computers | Test date: Nov-2005 | Hardware Avail: Oct-2005 | Software Avail: Sep-2005

Notes/Tuning Information (Continued)

This result was measured with 32-bit binaries using the 32-bit version of the operating system.

For information about Fujitsu Siemens Computers in your country please see:
<http://www.fujitsu-siemens.com/countries>