



CINT2000 Result

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Acer Incorporated

Acer Altos R710 (3.8 GHz Intel Xeon processor, 2MB L2, 800MHz system bus)

SPECint2000 = 1791

SPECint_base2000 = 1790

SPEC license #: 97 Tested by: Acer Incorporated Test date: Oct-2005 Hardware Avail: Oct-2005 Software Avail: May-2005

Benchmark	Reference Time	Base Runtime	Base Ratio	Runtime	Ratio	1000 2000 3000 4000			
164.gzip	1400	112	1252	112	1252	[Bar chart showing ratio]			
175.vpr	1400	116	1211	115	1220	[Bar chart showing ratio]			
176.gcc	1100	51.4	2142	51.4	2142	[Bar chart showing ratio]			
181.mcf	1800	95.3	1888	95.3	1888	[Bar chart showing ratio]			
186.crafty	1000	71.6	1397	71.6	1397	[Bar chart showing ratio]			
197.parser	1800	117	1543	117	1540	[Bar chart showing ratio]			
252.eon	1300	54.7	2377	54.8	2370	[Bar chart showing ratio]			
253.perlbnk	1800	80.9	2225	80.7	2230	[Bar chart showing ratio]			
254.gap	1100	54.8	2009	54.8	2009	[Bar chart showing ratio]			
255.vortex	1900	64.0	2970	64.0	2970	[Bar chart showing ratio]			
256.bzip2	1500	111	1351	111	1346	[Bar chart showing ratio]			
300.twolf	3000	156	1927	156	1928	[Bar chart showing ratio]			

Hardware

CPU: Intel Xeon (3.8 GHz, 2MB L2, 800MHz system bus)
CPU MHz: 3800
FPU: Integrated
CPU(s) enabled: 1 core, 1 chip, 1 core/chip(Hyper-Threading Technology Disable)
CPU(s) orderable: 1,2
Parallel: No
Primary Cache: 12KB(I) micro-ops + 16KB(D) on chip
Secondary Cache: 2048KB(I+D) on chip
L3 Cache: N/A
Other Cache: N/A
Memory: 6 x 1024MB ECC DDR2-400 SDRAM
Disk Subsystem: 1 x 146GB 10000RPM U320 SCSI HDD
Other Hardware:

Software

Operating System: Microsoft Windows Server 2003 Enterprise Edition (Build 3790)
Compiler: Intel C++ Compiler 9.0 Build 20050430Z for IA32
Microsoft Visual Studio .Net 2003(7.1.3091, for libraries)
shlw32M.lib is SmartHeap Library Version 7.4 from http://www.microquill.com/
File System: NTFS
System State: Default

Notes/Tuning Information

```
+FDO: PASS1=-Qprof_gen PASS2=-Qprof_use
Base tuning for C programs: -fast +FDO shlw32M.lib
Base tuning for C++ programs: -fast -Qcxx_features +FDO shlw32M.lib
Portability flags:
176.gcc: -Dalloca=_alloca /F10000000
186.crafty: -DNT_i386
252.eon: Approved stdcpp src.alt used
253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO
Peak tuning:
164.gzip: basepeak=yes
175.vpr: -fast -Qansi_alias +FDO
176.gcc: basepeak=yes
181.mcf: basepeak=yes
186.crafty: basepeak=yes
197.parser: -fast -Qansi_alias +FDO
252.eon: -fast +FDO
253.perlbnk: -fast -Qansi_alias +FDO shlw32M.lib
254.gap: basepeak=yes
255.vortex: basepeak=yes
```



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Notes/Tuning Information (Continued)

256.bzip2: -fast -Oa -Qunroll1 +FDO
300.twolf: -fast -O3 +FDO shlw32M.lib
Enable hardware prefetching and adjacent cache line prefetch BIOS setting
Product description located at <http://www.acer.com>