



# CINT2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

**IBM**  
IBM eServer xSeries 346 (3.6 GHz Intel Xeon)

SPECint2000 = 1704  
SPECint\_base2000 = 1685

SPEC license #: 11 | Tested by: IBM, Research Triangle Park, NC | Test date: May-2005 | Hardware Avail: Feb-2005 | Software Avail: Apr-2005

| Benchmark   | Reference Time | Base Runtime | Base Ratio | Runtime | Ratio |  |
|-------------|----------------|--------------|------------|---------|-------|--|
| 164.gzip    | 1400           | 117          | 1200       | 117     | 1193  |  |
| 175.vpr     | 1400           | 116          | 1206       | 116     | 1206  |  |
| 176.gcc     | 1100           | 54.4         | 2023       | 54.4    | 2023  |  |
| 181.mcf     | 1800           | 96.0         | 1875       | 96.0    | 1875  |  |
| 186.crafty  | 1000           | 74.6         | 1340       | 74.4    | 1344  |  |
| 197.parser  | 1800           | 123          | 1469       | 123     | 1469  |  |
| 252.eon     | 1300           | 59.1         | 2200       | 58.5    | 2223  |  |
| 253.perlbmk | 1800           | 96.6         | 1864       | 86.2    | 2088  |  |
| 254.gap     | 1100           | 57.4         | 1915       | 57.4    | 1915  |  |
| 255.vortex  | 1900           | 69.6         | 2728       | 69.6    | 2728  |  |
| 256.bzip2   | 1500           | 119          | 1265       | 117     | 1278  |  |
| 300.twolf   | 3000           | 167          | 1793       | 168     | 1791  |  |

### Hardware

CPU: Intel Xeon (3.6 GHz Xeon, 2MB L2 Cache, 800 MHz system bus)  
CPU MHz: 3600  
FPU: Integrated  
CPU(s) enabled: 1 core, 1 chip, 1 core/chip (Hyper-Threading Technology disabled)  
CPU(s) orderable: 1,2  
Parallel: No  
Primary Cache: 12K(I) micro-ops + 16KB(D) on chip  
Secondary Cache: 2048KB(I+D) on chip  
L3 Cache: N/A  
Other Cache: N/A  
Memory: 8 x 512 MB ECC PC2-3200 Single Rank  
Disk Subsystem: 73GB SCSI 10K RPM  
Other Hardware:

### Software

Operating System: Windows Server 2003 Standard Edition  
Compiler: Intel C/C++ Compiler 8.1 (20050409)  
Intel Fortran Compiler 8.1 (20050405Z)  
Microsoft Visual Studio .NET 13.0.9466 (for libraries)  
MicroQuill Smartheap Library 7.30  
File System: NTFS  
System State: Default

## Notes/Tuning Information

### GENERAL

ONESTEP=yes  
+FDO: PASS1=-Qprof\_gen PASS2=-Qprof\_use

### PORTABILITY FLAGS

176.gcc: -Dalloca=\_alloca /F10000000  
186.crafty: -DNT\_i386  
253.perlbmk: -DSPEC\_CPU2000\_NTOS -DPERLDLL /MT  
254.gap: -DSYS\_HAS\_CALLOC\_PROTO -DSYS\_HAS\_MALLOC\_PROTO

### BASE TUNING

C: -fast -QxP -Qansi\_alias +FDO  
C++: -QxP -Qipo -Qcxx\_features +FDO

### PEAK TUNING

164.gzip: -fast -QxP -Qansi\_alias -Oa +FDO  
175.vpr: basepeak=yes  
176.gcc: basepeak=yes  
181.mcf: basepeak=yes  
186.crafty: -fast -QxP -Oa +FDO shlW32M.lib  
197.parser: basepeak=yes  
252.eon: -fast -QxP +FDO  
253.perlbmk: -fast -QxP -Qansi\_alias +FDO shlW32M.lib



# CINT2000 Result

Copyright ©1999-2005, Standard Performance Evaluation Corporation

IBM

IBM eServer xSeries 346 (3.6 GHz Intel Xeon)

SPECint2000 = 1704

SPECint\_base2000 = 1685

SPEC license #: 11 | Tested by: IBM, Research Triangle Park, NC | Test date: May-2005 | Hardware Avail: Feb-2005 | Software Avail: Apr-2005

## Notes/Tuning Information (Continued)

```
254.gap:      basepeak=yes
255.vortex    -fast -QxP +FDO
256.bzip2:    -fast -QxP -Qunroll1 -Oa +FDO
300.twolf:    -fast -QxP +FDO shlw32M.lib
EXTRA LIBRARIES
shlw32M.lib:  MicroQuill SmartHeap Library 7.3
              www.microquill.com
```