



# CFP2000 Result

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**SGI**  
SGI Altix 3000 (1000MHz, Itanium 2)

SPECfp\_rate2000 = 116  
SPECfp\_rate\_base2000 = 115

SPEC license #: 4 | Tested by: SGI | Test date: Apr-2003 | Hardware Avail: Feb-2003 | Software Avail: Apr-2003

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	8	165	89.7	8	165	89.7
171.swim	8	144	200	8	144	200
172.mgrid	8	132	126	8	130	128
173.applu	8	94.8	206	8	94.8	206
177.mesa	8	187	69.4	8	187	69.4
178.galgel	8	131	206	8	131	206
179.art	8	81.9	295	8	81.9	295
183.quake	8	79.2	152	8	79.2	152
187.facerec	8	169	104	8	169	104
188.amp	8	261	78.3	8	261	78.3
189.lucas	8	210	88.3	8	206	90.2
191.fma3d	8	306	63.6	8	306	63.6
200.sixtrack	8	120	84.9	8	120	84.9
301.apsi	8	404	59.8	8	368	65.5

### Hardware

CPU: Intel Itanium 2  
CPU MHz: 1000  
FPU: Integrated  
CPU(s) enabled: 8 cores, 8 chips, 1 core/chip  
CPU(s) orderable: 4-64  
Parallel: No  
Primary Cache: 16KBI + 16KBD (on chip) per CPU  
Secondary Cache: 256KB (on chip) per CPU  
L3 Cache: 3.0MB (on chip) per CPU  
Other Cache: N/A  
Memory: 16 GB (16\*512MB DIMMS per 4cpu module)  
Disk Subsystem: 1 x 36 GB SCSI (Seagate Cheetah 15k rpm)  
Other Hardware: None

### Software

Operating System: SGI ProPack(TM) v2.1  
Compiler: Intel(R) Fortran Compiler for Linux 7.1 (Build 20030327)  
Intel(R) C++ Compiler for Linux 7.1 (Build 20030327)  
File System: xfs  
System State: Single-user

## Notes/Tuning Information

+FDO: PASS1=-prof\_gen PASS2=-prof\_use

### Baseline optimization flags:

C programs: -ipo -O3 -ansi\_alias +FDO  
Fortran programs: -ipo -O3 +FDO

### Portability Flags:

178.galgel: -FI

### Peak optimization flags:

168.wupwise: basepeak=yes  
171.swim: basepeak=yes  
172.mgrid: -O3 -ipo  
173.applu: basepeak=yes  
177.mesa: basepeak=yes  
178.galgel: basepeak=yes  
179.art: basepeak=yes



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## Notes/Tuning Information (Continued)

183.equake: basepeak=yes  
187.facerec: basepeak=yes  
188.ammmp: basepeak=yes  
189.lucas: -O3 -ipo  
191.fma3d: basepeak=yes  
200.sixtrack: basepeak=yes  
301.apsi: -O3 -ipo

Processes were bound to CPUs using dplace.