



CFP2000 Result

Copyright ©1999-2004, Standard Performance Evaluation Corporation

Sun Microsystems
Sun Fire V240 (1002MHz)

SPECfp_rate2000 = 14.9

SPECfp_rate_base2000 = 12.8

SPEC license #: 6 Tested by: Sun Microsystems Test date: Mar-2003 Hardware Avail: May-2003 Software Avail: May-2003

Benchmark	Base Copies	Base Runtime	Base Ratio	Copies	Runtime	Ratio
168.wupwise	2	246	15.1	2	226	16.4
171.swim	2	708	10.2	2	286	25.1
172.mgrid	2	495	8.44	2	489	8.54
173.applu	2	400	12.2	2	318	15.3
177.mesa	2	261	12.5	2	240	13.5
178.galgel	2	325	20.7	2	283	23.8
179.art	2	56.8	106	2	53.2	113
183.quake	2	170	17.7	2	164	18.3
187.facerec	2	254	17.3	2	239	18.4
188.amp	2	681	7.49	2	687	7.43
189.lucas	2	798	5.81	2	524	8.85
191.fma3d	2	567	8.60	2	526	9.26
200.sixtrack	2	488	5.23	2	472	5.40
301.apsi	2	492	12.3	2	495	12.2

Hardware

CPU: UltraSPARC IIIi
CPU MHz: 1002
FPU: Integrated
CPU(s) enabled: 2 cores, 2 chips, 1 core/chip
CPU(s) orderable: 1-2
Parallel: No
Primary Cache: 32KBI+64KBD on chip
Secondary Cache: 1MB(I+D) on chip
L3 Cache: None
Other Cache: None
Memory: 8GB 16-way interleaved
Disk Subsystem: 2 x 36GB SEAGATE ST336605L
Other Hardware: None

Software

Operating System: Solaris 8 HW 12/02
Compiler: Sun ONE Studio 8 (pre-FCS build 3/9)
Sun Performance Library 8 (pre-FCS build 3/9)
File System: ufs with ufs logging
System State: Multi-User

Notes/Tuning Information

SPEC has determined that this result was not in compliance with the SPEC continued availability policy. Specifically, although the configuration tested was available within the required 3 months of publication, it was then unavailable for a period of 30 days or more during the first 90 days after initial overall availability. The vendor informs SPEC that the configuration is again available, on or before the date of this notice, 08/13/2003.

Compiler invocation:
C: cc
CXX: CC



CFP2000 Result

Copyright ©1999-2004, Standard Performance Evaluation Corporation

Sun Microsystems
Sun Fire V240 (1002MHz)

SPECfp_rate2000 = 14.9
SPECfp_rate_base2000 = 12.8

SPEC license #: 6 | Tested by: Sun Microsystems | Test date: Mar-2003 | Hardware Avail: May-2003 | Software Avail: May-2003

Notes/Tuning Information (Continued)

F90: f90
F77: f90

Floating point base flags:

C: -fast -xipo=2 -xalias_level=std with ONESTEP=yes and feedback
F90: -fast -xipo=2 with ONESTEP=yes and feedback

Floating point peak flags:

ONESTEP=yes and feedback for all benchmarks, unless otherwise noted

168.wupwise: -fast -xipo=2 -Qoption iropt -Ainline:inc=800:cp=1
171.swim: -fast -xpad=common:384 -xprefetch=latx:1.6
-Qoption iropt -Atile:skewp:b6,-Ainline:cs=700
(no feedback)
172.mgrid: -fast -xipo=2
173.applu: -fast -xipo=2
-Qoption cg -Qlp=1-av=192-fa=1,-Qms_pipe+prefolim=7
-Qoption iropt -Aujam:inner=g
177.mesa: -fast -xipo=2 -xalias_level=strong -xrestrict
-Wc,-Qms_pipe+unoovf
178.galgel: -fast -xipo=2 -Qoption iropt -Addint:sf=9 -xlic_lib=sunperf
RM_SOURCES=lapak.f90
179.art: -fast -xipo=2 -xalias_level=std -xprefetch=latx:1.5
183.quake: -fast -xipo=2 -xalias_level=strong -xprefetch_level=2
187.facerec: -fast -xipo=2 -xprefetch=latx:1.5
188.ammp: -fast -xipo=2 -xalias_level=std -lmopt -lm
189.lucas: -fast -xipo=2 -xprefetch_level=3 -Qoption iropt -Apf:pdl=1
-Qoption f90comp -array_pad_rows,1977
191.fma3d: -fast -xipo=2 -stackvar -xprefetch_level=3
-Qoption iropt -Apf:pdl=1
200.sixtrack: -O -dalign -xchip=ultra3 -xarch=v8plusb -fsimple=2
301.apsi: -fast -xipo=2

Feedback is done as follows, unless otherwise noted:

fdo_pre0: rm -rf ./feedback.profile ./SunWS_cache
PASS1: -xprofile=collect:./feedback
PASS2: -xprofile=use:./feedback

Portability:

178.galgel: -e -fixed

Shell Environments:

Stack size set to unlimited via "ulimit -s unlimited"
PRISM_HEAP=268435456
PRISM_MODE=2

Kernel Parameters (/etc/system):

default
autoup=900
tune_t_fsflushr=1