



# SPEChpc™ 2021 Tiny Result

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## Intel

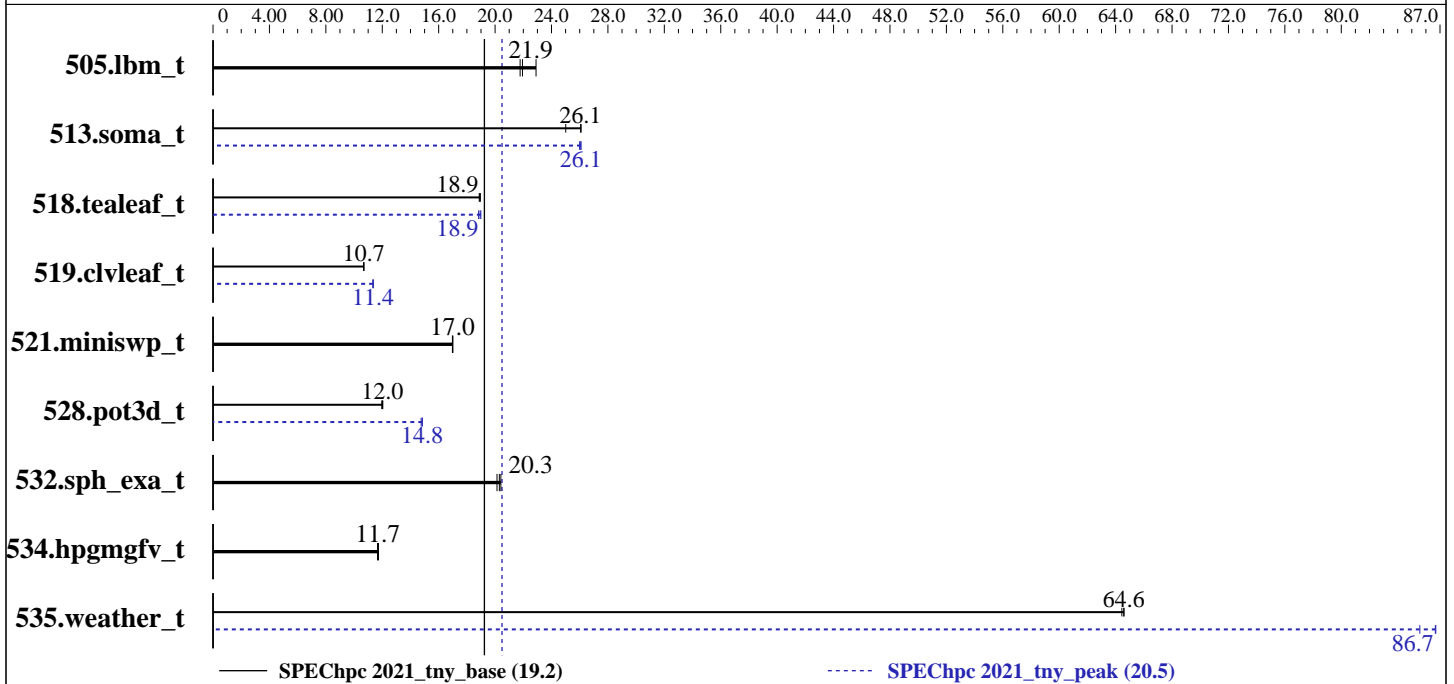
Endeavour: Intel Server D50DNP1SBB (Intel Xeon Platinum 8592+)

SPEChpc 2021\_tny\_base = 19.2

SPEChpc 2021\_tny\_peak = 20.5

hpc2021 License: 13  
Test Sponsor: Intel  
Tested by: Intel

Test Date: Apr-2024  
Hardware Availability: Dec-2023  
Software Availability: Mar-2024



## Results Table

Benchmark	Base										Peak							
	Model	Ranks	Thrds/Rnk	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Model	Ranks	Thrds/Rnk	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
505.lbm_t	OMP	8	32	98.2	22.9	<b>103</b>	<b>21.9</b>	103	21.8	OMP	8	32	98.2	22.9	<b>103</b>	<b>21.9</b>	103	21.8
513.soma_t	OMP	8	32	142	26.1	<b>142</b>	<b>26.1</b>	148	25.0	OMP	8	32	<b>142</b>	<b>26.1</b>	142	26.0	142	26.1
518.tealeaf_t	OMP	8	32	87.4	18.9	87.1	19.0	<b>87.2</b>	<b>18.9</b>	OMP	8	32	86.9	19.0	87.6	18.8	<b>87.1</b>	<b>18.9</b>
519.clvleaf_t	OMP	8	32	154	10.7	<b>154</b>	<b>10.7</b>	154	10.7	OMP	8	32	145	11.3	<b>145</b>	<b>11.4</b>	145	11.4
521.miniswp_t	OMP	8	32	<b>94.2</b>	<b>17.0</b>	94.1	17.0	94.3	17.0	OMP	8	32	<b>94.2</b>	<b>17.0</b>	94.1	17.0	94.3	17.0
528.pot3d_t	OMP	8	32	178	12.0	<b>177</b>	<b>12.0</b>	177	12.0	OMP	32	8	144	14.8	143	14.8	<b>143</b>	<b>14.8</b>
532.sph_exa_t	OMP	8	32	<b>96.0</b>	<b>20.3</b>	96.8	20.1	95.5	20.4	OMP	8	32	<b>96.0</b>	<b>20.3</b>	96.8	20.1	95.5	20.4
534.hpgmgfv_t	OMP	8	32	<b>101</b>	<b>11.7</b>	101	11.7	100	11.7	OMP	8	32	<b>101</b>	<b>11.7</b>	101	11.7	100	11.7
535.weather_t	OMP	8	32	50.0	64.4	49.9	64.6	<b>49.9</b>	<b>64.6</b>	OMP	128	2	37.2	86.7	37.7	85.6	<b>37.2</b>	<b>86.7</b>

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.



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### Hardware Summary

Type of System: Homogenous Cluster  
Compute Node: Intel Server D50DNP1SBB (Xeon 8592+)  
Interconnect: Mellanox HDR  
Compute Nodes Used: 2  
Total Chips: 4  
Total Cores: 256  
Total Threads: 512  
Total Memory: 1 TB  
Max. Peak Threads: 32

### Software Summary

Compiler: Intel oneAPI Compiler 2024.1.0  
MPI Library: Intel MPI Library 2021.12 for Linux OS  
Other MPI Info: None  
Other Software: None  
Base Parallel Model: OMP  
Base Ranks Run: 8  
Base Threads Run: 32  
Peak Parallel Models: OMP  
Minimum Peak Ranks: 8  
Maximum Peak Ranks: 128  
Max. Peak Threads: 32  
Min. Peak Threads: 2

### Node Description: Intel Server D50DNP1SBB (Xeon 8592+)

#### Hardware

Number of nodes: 2  
Uses of the node: Compute  
Vendor: Intel  
Model: Intel Server D50DNP1SBB (Xeon 8592+)  
CPU Name: Intel Xeon Platinum 8592+  
CPU(s) orderable: 1, 2 chips  
Chips enabled: 2  
Cores enabled: 128  
Cores per chip: 64  
Threads per core: 2  
CPU Characteristics: Turbo Boost Technology up to 3.9 GHz  
CPU MHz: 1900  
Primary Cache: 32 KB I + 48 KB D on chip per core  
Secondary Cache: 2 MB I+D on chip per core  
L3 Cache: 320 MB I+D on chip per chip  
Other Cache: None  
Memory: 512 GB (16 x 32 GB 2Rx8 PC5-5600B-R)  
Disk Subsystem: 1 x 1 TB NVMe U.2 2.5" SSD  
Other Hardware: None  
Accel Count: None  
Accel Model: None  
Accel Vendor: None  
Accel Type: None  
Accel Connection: None  
Accel ECC enabled: None  
Accel Description: None  
Adapter: Mellanox ConnectX-6 HDR  
Number of Adapters: 1  
Slot Type: PCI-Express 4.0 x16  
Data Rate: 200Gbit/s  
Ports Used: 1  
Interconnect Type: Mellanox HDR

#### Software

Accelerator Driver: None  
Adapter: Mellanox ConnectX-6 HDR  
Adapter Driver: 23.04-0.5.3  
Adapter Firmware: 20.37.1014  
Operating System: Rocky Linux 8.8 (Green Obsidian)  
4.18.0-477.15.1.el8\_8.x86\_64  
Local File System: xfs  
Shared File System: PANASAS FS  
System State: Run level 5  
Other Software: None



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## Interconnect Description: Mellanox HDR

### Hardware

### Software

Vendor: Mellanox : --  
Model: Mellanox HDR  
Switch Model: Mellanox MQM8790-HS2F Quantum HDR InfiniBand Switch  
Number of Switches: 18  
Number of Ports: 40  
Data Rate: 200 Gbit/s  
Firmware: 20.36.1010  
Topology: Fat-tree  
Primary Use: MPI Traffic

## Submit Notes

The config file option 'submit' was used.

## General Notes

The PANASAS filesystem as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC HPG Policy document, <http://www.spec.org/hpg/policy.html>

## Compiler Version Notes

=====  
CXXC 532.sph\_exa\_t(base, peak)  
-----

Intel(R) oneAPI DPC++/C++ Compiler 2024.1.0 (2024.1.0.20240308)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir:  
/global/panfs05/admin5/opt/intel/oneAPI/2024.1.0/compiler/2024.1/bin/compiler  
Configuration file:  
/global/panfs05/admin5/opt/intel/oneAPI/2024.1.0/compiler/2024.1/bin/compiler/./icpx.cfg  
-----

=====  
CC 505.lbm\_t(base, peak) 513.soma\_t(base, peak) 518.tealeaf\_t(base, peak)  
521.miniswp\_t(base, peak) 534.hpgmgfv\_t(base, peak)  
-----

Intel(R) oneAPI DPC++/C++ Compiler 2024.1.0 (2024.1.0.20240308)  
Target: x86\_64-unknown-linux-gnu

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## Compiler Version Notes (Continued)

```

Thread model: posix
InstalledDir:
  /global/panfs05/admin5/opt/intel/oneAPI/2024.1.0/compiler/2024.1/bin/compiler
Configuration file:
  /global/panfs05/admin5/opt/intel/oneAPI/2024.1.0/compiler/2024.1/bin/compiler/./icx.cfg
-----

=====
FC 519.clvleaf_t(base, peak) 528.pot3d_t(base, peak) 535.weather_t(base,
  peak)
-----

ifx (IFX) 2024.1.0 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.
-----

```

## Base Compiler Invocation

```

C benchmarks:
mpiicc -cc=icx

C++ benchmarks:
mpiicpc -cxx=icpx

Fortran benchmarks:
mpiifort -fc=ifx

```

## Base Portability Flags

```

505.lbm_t: -lstdc++ -std=c++14
513.soma_t: -lstdc++ -std=c++14
518.tealeaf_t: -lstdc++ -std=c++14
521.miniswp_t: -lstdc++ -std=c++14
534.hpgmgfv_t: -lstdc++ -std=c++14

```

## Base Optimization Flags

```

C benchmarks:
-O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512
-qopt-multiple-gather-scatter-by-shuffles -fiopenmp -ffast-math -flto
-funroll-loops

```

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512  
-qopt-multiple-gather-scatter-by-shuffles -fiopenmp -ffast-math -flto  
-funroll-loops
```

Fortran benchmarks:

```
-O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512  
-qopt-multiple-gather-scatter-by-shuffles -fiopenmp -ffast-math -flto  
-funroll-loops -nostandard-realloc-lhs -align array64byte
```

## Base Other Flags

C benchmarks:

```
-Wno-incompatible-function-pointer-types
```

## Peak Compiler Invocation

C benchmarks:

```
mpiicc -cc=icx
```

C++ benchmarks:

```
mpiicpc -cxx=icpx
```

Fortran benchmarks:

```
mpiifort -fc=ifx
```

## Peak Portability Flags

```
505.lbm_t: -lstdc++ -std=c++14  
513.soma_t: -lstdc++ -std=c++14  
518.tealeaf_t: -lstdc++ -std=c++14  
521.miniswp_t: -lstdc++ -std=c++14  
534.hpgmgfv_t: -lstdc++ -std=c++14
```



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## Peak Optimization Flags

C benchmarks:

505.lbm\_t: basepeak = yes

513.soma\_t: -O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512  
-qopt-multiple-gather-scatter-by-shuffles -fiopenmp  
-ffast-math -flto -funroll-loops

518.tealeaf\_t: Same as 513.soma\_t

521.miniswp\_t: basepeak = yes

534.hpgmgfv\_t: basepeak = yes

C++ benchmarks:

532.sph\_exa\_t: basepeak = yes

Fortran benchmarks:

519.clvleaf\_t: -O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512  
-qopt-multiple-gather-scatter-by-shuffles -fiopenmp  
-ffast-math -flto -funroll-loops  
-qopt-streaming-stores=always -nostandard-realloc-lhs  
-align array64byte

528.pot3d\_t: -O3 -Ofast -xCORE-AVX512 -mprefer-vector-width=512  
-qopt-multiple-gather-scatter-by-shuffles -fiopenmp  
-ffast-math -flto -funroll-loops -nostandard-realloc-lhs  
-align array64byte

535.weather\_t: Same as 528.pot3d\_t

## Peak Other Flags

C benchmarks:

-Wno-incompatible-function-pointer-types

The flags file that was used to format this result can be browsed at

[http://www.spec.org/hpc2021/flags/Intel\\_compiler\\_flags.2024-12-11.html](http://www.spec.org/hpc2021/flags/Intel_compiler_flags.2024-12-11.html)



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You can also download the XML flags source by saving the following link:

[http://www.spec.org/hpc2021/flags/Intel\\_compiler\\_flags.2024-12-11.xml](http://www.spec.org/hpc2021/flags/Intel_compiler_flags.2024-12-11.xml)

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For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

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