



# SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SD535 V3  
(3.20 GHz, AMD EPYC 9255)

SPECrate®2017\_fp\_base = 492

SPECrate®2017\_fp\_peak = 504

CPU2017 License: 9017

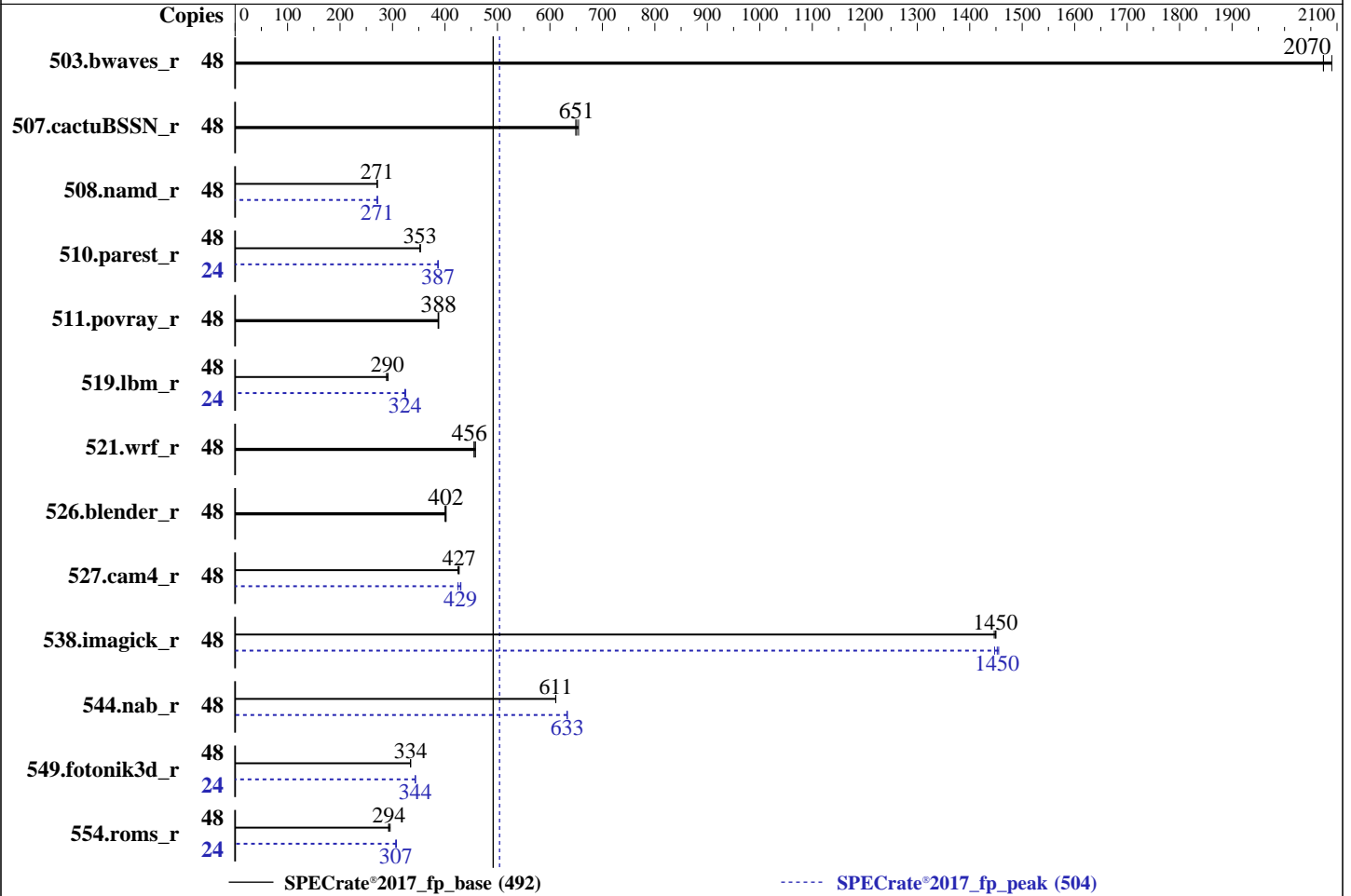
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Mar-2025

Hardware Availability: Feb-2025

Software Availability: Oct-2024



### Hardware

CPU Name: AMD EPYC 9255  
 Max MHz: 4300  
 Nominal: 3200  
 Enabled: 24 cores, 1 chip, 2 threads/core  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 128 MB I+D on chip per chip,  
 32 MB shared / 6 cores  
 Other: None  
 Memory: 384 GB (12 x 32 GB 2Rx8 PC5-6400B-R, running at 6000)  
 Storage: 1 x 960 GB M.2 SATA SSD  
 Other: CPU Cooling: Air

### Software

OS: Red Hat Enterprise Linux 9.4 (Plow)  
 Kernel 5.14.0-427.13.1.el9\_4.x86\_64  
 Compiler: C/C++/Fortran: Version 5.0.0 of AOCC  
 Parallel: No  
 Firmware: Lenovo BIOS Version GPE113I 4.10 released Dec-2024  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	48	230	2090	232	2070	<u>232</u>	<u>2070</u>	48	230	2090	232	2070	<u>232</u>	<u>2070</u>
507.cactuBSSN_r	48	<u>93.3</u>	<u>651</u>	92.8	655	93.6	649	48	<u>93.3</u>	<u>651</u>	92.8	655	93.6	649
508.namd_r	48	168	271	168	271	<u>168</u>	<u>271</u>	48	<u>168</u>	<u>271</u>	168	271	168	271
510.parest_r	48	<u>356</u>	<u>353</u>	356	352	356	353	24	<u>162</u>	<u>387</u>	162	387	162	386
511.povray_r	48	289	388	290	387	<u>289</u>	<u>388</u>	48	289	388	290	387	<u>289</u>	<u>388</u>
519.lbm_r	48	175	288	<u>174</u>	<u>290</u>	173	292	24	78.2	324	<u>78.1</u>	<u>324</u>	77.8	325
521.wrf_r	48	<u>236</u>	<u>456</u>	235	458	236	456	48	<u>236</u>	<u>456</u>	235	458	236	456
526.blender_r	48	183	400	<u>182</u>	<u>402</u>	182	402	48	183	400	<u>182</u>	<u>402</u>	182	402
527.cam4_r	48	198	425	197	427	<u>197</u>	<u>427</u>	48	198	425	<u>195</u>	<u>429</u>	195	430
538.imagick_r	48	82.5	1450	82.3	1450	<u>82.4</u>	<u>1450</u>	48	82.0	1460	<u>82.2</u>	<u>1450</u>	82.5	1450
544.nab_r	48	132	611	<u>132</u>	<u>611</u>	132	611	48	<u>128</u>	<u>633</u>	128	633	128	633
549.fotonik3d_r	48	<u>559</u>	<u>334</u>	559	334	560	334	24	<u>272</u>	<u>344</u>	272	343	272	344
554.roms_r	48	261	292	<u>260</u>	<u>294</u>	258	296	24	124	308	<u>124</u>	<u>307</u>	124	306

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

cpupower set to performance mode

(Continued on next page)



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## Operating System Notes (Continued)

```
cpupower frequency-set -r -g performance
To enable Transparent Hugepages (THP) for all allocations:
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

## Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH =
"/home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2/amd_rate_aocc500_znver5_A_lib/lib:/home/cpu2017-1.1.9-amd
-aocc500_znver5_A1.2/amd_rate_aocc500_znver5_A_lib/lib32:"
MALLOC_CONF = "retain:true"
```

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

## Platform Notes

BIOS configuration:  
Choose Operating Mode set to Maximum Performance and then set it to Custom Mode  
NUMA Nodes per Socket set to NPS4  
P-State set to Enabled

```
Sysinfo program /home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost.localdomain Wed Mar 5 10:13:52 2025
```

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 252 (252-32.e19\_4)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info

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### Platform Notes (Continued)

- 15. sysctl
- 16. /sys/kernel/mm/transparent\_hugepage
- 17. /sys/kernel/mm/transparent\_hugepage/khugepaged
- 18. OS release
- 19. Disk information
- 20. /sys/devices/virtual/dmi/id
- 21. dmidecode
- 22. BIOS

```
1. uname -a
Linux localhost.localdomain 5.14.0-427.13.1.el9_4.x86_64 #1 SMP PREEMPT_DYNAMIC Wed Apr 10 10:29:16 EDT
2024 x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
10:13:52 up 3 min,  0 users,  load average: 0.08, 0.03, 0.00
USER      TTY      LOGIN@  IDLE   JCPU   PCPU   WHAT
```

```
3. Username
From environment variable $USER:  root
```

```
4. ulimit -a
real-time non-blocking time (microseconds, -R) unlimited
core file size              (blocks, -c) 0
data seg size               (kbytes, -d) unlimited
scheduling priority         (-e) 0
file size                   (blocks, -f) unlimited
pending signals             (-i) 1546046
max locked memory           (kbytes, -l) 2097152
max memory size             (kbytes, -m) unlimited
open files                  (-n) 1024
pipe size                   (512 bytes, -p) 8
POSIX message queues        (bytes, -q) 819200
real-time priority          (-r) 0
stack size                  (kbytes, -s) unlimited
cpu time                    (seconds, -t) unlimited
max user processes          (-u) 1546046
virtual memory              (kbytes, -v) unlimited
file locks                  (-x) unlimited
```

```
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 31
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
/bin/bash ./02.remote_local_SPECcpu_1.01.sh
/bin/bash ./Run026-compliant-amd-ratefp.sh
python3 ./run_amd_rate_aocc500_znver5_A1.py
/bin/bash ./amd_rate_aocc500_znver5_A1.sh
runcpu --config amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 fprate
runcpu --configfile amd_rate_aocc500_znver5_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode rate --tune base:peak --size test:train:refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.195/templogs/preenv.fprate.195.0.log --lognum 195.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2
```

(Continued on next page)



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### Platform Notes (Continued)

```

-----
6. /proc/cpuinfo
model name      : AMD EPYC 9255 24-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 26
model          : 2
stepping       : 1
microcode      : 0xb00211a
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size      : 192 4K pages
cpu cores     : 24
siblings      : 48
1 physical ids (chips)
48 processors (hardware threads)
physical id 0: core ids 0-5,8-13,16-21,24-29
physical id 0: apicids 0-11,16-27,32-43,48-59
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

```

#### 7. lscpu

```

From lscpu from util-linux 2.37.4:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         52 bits physical, 57 bits virtual
Byte Order:            Little Endian
CPU(s):                48
On-line CPU(s) list:   0-47
Vendor ID:             AuthenticAMD
BIOS Vendor ID:        Advanced Micro Devices, Inc.
Model name:            AMD EPYC 9255 24-Core Processor
BIOS Model name:       AMD EPYC 9255 24-Core Processor
CPU family:            26
Model:                 2
Thread(s) per core:    2
Core(s) per socket:    24
Socket(s):             1
Stepping:              1
Frequency boost:       enabled
CPU(s) scaling MHz:    100%
CPU max MHz:           3200.0000
CPU min MHz:           1500.0000
BogoMIPS:              6390.26
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp
lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid extd_apicid
aperfmpperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2
x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm
extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit
wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb
cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2 ibrs ibpb stibp
ibrs_enhanced vmmcall fsgsbase tsc_adjust bmil avx2 smep bmi2 erms
invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
avx_vnni avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd amd_ppin
cппc arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid
decodeassists pausefilter pfthreshold avic v_vmsave_vmload vgif x2avic

```

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### Platform Notes (Continued)

v\_spec\_ctrl vmmi avx512vbmi umip pku ospke avx512\_vbmi2 gfni vaes  
vpc1mulqdq avx512\_vnni avx512\_bitalg avx512\_vpopcntdq la57 rdpid  
bus\_lock\_detect movdiri movdir64b overflow\_recov succor smca fsmr  
avx512\_vp2intersect flush\_lld debug\_swap

Virtualization:

L1d cache: 1.1 MiB (24 instances)  
L1i cache: 768 KiB (24 instances)  
L2 cache: 24 MiB (24 instances)  
L3 cache: 128 MiB (4 instances)

NUMA node(s): 4  
NUMA node0 CPU(s): 0-5,24-29  
NUMA node1 CPU(s): 6-11,30-35  
NUMA node2 CPU(s): 12-17,36-41  
NUMA node3 CPU(s): 18-23,42-47

Vulnerability Gather data sampling: Not affected

Vulnerability Itlb multihit: Not affected

Vulnerability L1tf: Not affected

Vulnerability Mds: Not affected

Vulnerability Meltdown: Not affected

Vulnerability Mmio stale data: Not affected

Vulnerability Retbleed: Not affected

Vulnerability Spec rstack overflow: Not affected

Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl

Vulnerability Spectre v1: Mitigation; usercopy/swaps barriers and \_\_user pointer sanitization

Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS, IBPB conditional, STIBP

always-on, RSB filling, PBRSE-eIBRS Not affected

Vulnerability Srbds: Not affected

Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	1.1M	12	Data	1	64	1	64
L1i	32K	768K	8	Instruction	1	64	1	64
L2	1M	24M	16	Unified	2	1024	1	64
L3	32M	128M	16	Unified	3	32768	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)  
node 0 cpus: 0-5,24-29  
node 0 size: 96349 MB  
node 0 free: 95893 MB  
node 1 cpus: 6-11,30-35  
node 1 size: 96764 MB  
node 1 free: 96362 MB  
node 2 cpus: 12-17,36-41  
node 2 size: 96723 MB  
node 2 free: 96308 MB  
node 3 cpus: 18-23,42-47  
node 3 size: 96713 MB  
node 3 free: 96270 MB  
node distances:  
node 0 1 2 3  
0: 10 12 12 12  
1: 12 10 12 12  
2: 12 12 10 12  
3: 12 12 12 10

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### Platform Notes (Continued)

9. /proc/meminfo  
MemTotal: 395827852 kB

10. who -r  
run-level 3 Mar 5 10:10

11. Systemd service manager version: systemd 252 (252-32.el9\_4)  
Default Target Status  
multi-user running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	NetworkManager NetworkManager-dispatcher NetworkManager-wait-online auditd chronyd crond dbus-broker getty@ insights-client-boot irqbalance kdump low-memory-monitor mdmonitor microcode nis-domainname nvme-fc-boot-connections rhsmcertd rsyslog rtkit-daemon selinux-autorelabel-mark sshd sssd systemd-boot-update systemd-network-generator udisks2 upower
enabled-runtime	systemd-remount-fs
disabled	canberra-system-bootup canberra-system-shutdown canberra-system-shutdown-reboot chrony-wait chronyd-restricted console-getty cpupower debug-shell dnf-system-upgrade firewalld kvm_stat man-db-restart-cache-update nftables nvme-autoconnect pesign rdisc rhcd rhsm rhsm-facts rpmbd-rebuild selinux-check-proper-disable serial-getty@ sshd-keygen@ systemd-boot-check-no-failures systemd-pstore systemd-sysex
generated	ntp_sync
indirect	sss-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate systemd-sysupdate-reboot

13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=(hd3,gpt2)/boot/vmlinuz-5.14.0-427.13.1.el9\_4.x86\_64  
root=UUID=609e1264-f568-475c-b502-9e0795850886  
ro  
resume=UUID=7f8ee8bf-b24d-4aea-9bd8-e670d4778be8

14. cpupower frequency-info  
analyzing CPU 43:  
current policy: frequency should be within 1.50 GHz and 3.20 GHz.  
The governor "performance" may decide which speed to use within this range.  
boost state support:  
Supported: yes  
Active: yes  
Boost States: 0  
Total States: 3  
Pstate-P0: 12800MHz

15. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	0
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	8

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### Platform Notes (Continued)

```

vm.dirty_writeback_centisecs      500
vm.dirtytime_expire_seconds      43200
vm.extfrag_threshold              500
vm.min_unmapped_ratio            1
vm.nr_hugepages                   0
vm.nr_hugepages_mempolicy        0
vm.nr_overcommit_hugepages       0
vm.swappiness                      1
vm.watermark_boost_factor        15000
vm.watermark_scale_factor        10
vm.zone_reclaim_mode             1

```

```

-----
16. /sys/kernel/mm/transparent_hugepage
defrag          [always] defer+madvise madvise never
enabled        [always] madvise never
hpage_pmd_size 2097152
shmem_enabled  always within_size advise [never] deny force

```

```

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs  10000

```

```

-----
18. OS release
From /etc/*-release /etc/*-version
os-release      Red Hat Enterprise Linux 9.4 (Plow)
redhat-release Red Hat Enterprise Linux release 9.4 (Plow)
system-release Red Hat Enterprise Linux release 9.4 (Plow)

```

```

-----
19. Disk information
SPEC is set to: /home/cpu2017-1.1.9-amd-aocc500_znver5_A1.2
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/nvme0n1p4  xfs   820G  39G  781G  5% /home

```

```

-----
20. /sys/devices/virtual/dmi/id
Vendor:      Lenovo
Product:     ThinkSystem SD535 V3
Product Family: ThinkSystem
Serial:      1234567890

```

```

-----
21. dmidecode
Additional information from dmidecode 3.5 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
  12x Samsung M321R4GA3PB2-CCPEC 32 GB 2 rank 6400, configured at 6000

```

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### Platform Notes (Continued)

22. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: Lenovo  
BIOS Version: GPE113I-4.10  
BIOS Date: 12/12/2024  
BIOS Revision: 4.10  
Firmware Revision: 5.10

### Compiler Version Notes

=====  
C | 519.lbm\_r(base, peak) 538.imagick\_r(base, peak) 544.nab\_r(base, peak)  
-----

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
-----

=====  
C++ | 508.namd\_r(base, peak) 510.parest\_r(base, peak)  
-----

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
-----

=====  
C++, C | 511.povray\_r(base, peak) 526.blender\_r(base, peak)  
-----

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
-----

=====  
C++, C, Fortran | 507.cactuBSSN\_r(base, peak)  
-----

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
-----  
=====

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# SPEC CPU®2017 Floating Point Rate Result

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## Lenovo Global Technology

ThinkSystem SD535 V3  
(3.20 GHz, AMD EPYC 9255)

SPECrate®2017\_fp\_base = 492

SPECrate®2017\_fp\_peak = 504

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Mar-2025

**Hardware Availability:** Feb-2025

**Software Availability:** Oct-2024

### Compiler Version Notes (Continued)

Fortran | 503.bwaves\_r(base, peak) 549.fotonik3d\_r(base, peak) 554.roms\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

Fortran, C | 521.wrf\_r(base, peak) 527.cam4\_r(base, peak)

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)

Target: x86\_64-unknown-linux-gnu

Thread model: posix

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

### Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

### Base Portability Flags

503.bwaves\_r: -DSPEC\_LP64

507.cactuBSSN\_r: -DSPEC\_LP64

508.namd\_r: -DSPEC\_LP64

510.parest\_r: -DSPEC\_LP64

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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## Lenovo Global Technology

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Software Availability: Oct-2024

## Base Portability Flags (Continued)

```

511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
526.blender_r: -funsigned-char -DSPEC_LP64
527.cam4_r: -DSPEC_CASE_FLAG -DSPEC_LP64
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

```

## Base Optimization Flags

### C benchmarks:

```

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-ldist-scalar-expand -fenable-aggressive-gather -O3
-march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl

```

### C++ benchmarks:

```

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100 -mllvm -loop-unswitch-threshold=200000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm -lamdalloc
-lflang -ldl

```

### Fortran benchmarks:

```

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -flto -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -zopt -lamdlibm -lamdalloc
-lflang -ldl

```

### Benchmarks using both Fortran and C:

```

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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## Lenovo Global Technology

ThinkSystem SD535 V3  
(3.20 GHz, AMD EPYC 9255)

SPECrate®2017\_fp\_base = 492

SPECrate®2017\_fp\_peak = 504

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Mar-2025

Hardware Availability: Feb-2025

Software Availability: Oct-2024

## Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-aggressive-gather=true
-Wl,-mllvm -Wl,-enable-masked-gather-sequence=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamplib -lamdalloc -lflang -ldl
```

Benchmarks using both C and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -lamplib -lamdalloc -lflang
-ldl
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Wl,-mllvm -Wl,-extra-inliner
-O3 -march=znver5 -fveclib=AMDLIBM -ffast-math -fno-PIE -no-pie
-flto -fstruct-layout=7 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays -fstrip-mining
-mllvm -reduce-array-computations=3 -zopt -mllvm -unroll-threshold=100
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fepilog-vectorization-of-inductions
-lamplib -lamdalloc -lflang -ldl
```

## Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

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# SPEC CPU®2017 Floating Point Rate Result

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**Lenovo Global Technology**

ThinkSystem SD535 V3  
(3.20 GHz, AMD EPYC 9255)

SPECrate®2017\_fp\_base = 492

SPECrate®2017\_fp\_peak = 504

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Mar-2025

**Hardware Availability:** Feb-2025

**Software Availability:** Oct-2024

## Base Other Flags (Continued)

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using both C and C++:

clang++ clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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## Lenovo Global Technology

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**Software Availability:** Oct-2024

## Peak Optimization Flags (Continued)

```
519.lbm_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=7 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

538.imagick\_r: Same as 519.lbm\_r

```
544.nab_r: -m64 -flto -Wl,-mllvm -Wl,-ldist-scalar-expand
-fenable-aggressive-gather -Ofast -march=znver5
-fveclib=AMDLIBM -ffast-math -fstruct-layout=7
-mllvm -unroll-threshold=50 -freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

C++ benchmarks:

```
508.namd_r: -m64 -std=c++14
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

```
510.parest_r: -m64 -std=c++14 -flto -Wl,-mllvm -Wl,-suppress-fmas
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math
-mllvm -unroll-threshold=100
-mllvm -reduce-array-computations=3 -zopt -lamdlibm
-lamdalloc -ldl
```

Fortran benchmarks:

503.bwaves\_r: basepeak = yes

```
549.fotonik3d_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto
-Mrecursive -mllvm -reduce-array-computations=3
-fepilog-vectorization-of-inductions -fvector-transform
```

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# SPEC CPU®2017 Floating Point Rate Result

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## Lenovo Global Technology

ThinkSystem SD535 V3  
(3.20 GHz, AMD EPYC 9255)

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SPECrate®2017\_fp\_peak = 504

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Mar-2025

**Hardware Availability:** Feb-2025

**Software Availability:** Oct-2024

## Peak Optimization Flags (Continued)

549.fotonik3d\_r (continued):

-fscalar-transform -lamdlibm -lamdalloc -ldl -lflang

554.roms\_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-Mrecursive -mllvm -reduce-array-computations=3  
-fepilog-vectorization-of-inductions -zopt -lamdlibm  
-lamdalloc -ldl -lflang

Benchmarks using both Fortran and C:

521.wrf\_r: basepeak = yes

527.cam4\_r: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast  
-march=znver5 -fveclib=AMDLIBM -ffast-math -flto  
-fstruct-layout=7 -mllvm -unroll-threshold=50  
-mllvm -inline-threshold=1000 -fremap-arrays  
-mllvm -reduce-array-computations=3 -zopt -Mrecursive  
-funroll-loops -mllvm -lsr-in-nested-loop  
-fepilog-vectorization-of-inductions -lamdlibm -lamdalloc  
-ldl -lflang

Benchmarks using both C and C++:

511.povray\_r: basepeak = yes

526.blender\_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN\_r: basepeak = yes

## Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument

C++ benchmarks:

-Wno-unused-command-line-argument

(Continued on next page)



# SPEC CPU®2017 Floating Point Rate Result

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## Lenovo Global Technology

ThinkSystem SD535 V3  
(3.20 GHz, AMD EPYC 9255)

SPECrate®2017\_fp\_base = 492

SPECrate®2017\_fp\_peak = 504

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**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Mar-2025

**Hardware Availability:** Feb-2025

**Software Availability:** Oct-2024

## Peak Other Flags (Continued)

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument

Benchmarks using both C and C++:

-Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Turin-E.html>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Turin-E.xml>

<http://www.spec.org/cpu2017/flags/aocc500-flags.2024-10-10.xml>

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