



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

## Dell Inc.

SPECspeed®2017\_fp\_base = 545

PowerEdge M7725 (AMD EPYC 9755 128-Core Processor)

SPECspeed®2017\_fp\_peak = 558

CPU2017 License: 6573

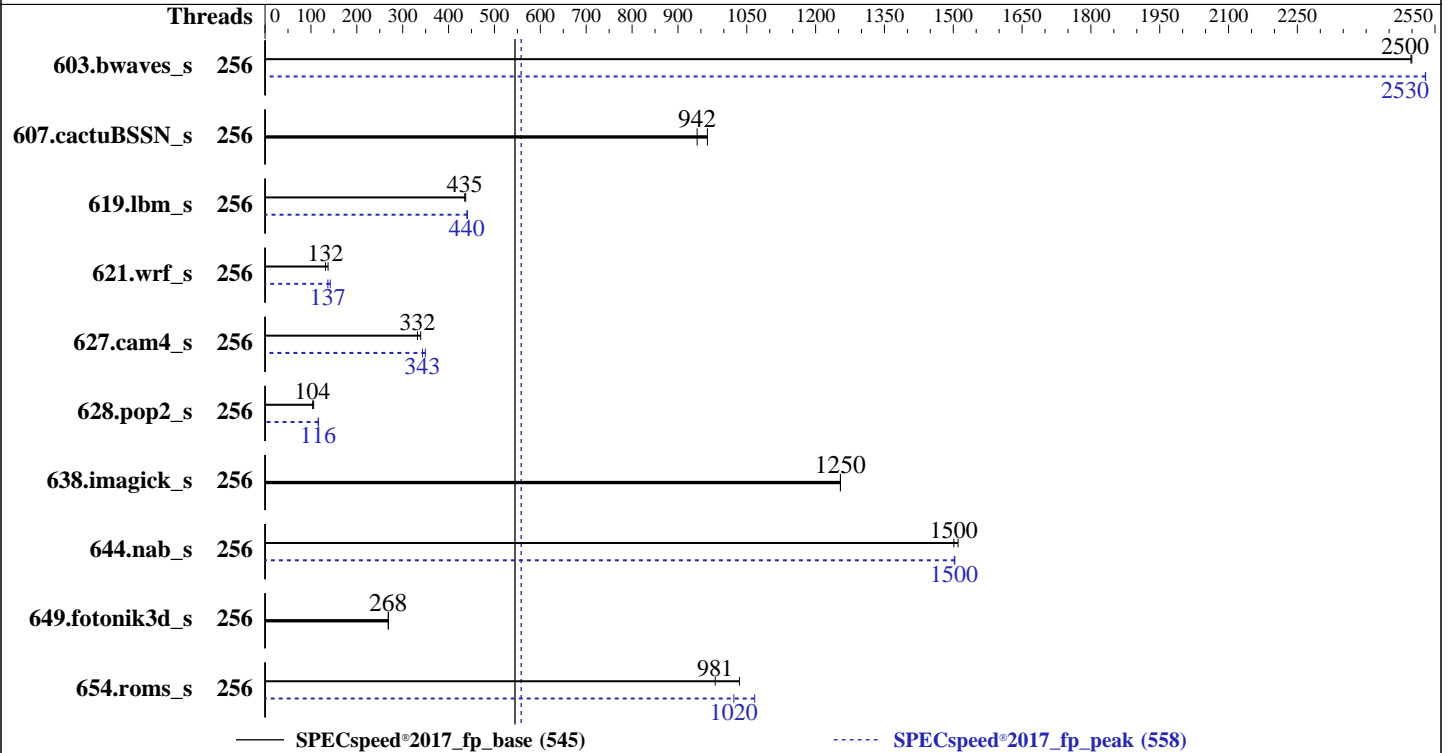
Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Mar-2025

Hardware Availability: Mar-2025

Software Availability: Feb-2025



### Hardware

CPU Name: AMD EPYC 9755  
 Max MHz: 4100  
 Nominal: 2700  
 Enabled: 256 cores, 2 chips  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 512 MB I+D on chip per chip, 32 MB shared / 8 cores  
 Other: None  
 Memory: 1536 GB (24 x 64 GB 2Rx4 PC5-6400B-R)  
 Storage: 130 GB on tmpfs  
 Other: CPU Cooling: DLC

### Software

OS: Ubuntu 24.04.2 LTS  
 6.8.0-54-generic  
 Compiler: C/C++/Fortran: Version 5.0.0 of AOCC  
 Parallel: Yes  
 Firmware: Version 1.0.0 released Jan-2025  
 File System: tmpfs  
 System State: Run level 5 (graphical multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: None  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base						Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	256	23.6	2500	<b><u>23.6</u></b>	<b><u>2500</u></b>			256	<b><u>23.3</u></b>	<b><u>2530</u></b>	23.3	2530		
607.cactuBSSN_s	256	<b><u>17.7</u></b>	<b><u>942</u></b>	17.3	964			256	<b><u>17.7</u></b>	<b><u>942</u></b>	17.3	964		
619.lbm_s	256	<b><u>12.0</u></b>	<b><u>435</u></b>	12.0	437			256	<b><u>11.9</u></b>	<b><u>440</u></b>	11.9	441		
621.wrf_s	256	<b><u>100</u></b>	<b><u>132</u></b>	96.3	137			256	93.1	142	<b><u>96.6</u></b>	<b><u>137</u></b>		
627.cam4_s	256	26.1	339	<b><u>26.7</u></b>	<b><u>332</u></b>			256	25.4	349	<b><u>25.8</u></b>	<b><u>343</u></b>		
628.pop2_s	256	112	106	<b><u>115</u></b>	<b><u>104</u></b>			256	<b><u>103</u></b>	<b><u>116</u></b>	102	117		
638.imagick_s	256	<b><u>11.5</u></b>	<b><u>1250</u></b>	11.5	1250			256	<b><u>11.5</u></b>	<b><u>1250</u></b>	11.5	1250		
644.nab_s	256	<b><u>11.6</u></b>	<b><u>1500</u></b>	11.6	1510			256	11.6	1500	<b><u>11.6</u></b>	<b><u>1500</u></b>		
649.fotonik3d_s	256	33.9	269	<b><u>34.0</u></b>	<b><u>268</u></b>			256	33.9	269	<b><u>34.0</u></b>	<b><u>268</u></b>		
654.roms_s	256	15.2	1030	<b><u>16.0</u></b>	<b><u>981</u></b>			256	14.8	1070	<b><u>15.4</u></b>	<b><u>1020</u></b>		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at <http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage, 'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations, 'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and 'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.



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## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

GOMP\_CPU\_AFFINITY = "0-255"

LD\_LIBRARY\_PATH =

"/mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5\_A1/amd\_speed\_aocc500\_znver5\_A\_lib/lib:/mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5\_A1/amd\_speed\_aocc500\_znver5\_A\_lib/lib32:"

LIBOMP\_NUM\_HIDDEN\_HELPER\_THREADS = "0"

MALLOC\_CONF = "retain:true"

OMP\_DYNAMIC = "false"

OMP\_SCHEDULE = "static"

OMP\_STACKSIZE = "128M"

OMP\_THREAD\_LIMIT = "256"

Environment variables set by runcpu during the 603.bwaves\_s peak run:

GOMP\_CPU\_AFFINITY = "0-255"

Environment variables set by runcpu during the 619.lbm\_s peak run:

GOMP\_CPU\_AFFINITY = "0-255"

Environment variables set by runcpu during the 621.wrf\_s peak run:

GOMP\_CPU\_AFFINITY = "0-255"

Environment variables set by runcpu during the 627.cam4\_s peak run:

GOMP\_CPU\_AFFINITY = "0-255"

Environment variables set by runcpu during the 628.pop2\_s peak run:

GOMP\_CPU\_AFFINITY = "0-255"

Environment variables set by runcpu during the 644.nab\_s peak run:

GOMP\_CPU\_AFFINITY = "0-255"

Environment variables set by runcpu during the 654.roms\_s peak run:

GOMP\_CPU\_AFFINITY = "0-255"

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 9D64 CPU + 500GiB Memory using Ubuntu 22.04

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 130 GB ramdisk created with the cmd: "mount -t tmpfs -o size=130G tmpfs /mnt/ramdisk"

## Platform Notes

BIOS Settings:

Logical Processor : Disabled  
Virtualization Technology : Disabled

System Profile : Custom  
C-States : Disabled  
Memory Patrol Scrub : Disabled

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## Platform Notes (Continued)

```

PCI ASPM L1 Link Power Management : Disabled
Periodic Directory Rinse Tuning : Blended
Determinism Control : Manual
Determinism Slider : Power Determinism
Optimizer Mode : Enabled
DF CState : Disabled
Algorithm Performance Boost Disable : Enabled
CPPC : Disabled
Adaptive Allocation : Enabled
Dram Refresh Delay : Performance
Correctable Memory ECC SMI : Disabled
DIMM Self Healing -
on Uncorrectable Memory Error : Disabled

```

```

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on L302624-M7725 Thu Mar 6 15:43:08 2025

```

SUT (System Under Test) info as seen by some common utilities.

### Table of contents

- ```

-----
1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 255 (255.4-lubuntu8.5)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS
-----

1. uname -a
Linux L302624-M7725 6.8.0-54-generic #56-Ubuntu SMP PREEMPT_DYNAMIC Sat Feb 8 00:37:57 UTC 2025 x86_64
x86_64 x86_64 GNU/Linux
-----

2. w
 15:43:08 up 2:10, 2 users, load average: 6.95, 5.69, 3.47
USER  TTY      FROM          LOGIN@   IDLE   JCPU   PCPU   WHAT
root  tty1    -             13:48   1:52m  1.55s  0.54s  /bin/bash ./amd_speed_aocc500_znver5_A1.sh
root  tty2    -             15:24   17:40  0.03s  ?      -bash

```

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## Platform Notes (Continued)

### 3. Username

From environment variable \$USER: root

### 4. ulimit -a

```

time(seconds)          unlimited
file(blocks)           unlimited
data(kbytes)           unlimited
stack(kbytes)          unlimited
coredump(blocks)       0
memory(kbytes)         unlimited
locked memory(kbytes) 2097152
process                6188981
nofiles                1024
vmemory(kbytes)        unlimited
locks                  unlimited
rtprio                 0

```

### 5. sysinfo process ancestry

```

/sbin/init
/bin/login -p --
-bash
/bin/bash /home/DellFiles/bin/DELL_speed.sh
/bin/bash /home/DellFiles/bin/dell-run-main.sh speed
/bin/bash /home/DellFiles/bin/dell-run-main.sh speed
/bin/bash /home/DellFiles/bin/AMD/dell-run-speccpu.sh speed --define DL-VERS=6.1a --output_format
html,pdf,txt
python3 ./run_amd_speed_aocc500_znver5_A1.py
/bin/bash ./amd_speed_aocc500_znver5_A1.sh
runcpu --config amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define
DL-BIOS-NPS=1 --define DL-VERS=6.1a --output_format html,pdf,txt fpspeed
runcpu --configfile amd_speed_aocc500_znver5_A1.cfg --tune all --reportable --iterations 2 --define
DL-BIOS-NPS=1 --define DL-VERS=6.1a --output_format html,pdf,txt --nopower --runmode speed --tune
base:peak --size test:train:refspeed fpspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.002/temlogs/preenv.fpspeed.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5_A1

```

### 6. /proc/cpuinfo

```

model name      : AMD EPYC 9755 128-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 26
model          : 2
stepping       : 1
microcode      : 0xb00211e
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size      : 192 4K pages
cpu cores     : 128
siblings      : 128
2 physical ids (chips)
256 processors (hardware threads)
physical id 0: core ids
0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183,192-199,208-215,224-231,
240-247
physical id 1: core ids
0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183,192-199,208-215,224-231,
240-247

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### Platform Notes (Continued)

physical id 0: apicids  
0-7,16-23,32-39,48-55,64-71,80-87,96-103,112-119,128-135,144-151,160-167,176-183,192-199,208-215,224-231,240-247  
physical id 1: apicids  
256-263,272-279,288-295,304-311,320-327,336-343,352-359,368-375,384-391,400-407,416-423,432-439,448-455,464-471,480-487,496-503  
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

#### 7. lscpu

From lscpu from util-linux 2.39.3:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          52 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 256
On-line CPU(s) list:   0-255
Vendor ID:              AuthenticAMD
BIOS Vendor ID:        AMD
Model name:             AMD EPYC 9755 128-Core Processor
BIOS Model name:       AMD EPYC 9755 128-Core Processor
BIOS CPU family:       107
CPU family:            26
Model:                  2
Thread(s) per core:    1
Core(s) per socket:    128
Socket(s):              2
Stepping:               1
Frequency boost:       enabled
CPU(s) scaling MHz:    100%
CPU max MHz:           2700.0000
CPU min MHz:           1500.0000
BogoMIPS:              5392.16
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
                        pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
                        rdtscp lm constant_tsc rep_good amd_lbr_v2 nopl nonstop_tsc cpuid
                        extd_apicid aperfmpperf rapl pni pclmulqdq monitor ssse3 fma cx16 pcid
                        sse4_1 sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
                        cmp_legacy extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
                        osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
                        perfctr_llc mwaitx cpb cat_l3 cdp_l3 hw_pstate ssbd mba perfmon_v2
                        ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase tsc_adjust bmi1 avx2
                        smep bmi2 invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
                        avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
                        xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                        cqm_mbm_local user_shstk avx_vnni avx512_bf16 clzero irperf
                        xsaveerptr rdpru wbnoinvd amd_ppin cppc arat npt lbrv svm_lock
                        nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
                        pfthreshold avic v_vmsave_vmload vgif x2avic v_spec_ctrl vnni
                        avx512vbmi umip pku ospke avx512_vbmi2 gfni vaes vpclmulqdq
                        avx512_vnni avx512_bitalg avx512_vpoptcdq la57 rdpid bus_lock_detect
                        movdiri movdir64b overflow_recov succor smca avx512_vp2intersect
                        flush_lld debug_swap
L1d cache:             12 MiB (256 instances)
L1i cache:             8 MiB (256 instances)
L2 cache:              256 MiB (256 instances)
L3 cache:              1 GiB (32 instances)
NUMA node(s):          2

```

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### Platform Notes (Continued)

```

NUMA node0 CPU(s):          0-127
NUMA node1 CPU(s):          128-255
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:         Not affected
Vulnerability Mds:          Not affected
Vulnerability Meltdown:     Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed:     Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:   Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:   Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
                             disabled; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected
Vulnerability Srbds:        Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

| NAME | ONE-SIZE | ALL-SIZE | WAYS | TYPE        | LEVEL | SETS  | PHY-LINE | COHERENCY-SIZE |
|------|----------|----------|------|-------------|-------|-------|----------|----------------|
| L1d  | 48K      | 12M      | 12   | Data        | 1     | 64    | 1        | 64             |
| L1i  | 32K      | 8M       | 8    | Instruction | 1     | 64    | 1        | 64             |
| L2   | 1M       | 256M     | 16   | Unified     | 2     | 1024  | 1        | 64             |
| L3   | 32M      | 1G       | 16   | Unified     | 3     | 32768 | 1        | 64             |

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 2 nodes (0-1)
node 0 cpus: 0-127
node 0 size: 773275 MB
node 0 free: 767065 MB
node 1 cpus: 128-255
node 1 size: 774044 MB
node 1 free: 772230 MB
node distances:
node  0  1
  0: 10 32
  1: 32 10

```

9. /proc/meminfo

MemTotal: 1584455532 kB

10. who -r

run-level 5 Mar 6 13:32

11. Systemd service manager version: systemd 255 (255.4-lubuntu8.5)

```

Default Target Status
graphical      running

```

12. Services, from systemctl list-unit-files

```

STATE UNIT FILES
enabled ModemManager apparmor appport blk-availability cloud-config cloud-final cloud-init
cloud-init-local console-setup cron dmesg e2scrub_reap finalrd getty@ gpu-manager
grub-common grub-initrd-fallback keyboard-setup lm-sensors lvm2-monitor multipathd
networkd-dispatcher open-iscsi open-vm-tools pollinate rsyslog secureboot-db setvtrgb

```

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## Platform Notes (Continued)

```

enabled-runtime  sysstat systemd-networkd systemd-networkd-wait-online systemd-pstore systemd-resolved
disabled         systemd-timesyncd thermald tuned ua-reboot-cmds ubuntu-advantage udisks2 ufw vgauth
                 netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
                 console-getty debug-shell iscsid nftables rsync serial-getty@ ssh
                 systemd-boot-check-no-failures systemd-confext systemd-network-generator
                 systemd-networkd-wait-online@ systemd-pcrlock-file-system systemd-pcrlock-firmware-code
                 systemd-pcrlock-firmware-config systemd-pcrlock-machine-id systemd-pcrlock-make-policy
                 systemd-pcrlock-secureboot-authority systemd-pcrlock-secureboot-policy systemd-sysext
                 systemd-time-wait-sync upower
indirect         systemd-sysupdate systemd-sysupdate-reboot uuid
masked          cryptdisks cryptdisks-early hwclock multipath-tools-boot screen-cleanup sudo x11-common

```

```

-----
13. Linux kernel boot-time arguments, from /proc/cmdline
BOOT_IMAGE=/vmlinuz-6.8.0-54-generic
root=/dev/mapper/ubuntu--vg-ubuntu--lv
ro

```

```

-----
14. cpupower frequency-info
analyzing CPU 251:
  current policy: frequency should be within 1.50 GHz and 2.70 GHz.
                  The governor "performance" may decide which speed to use
                  within this range.

  boost state support:
    Supported: yes
    Active: yes
    Boost States: 0
    Total States: 3
    Pstate-P0: 2700MHz

```

```

-----
15. tuned-adm active
  Current active profile: latency-performance

```

```

-----
16. sysctl
kernel.numa_balancing          1
kernel.randomize_va_space     0
vm.compaction_proactiveness    20
vm.dirty_background_bytes      0
vm.dirty_background_ratio      3
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 8
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds    43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio          1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy      0
vm.nr_overcommit_hugepages     0
vm.swappiness                  1
vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           1

```

```

-----
17. /sys/kernel/mm/transparent_hugepage
defrag      [always] defer defer+madvise madvise never

```

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## Platform Notes (Continued)

enabled [always] madvise never  
hpage\_pmd\_size 2097152  
shmem\_enabled always within\_size advise [never] deny force

-----  
18. /sys/kernel/mm/transparent\_hugepage/khugepaged  
alloc\_sleep\_millisecs 60000  
defrag 1  
max\_ptes\_none 511  
max\_ptes\_shared 256  
max\_ptes\_swap 64  
pages\_to\_scan 4096  
scan\_sleep\_millisecs 10000

-----  
19. OS release  
From /etc/\*-release /etc/\*-version  
os-release Ubuntu 24.04.2 LTS

-----  
20. Disk information  
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-aocc500-znerv5\_A1  
Filesystem Type Size Used Avail Use% Mounted on  
tmpfs tmpfs 130G 3.3G 127G 3% /mnt/ramdisk

-----  
21. /sys/devices/virtual/dmi/id  
Vendor: Dell Inc.  
Product: PowerEdge M7725  
Product Family: PowerEdge  
Serial: L302624

-----  
22. dmidecode  
Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.  
Memory:  
4x 80AD000080AD HMC94AHBRA277N 64 GB 2 rank 6400  
20x 80AD000080AD HMC94AHBRA480N 64 GB 2 rank 6400

-----  
23. BIOS  
(This section combines info from /sys/devices and dmidecode.)  
BIOS Vendor: Dell Inc.  
BIOS Version: 1.0.0  
BIOS Date: 01/23/2025  
BIOS Revision: 1.0

## Compiler Version Notes

=====  
C | 619.lbm\_s(base, peak) 638.imagick\_s(base, peak) 644.nab\_s(base, peak)  
=====

AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix

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PowerEdge M7725 (AMD EPYC 9755 128-Core Processor)

SPECSpeed®2017\_fp\_peak = 558

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Mar-2025  
Hardware Availability: Mar-2025  
Software Availability: Feb-2025

## Compiler Version Notes (Continued)

InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====  
C++, C, Fortran | 607.cactuBSSN\_s(base, peak)

-----  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====  
Fortran | 603.bwaves\_s(base, peak) 649.fotonik3d\_s(base, peak) 654.roms\_s(base, peak)

-----  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

=====  
Fortran, C | 621.wrf\_s(base, peak) 627.cam4\_s(base, peak) 628.pop2\_s(base, peak)

-----  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin  
AMD clang version 17.0.6 (CLANG: AOCC\_5.0.0-Build#1316 2024\_09\_09)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc/aocc-compiler-rel-5.0.0-4925-1316/bin

## Base Compiler Invocation

C benchmarks:  
clang

Fortran benchmarks:  
flang

Benchmarks using both Fortran and C:  
flang clang

Benchmarks using Fortran, C, and C++:  
clang++ clang flang



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## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64  
607.cactuBSSN\_s: -DSPEC\_LP64  
619.lbm\_s: -DSPEC\_LP64  
621.wrf\_s: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
627.cam4\_s: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
628.pop2\_s: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
638.imagick\_s: -DSPEC\_LP64  
644.nab\_s: -DSPEC\_LP64  
649.fotonik3d\_s: -DSPEC\_LP64  
654.roms\_s: -DSPEC\_LP64

## Base Optimization Flags

### C benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC\_OPENMP -flto  
-fremap-arrays -fstrip-mining -fstruct-layout=7  
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt -mrecip=none -fopenmp=libomp -lomp  
-lamdlibm -lamdalloc -lflang

### Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC\_OPENMP -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -funroll-loops  
-mllvm -lsr-in-nested-loop -mllvm -reduce-array-computations=3  
-Mrecursive -zopt -fopenmp=libomp -lomp -lamdlibm -lamdalloc  
-lflang

### Benchmarks using both Fortran and C:

-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3  
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver5  
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC\_OPENMP -flto  
-fremap-arrays -fstrip-mining -fstruct-layout=7  
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3  
-mllvm -unroll-threshold=50 -zopt -funroll-loops  
-mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none -fopenmp=libomp  
-lomp -lamdlibm -lamdalloc -lflang

### Benchmarks using Fortran, C, and C++:

-m64 -std=c++14 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

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Software Availability: Feb-2025

## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

```
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver5
-fveclib=AMDLIBM -ffast-math -fopenmp -DSPEC_OPENMP -flto
-fremap-arrays -fstrip-mining -fstruct-layout=7
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt
-mllvm -loop-unswitch-threshold=200000 -mllvm -unroll-threshold=100
-funroll-loops -mllvm -lsr-in-nested-loop -Mrecursive -mrecip=none
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

## Base Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Benchmarks using Fortran, C, and C++:

```
-Wno-return-type -Wno-unused-command-line-argument
```

## Peak Compiler Invocation

C benchmarks:

```
clang
```

Fortran benchmarks:

```
flang
```

Benchmarks using both Fortran and C:

```
flang clang
```

Benchmarks using Fortran, C, and C++:

```
clang++ clang flang
```



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Software Availability: Feb-2025

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
619.lbm_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang
```

638.imagick\_s: basepeak = yes

```
644.nab_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -mrecip=none
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

Fortran benchmarks:

```
603.bwaves_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP
-Ofast -march=znver5 -fveclib=AMDLIBM -ffast-math
-fopenmp -fscalar-transform -fvector-transform
-mllvm -reduce-array-computations=3 -Mrecursive
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

649.fotonik3d\_s: basepeak = yes

654.roms\_s: Same as 603.bwaves\_s

Benchmarks using both Fortran and C:

```
621.wrf_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
```

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## Peak Optimization Flags (Continued)

621.wrf\_s (continued):

```
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -funroll-loops
-mllvm -lsr-in-nested-loop -Mrecursive -fopenmp=libomp
-lomp -lamdlibm -lamdalloc -lflang
```

627.cam4\_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

```
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -Mrecursive
-mrecip=none -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang
```

628.pop2\_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

```
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver5 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -DSPEC_OPENMP -fremap-arrays -fstrip-mining
-fstruct-layout=9 -mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3
-mllvm -unroll-threshold=50 -zopt -fscalar-transform
-fvector-transform -Mrecursive -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang
```

Benchmarks using Fortran, C, and C++:

607.cactuBSSN\_s: basepeak = yes

## Peak Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

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## Peak Other Flags (Continued)

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc500-flags.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc500-flags.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-AMD-EPYC-v1.8.xml>

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