



SPEC CPU®2017 Floating Point Rate Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

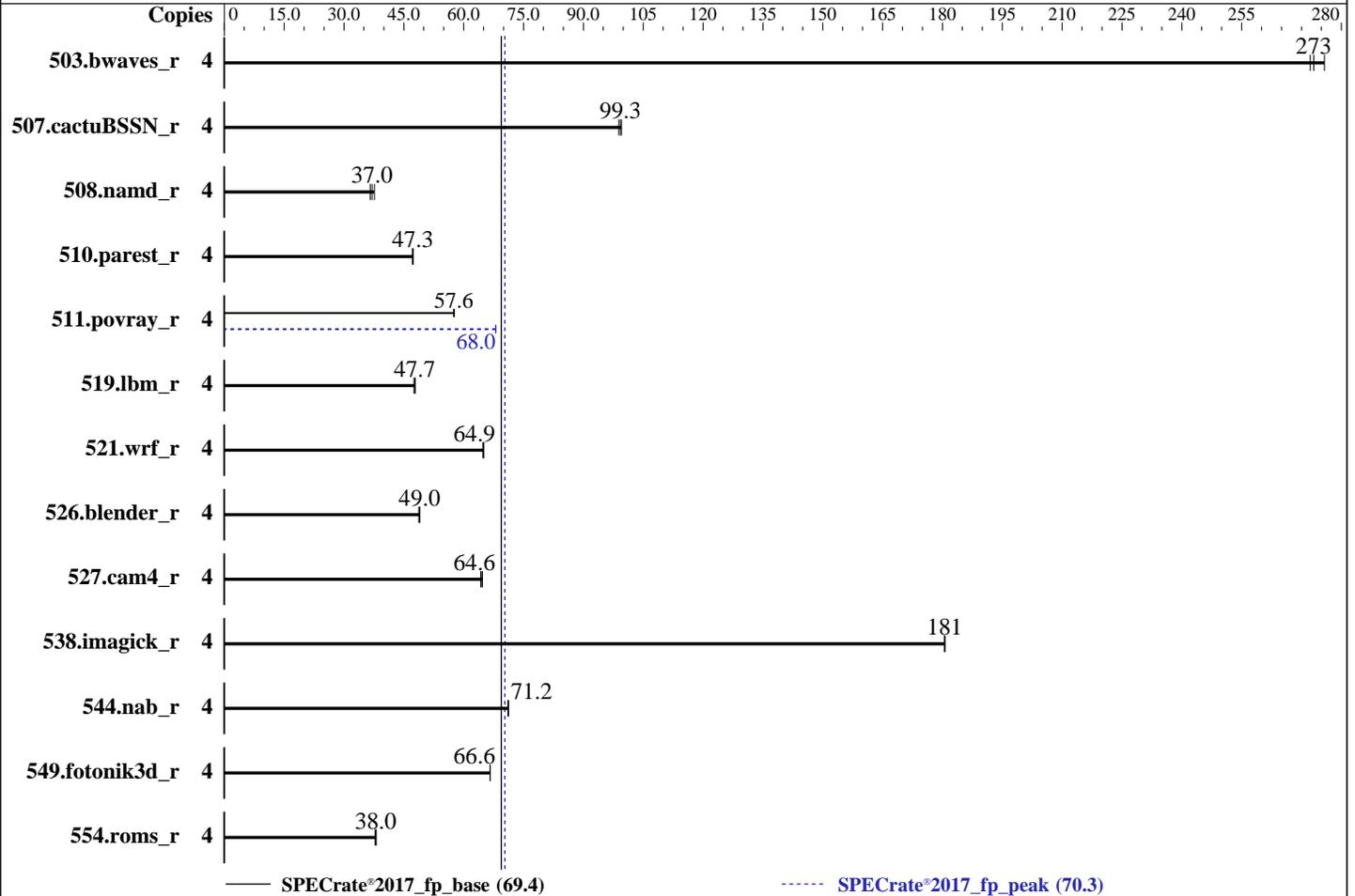
ProLiant MicroServer Gen11
(2.80 GHz, Intel Xeon 6315P)

SPECrate®2017_fp_base = 69.4

SPECrate®2017_fp_peak = 70.3

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Jan-2025
Hardware Availability: Mar-2025
Software Availability: Apr-2024



Hardware

CPU Name: Intel Xeon 6315P
 Max MHz: 4700
 Nominal: 2800
 Enabled: 4 cores, 1 chip
 Orderable: 1 Chip
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 12 MB I+D on chip per chip
 Other: None
 Memory: 64 GB (2 x 32 GB 2Rx8 PC5-5600B-E, running at 4400 , orderable using HPE part# P64339-B21)
 Storage: 1 x 1 TB 7.2 K SATA HDD
 Other: CPU Cooling: Air

Software

OS: Red Hat Enterprise Linux 9.4 (Plow)
 Kernel 5.14.0-427.13.1.el9_4.x86_64
 Compiler: C/C++: Version 2024.1 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2024.1 of Intel Fortran Compiler for Linux;
 Parallel: No
 Firmware: HPE BIOS Version v2.10 12/06/2024 released Dec-2024
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
503.bwaves_r	4	147	272	145	276	<u>147</u>	<u>273</u>	4	147	272	145	276	<u>147</u>	<u>273</u>
507.cactuBSSN_r	4	<u>51.0</u>	<u>99.3</u>	50.9	99.5	51.2	98.9	4	<u>51.0</u>	<u>99.3</u>	50.9	99.5	51.2	98.9
508.namd_r	4	104	36.6	101	37.6	<u>103</u>	<u>37.0</u>	4	104	36.6	101	37.6	<u>103</u>	<u>37.0</u>
510.parest_r	4	221	47.3	222	47.1	<u>221</u>	<u>47.3</u>	4	221	47.3	222	47.1	<u>221</u>	<u>47.3</u>
511.povray_r	4	162	57.6	162	57.5	<u>162</u>	<u>57.6</u>	4	<u>137</u>	<u>68.0</u>	137	68.1	137	68.0
519.lbm_r	4	<u>88.3</u>	<u>47.7</u>	88.0	47.9	88.7	47.5	4	<u>88.3</u>	<u>47.7</u>	88.0	47.9	88.7	47.5
521.wrf_r	4	138	65.1	<u>138</u>	<u>64.9</u>	138	64.8	4	138	65.1	<u>138</u>	<u>64.9</u>	138	64.8
526.blender_r	4	125	48.7	124	49.0	<u>124</u>	<u>49.0</u>	4	125	48.7	124	49.0	<u>124</u>	<u>49.0</u>
527.cam4_r	4	108	64.7	<u>108</u>	<u>64.6</u>	109	64.3	4	108	64.7	<u>108</u>	<u>64.6</u>	109	64.3
538.imagick_r	4	55.1	180	55.1	181	<u>55.1</u>	<u>181</u>	4	55.1	180	55.1	181	<u>55.1</u>	<u>181</u>
544.nab_r	4	<u>94.6</u>	<u>71.2</u>	94.6	71.1	94.5	71.2	4	<u>94.6</u>	<u>71.2</u>	94.6	71.1	94.5	71.2
549.fotonik3d_r	4	<u>234</u>	<u>66.6</u>	234	66.6	234	66.6	4	<u>234</u>	<u>66.6</u>	234	66.6	234	66.6
554.roms_r	4	167	38.1	168	37.8	<u>167</u>	<u>38.0</u>	4	167	38.1	168	37.8	<u>167</u>	<u>38.0</u>

SPECrate®2017_fp_base = **69.4**

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The taskset mechanism was used to bind copies to processors. The config file option 'submit' was used to generate taskset commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
tuned-adm profile was set to throughput-performance using "tuned-adm profile throughput-performance"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"
MALLOCONF = "retain:true"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Red Hat Enterprise Linux 8.4
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

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General Notes (Continued)

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS Configuration:

Workload Profile set to General Throughput Compute
Thermal Configuration set to Maximum Cooling
Enhanced Processor Performance Profile set to Enabled

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on localhost.localdomain Mon Jan 27 06:53:45 2025

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 252 (252-32.el9_4)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. cpupower frequency-info
16. tuned-adm active
17. sysctl
18. /sys/kernel/mm/transparent_hugepage
19. /sys/kernel/mm/transparent_hugepage/khugepaged
20. OS release
21. Disk information
22. /sys/devices/virtual/dmi/id
23. dmidecode
24. BIOS

1. uname -a
Linux localhost.localdomain 5.14.0-427.13.1.el9_4.x86_64 #1 SMP PREEMPT_DYNAMIC Wed Apr 10 10:29:16 EDT 2024 x86_64 x86_64 x86_64 GNU/Linux

2. w
06:53:45 up 3:19, 1 user, load average: 0.00, 0.00, 0.52
USER TTY LOGIN@ IDLE JCPU PCPU WHAT
root pts/0 03:37 9.00s 0.65s 0.00s -bash

3. Username
From environment variable \$USER: root

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Platform Notes (Continued)

```

4. ulimit -a
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 256651
max locked memory (kbytes, -l) 8192
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 256651
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited

```

```

5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 31
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@pts/0
-bash
-bash
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=4 -c
ic2024.1-lin-core-avx2-rate-20240308.cfg --define smt-on --define cores=2 --define physicalfirst --define
no-numa --tune base,peak -o all --define drop_caches fprate
runcpu --nobuild --action validate --define default-platform-flags --define numcopies=4 --configfile
ic2024.1-lin-core-avx2-rate-20240308.cfg --define smt-on --define cores=2 --define physicalfirst --define
no-numa --tune base,peak --output_format all --define drop_caches --nopower --runmode rate --tune
base:peak --size refrate fprate --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.007/templots/preenv.fprate.007.0.log --lognum 007.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

```

```

6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) 6315P
vendor_id      : GenuineIntel
cpu family     : 6
model          : 183
stepping       : 1
microcode      : 0x12c
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs eibrs_pbrsb
cpu cores      : 4
siblings       : 4
1 physical ids (chips)
4 processors (hardware threads)
physical id 0: core ids 0-3
physical id 0: apicids 0,2,4,6
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.

```

7. lscpu

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Platform Notes (Continued)

From lscpu from util-linux 2.37.4:

```

Architecture:          x86_64
CPU op-mode(s):       32-bit, 64-bit
Address sizes:        46 bits physical, 48 bits virtual
Byte Order:           Little Endian
CPU(s):               4
On-line CPU(s) list: 0-3
Vendor ID:            GenuineIntel
BIOS Vendor ID:      Intel(R) Corporation
Model name:           Intel(R) Xeon(R) 6315P
BIOS Model name:     Intel(R) Xeon(R) 6315P
CPU family:           6
Model:                183
Thread(s) per core:  1
Core(s) per socket:  4
Socket(s):            1
Stepping:             1
BogoMIPS:             5606.40
Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                    clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb
                    rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl
                    xtopology nonstop_tsc cpuid aperfperf tsc_known_freq pni pclmulqdq
                    dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm
                    sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c
                    rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb ssbd ibrs ibpb stibp
                    ibrs_enhanced tpr_shadow flexpriority ept vpid ept_ad fsgsbase
                    tsc_adjust bmi1 avx2 smep bmi2 erms invpcid rdseed adx smap clflushopt
                    clwb intel_pt sha_ni xsaveopt xsavec xgetbv1 xsaves split_lock_detect
                    avx_vnni dtherm ida arat pln pts hfi vnmi umip pku ospke waitpkg gfni
                    vaes vpclmulqdq tme rdpid movdiri movdir64b fsrm md_clear serialize
                    pconfig arch_lbr ibt flush_lld arch_capabilities
Virtualization:       VT-x
L1d cache:           192 KiB (4 instances)
L1i cache:           128 KiB (4 instances)
L2 cache:            8 MiB (4 instances)
L3 cache:            12 MiB (1 instance)
NUMA node(s):        1
NUMA node0 CPU(s):  0-3
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit:      Not affected
Vulnerability L1tf:               Not affected
Vulnerability Mds:                Not affected
Vulnerability Meltdown:           Not affected
Vulnerability Mmio stale data:    Not affected
Vulnerability Retbleed:           Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass:  Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:         Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:         Mitigation; Enhanced / Automatic IBRS, IBPB conditional, RSB filling,
                    PBR SB-eIBRS SW sequence
Vulnerability Srbds:              Not affected
Vulnerability Tsx async abort:    Not affected

```

Virtualization:

```

L1d cache:           192 KiB (4 instances)
L1i cache:           128 KiB (4 instances)
L2 cache:            8 MiB (4 instances)
L3 cache:            12 MiB (1 instance)
NUMA node(s):        1
NUMA node0 CPU(s):  0-3

```

```

Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit:      Not affected
Vulnerability L1tf:               Not affected
Vulnerability Mds:                Not affected
Vulnerability Meltdown:           Not affected
Vulnerability Mmio stale data:    Not affected
Vulnerability Retbleed:           Not affected
Vulnerability Spec rstack overflow: Not affected
Vulnerability Spec store bypass:  Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1:         Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:         Mitigation; Enhanced / Automatic IBRS, IBPB conditional, RSB filling,
                    PBR SB-eIBRS SW sequence
Vulnerability Srbds:              Not affected
Vulnerability Tsx async abort:    Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	192K	12	Data	1	64	1	64
L1i	32K	128K	8	Instruction	1	64	1	64
L2	2M	8M	16	Unified	2	2048	1	64
L3	12M	12M	6	Unified	3	32768	1	64

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Platform Notes (Continued)

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 1 nodes (0)
node 0 cpus: 0-3
node 0 size: 64204 MB
node 0 free: 55987 MB
node distances:
node 0
0: 10
```

9. /proc/meminfo

```
MemTotal: 65745764 kB
```

10. who -r

```
run-level 3 Jan 27 03:35
```

11. Systemd service manager version: systemd 252 (252-32.el9_4)

```
Default Target Status
multi-user degraded
```

12. Failed units, from systemctl list-units --state=failed

```
UNIT                                LOAD    ACTIVE SUB    DESCRIPTION
* dnf-makecache.service             loaded failed failed dnf makecache
* NetworkManager-wait-online.service loaded failed failed Network Manager Wait Online
```

13. Services, from systemctl list-unit-files

```
STATE UNIT FILES
enabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online auditd crond
dbus-broker firewalld getty@ insights-client-boot irqbalance kdump lvm2-monitor mdmonitor
microcode nis-domainname rhsmcertd rsyslog selinux-autorelabel-mark sshd sssd
systemd-boot-update systemd-network-generator tuned udisks2
enabled-runtime systemd-remount-fs
disabled blk-availability console-getty cpupower debug-shell dnf-system-upgrade hwloc-dump-hwdata
kvm_stat man-db-restart-cache-update nftables powertop rdisc rhcd rhsm rhsm-facts
rpmdb-rebuild selinux-check-proper-disable serial-getty@ sshd-keygen@
systemd-boot-check-no-failures systemd-pstore systemd-sysex
indirect sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo systemd-sysupdate
systemd-sysupdate-reboot
```

14. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-427.13.1.el9_4.x86_64
root=/dev/mapper/rhel00-root
ro
resume=/dev/mapper/rhel00-swap
rd.lvm.lv=rhel00/root
rd.lvm.lv=rhel00/swap
```

15. cpupower frequency-info

```
analyzing CPU 0:
Unable to determine current policy
boost state support:
Supported: yes
```

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Platform Notes (Continued)

Active: yes

16. tuned-adm active
Current active profile: throughput-performance

17. sysctl
kernel.numa_balancing 0
kernel.randomize_va_space 2
vm.compaction_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 10
vm.dirty_bytes 0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio 40
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold 500
vm.min_unmapped_ratio 1
vm.nr_hugepages 0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness 10
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode 0

18. /sys/kernel/mm/transparent_hugepage
defrag always defer defer+madvice [madvice] never
enabled [always] madvice never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force

19. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000

20. OS release
From /etc/*-release /etc/*-version
os-release Red Hat Enterprise Linux 9.4 (Plow)
redhat-release Red Hat Enterprise Linux release 9.4 (Plow)
system-release Red Hat Enterprise Linux release 9.4 (Plow)

21. Disk information
SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel00-home xfs 829G 55G 774G 7% /home

22. /sys/devices/virtual/dmi/id

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Platform Notes (Continued)

Vendor: HPE
Product: ProLiant MicroServer Gen11
Product Family: ProLiant
Serial: 91ZV86L0HM

23. dmidecode

Additional information from dmidecode 3.5 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

2x Hynix HMC88AGBEA084N 32 GB 2 rank 5600, configured at 4400

24. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: HPE
BIOS Version: 2.10
BIOS Date: 12/06/2024
BIOS Revision: 2.10
Firmware Revision: 1.67

Compiler Version Notes

C | 519.lbm_r(base, peak) 538.imagick_r(base, peak) 544.nab_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C++ | 508.namd_r(base, peak) 510.parest_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C++, C | 511.povray_r(base, peak) 526.blender_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

C++, C, Fortran | 507.cactuBSSN_r(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Compiler Version Notes (Continued)

Fortran | 503.bwaves_r(base, peak) 549.fotonik3d_r(base, peak) 554.roms_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
Copyright (C) 1985-2024 Intel Corporation. All rights reserved.

Fortran, C | 521.wrf_r(base, peak) 527.cam4_r(base, peak)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2024.1.0 Build 20240308
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Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

503.bwaves_r: -DSPEC_LP64
507.cactuBSSN_r: -DSPEC_LP64
508.namd_r: -DSPEC_LP64
510.parest_r: -DSPEC_LP64
511.povray_r: -DSPEC_LP64
519.lbm_r: -DSPEC_LP64
521.wrf_r: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian

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SPEC CPU®2017 Floating Point Rate Result

Copyright 2017-2025 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant MicroServer Gen11
(2.80 GHz, Intel Xeon 6315P)

SPECrate®2017_fp_base = 69.4

SPECrate®2017_fp_peak = 70.3

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Jan-2025
Hardware Availability: Mar-2025
Software Availability: Apr-2024

Base Portability Flags (Continued)

526.blender_r: -DSPEC_LP64 -DSPEC_LINUX -funsigned-char
527.cam4_r: -DSPEC_LP64 -DSPEC_CASE_FLAG
538.imagick_r: -DSPEC_LP64
544.nab_r: -DSPEC_LP64
549.fotonik3d_r: -DSPEC_LP64
554.roms_r: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-w -std=c11 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

C++ benchmarks:

-w -std=c++14 -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Fortran benchmarks:

-w -m64 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both Fortran and C:

-w -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-Wno-implicit-int -nostandard-realloc-lhs -align array32byte -auto
-ljemalloc -L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using both C and C++:

-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib

Benchmarks using Fortran, C, and C++:

-w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX2 -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -nostandard-realloc-lhs
-align array32byte -auto -ljemalloc -L/usr/local/jemalloc64-5.0.1/lib



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Peak Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using both C and C++:

icpx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

519.lbm_r: basepeak = yes

538.imagick_r: basepeak = yes

544.nab_r: basepeak = yes

C++ benchmarks:

508.namd_r: basepeak = yes

510.parest_r: basepeak = yes

Fortran benchmarks:

503.bwaves_r: basepeak = yes

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CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jan-2025

Hardware Availability: Mar-2025

Software Availability: Apr-2024

Peak Optimization Flags (Continued)

549.fotonik3d_r: basepeak = yes

554.roms_r: basepeak = yes

Benchmarks using both Fortran and C:

521.wrf_r: basepeak = yes

527.cam4_r: basepeak = yes

Benchmarks using both C and C++:

```
511.povray_r: -w -std=c++14 -m64 -std=c11 -Wl,-z,muldefs
-fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX2 -flto
-Ofast -ffast-math -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -Wno-implicit-int -ljemalloc
-L/usr/local/jemalloc64-5.0.1/lib
```

526.blender_r: basepeak = yes

Benchmarks using Fortran, C, and C++:

507.cactuBSSN_r: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-GNR-rev1.1.html>

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-GNR-rev1.1.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2024-official-linux64.xml>

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