



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)
(Tyrone Camarero SDA200A2N-212)
(3.10 GHz, AMD EPYC 9554)

SPECspeed®2017_fp_base = 356

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6802

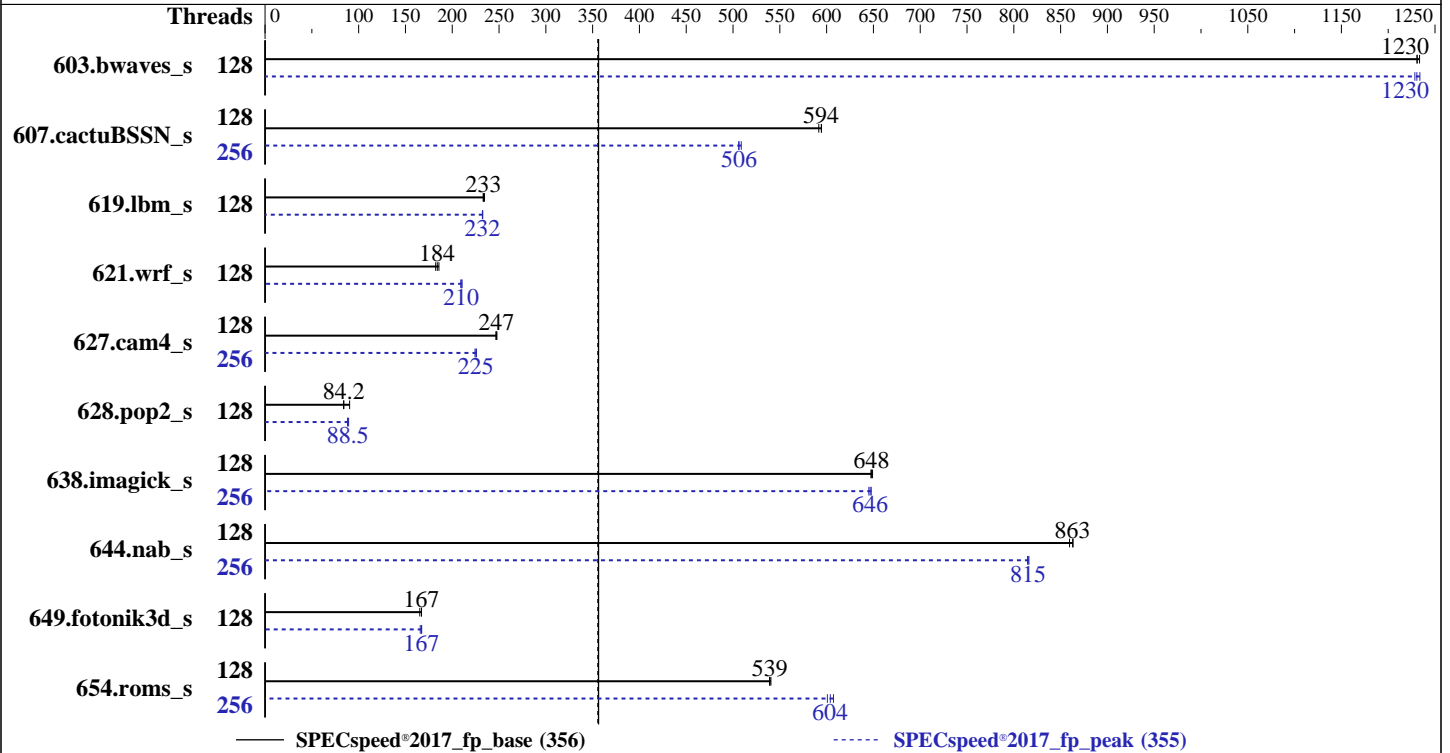
Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Aug-2024

Hardware Availability: Jun-2023

Software Availability: Aug-2024



Hardware

CPU Name: AMD EPYC 9554
 Max MHz: 3750
 Nominal: 3100
 Enabled: 256 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 1 MB I+D on chip per core
 L3: 256 MB I+D on chip per chip, 32 MB shared / 8 cores
 Other: None
 Memory: 1 TB (16 x 64 GB 2Rx4 PC5-4800B-R)
 Storage: 1 x 1 TB NVMe
 Other: CPU Cooling: Air

Software

OS: Ubuntu 20.04.4 LTS
 kernel version 5.15.0-119-generic
 Compiler: C/C++/Fortran: Version 4.0.0 of AOCC
 Parallel: Yes
 Firmware: Version 1.9a released Jun-2024
 File System: ext4
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: None
 Power Management: BIOS set to prefer performance at the cost of additional power usage.



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)
(Tyrone Camarero SDA200A2N-212)
(3.10 GHz, AMD EPYC 9554)

SPECSpeed®2017_fp_base = 356

SPECSpeed®2017_fp_peak = 355

CPU2017 License: 6802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Aug-2024

Hardware Availability: Jun-2023

Software Availability: Aug-2024

Results Table

Benchmark	Base						Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	128	<u>47.9</u>	<u>1230</u>	47.8	1230	47.9	1230	128	47.8	1230	<u>47.9</u>	<u>1230</u>	48.0	1230
607.cactuBSSN_s	128	28.2	592	28.0	594	<u>28.0</u>	<u>594</u>	256	<u>32.9</u>	<u>506</u>	32.8	509	32.9	506
619.lbm_s	128	22.5	233	22.3	234	<u>22.4</u>	<u>233</u>	128	22.6	232	<u>22.5</u>	<u>232</u>	22.5	233
621.wrf_s	128	<u>71.9</u>	<u>184</u>	72.6	182	71.2	186	128	62.8	211	63.2	209	<u>63.1</u>	<u>210</u>
627.cam4_s	128	35.8	248	35.9	247	<u>35.9</u>	<u>247</u>	256	39.5	224	39.2	226	<u>39.3</u>	<u>225</u>
628.pop2_s	128	131	90.4	142	83.9	<u>141</u>	<u>84.2</u>	128	<u>134</u>	<u>88.5</u>	133	89.3	135	88.1
638.imagick_s	128	<u>22.3</u>	<u>648</u>	22.2	649	22.3	647	256	22.4	645	22.3	648	<u>22.3</u>	<u>646</u>
644.nab_s	128	20.3	860	<u>20.2</u>	<u>863</u>	20.2	863	256	21.4	816	21.4	815	<u>21.4</u>	<u>815</u>
649.fotonik3d_s	128	<u>54.5</u>	<u>167</u>	54.5	167	55.1	165	128	<u>54.6</u>	<u>167</u>	55.0	166	54.5	167
654.roms_s	128	29.1	540	<u>29.2</u>	<u>539</u>	29.2	539	256	<u>26.1</u>	<u>604</u>	26.2	601	25.9	607

SPECSpeed®2017_fp_base = **356**

SPECSpeed®2017_fp_peak = **355**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at
<http://developer.amd.com/amd-aocc/>

Submit Notes

The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) only on request for base runs,
'echo madvise > /sys/kernel/mm/transparent_hugepage/enabled' run as root.
To enable THP for all allocations for peak runs,
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)
(Tyrone Camarero SDA200A2N-212)
(3.10 GHz, AMD EPYC 9554)

SPECspeed®2017_fp_base = 356

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Aug-2024

Hardware Availability: Jun-2023

Software Availability: Aug-2024

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
GOMP_CPU_AFFINITY = "0-255"
LD_LIBRARY_PATH = "/home/cpu2017/amd_speed_aocc400_znver4_A_lib/lib:"
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"
MALLOC_CONF = "oversize_threshold:0,retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "256"
```

Environment variables set by runcpu during the 603.bwaves_s peak run:

```
GOMP_CPU_AFFINITY = "0-127"
```

Environment variables set by runcpu during the 607.cactuBSSN_s peak run:

```
GOMP_CPU_AFFINITY = "0-255"
```

Environment variables set by runcpu during the 619.lbm_s peak run:

```
GOMP_CPU_AFFINITY = "0-127"
```

Environment variables set by runcpu during the 621.wrf_s peak run:

```
GOMP_CPU_AFFINITY = "0-127"
```

Environment variables set by runcpu during the 627.cam4_s peak run:

```
GOMP_CPU_AFFINITY = "0-255"
```

Environment variables set by runcpu during the 628.pop2_s peak run:

```
GOMP_CPU_AFFINITY = "0-127"
```

Environment variables set by runcpu during the 638.imagick_s peak run:

```
GOMP_CPU_AFFINITY = "0-255"
```

Environment variables set by runcpu during the 644.nab_s peak run:

```
GOMP_CPU_AFFINITY = "0-255"
```

Environment variables set by runcpu during the 649.fotonik3d_s peak run:

```
GOMP_CPU_AFFINITY = "0-127"
PGHPF_ZMEM = "yes"
```

Environment variables set by runcpu during the 654.roms_s peak run:

```
GOMP_CPU_AFFINITY = "0 128 1 129 2 130 3 131 4 132 5 133 6 134 7 135 8 136 9 137 10 138 11 139 12 140 13
141 14 142 15 143 16 144 17 145 18 146 19 147 20 148 21 149 22 150 23 151 24 152 25 153 26 154 27 155
28 156 29 157 30 158 31 159 32 160 33 161 34 162 35 163 36 164 37 165 38 166 39 167 40 168 41 169 42
170 43 171 44 172 45 173 46 174 47 175 48 176 49 177 50 178 51 179 52 180 53 181 54 182 55 183 56 184
57 185 58 186 59 187 60 188 61 189 62 190 63 191 64 192 65 193 66 194 67 195 68 196 69 197 70 198 71
199 72 200 73 201 74 202 75 203 76 204 77 205 78 206 79 207 80 208 81 209 82 210 83 211 84 212 85 213
86 214 87 215 88 216 89 217 90 218 91 219 92 220 93 221 94 222 95 223 96 224 97 225 98 226 99 227 100
228 101 229 102 230 103 231 104 232 105 233 106 234 107 235 108 236 109 237 110 238 111 239 112 240
113 241 114 242 115 243 116 244 117 245 118 246 119 247 120 248 121 249 122 250 123 251 124 252 125
253 126 254 127 255"
```

General Notes

Binaries were compiled on a system with 2x AMD EPYC 9174F CPU + 1.5TiB Memory using RHEL 8.6

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Tyrone Systems
(Test Sponsor: Netweb Technologies India Ltd)
(Tyrone Camarero SDA200A2N-212)
(3.10 GHz, AMD EPYC 9554)

SPECspeed®2017_fp_base = 356

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6802
Test Sponsor: Netweb Technologies India Ltd
Tested by: Tyrone Systems

Test Date: Aug-2024
Hardware Availability: Jun-2023
Software Availability: Aug-2024

General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Platform Notes

BIOS Settings:

cTDP: 400
Determinism Slider set to Power
Package Power: 400
EDC: 400
ACPI SRAT L3 Cache as NUMA Domain: enabled

Sysinfo program /home/cpu2017/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on amd2-Super-Server Sun Sep 1 12:38:11 2024

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 245 (245.4-4ubuntu3.20)
12. Failed units, from systemctl list-units --state=failed
13. Services, from systemctl list-unit-files
14. Linux kernel boot-time arguments, from /proc/cmdline
15. sysctl
16. /sys/kernel/mm/transparent_hugepage
17. /sys/kernel/mm/transparent_hugepage/khugepaged
18. OS release
19. Disk information
20. /sys/devices/virtual/dmi/id
21. dmidecode
22. BIOS

1. uname -a
Linux amd2-Super-Server 5.15.0-119-generic #129~20.04.1-Ubuntu SMP Wed Aug 7 13:07:13 UTC 2024 x86_64
x86_64 x86_64 GNU/Linux

2. w
12:38:11 up 1 day, 28 min, 1 user, load average: 6.28, 7.39, 4.35
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT
root tty1 - Sat11 3:22m 2.74s 1.36s /bin/bash ./amd_speed_aocc400_znver4_A1.sh

3. Username

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)
(Tyrone Camarero SDA200A2N-212)
(3.10 GHz, AMD EPYC 9554)

SPECspeed®2017_fp_base = 356

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Aug-2024

Hardware Availability: Jun-2023

Software Availability: Aug-2024

Platform Notes (Continued)

From environment variable \$USER: root

4. ulimit -a

```

time(seconds)          unlimited
file(blocks)           unlimited
data(kbytes)           unlimited
stack(kbytes)          unlimited
coredump(blocks)      0
memory(kbytes)         unlimited
locked memory(kbytes) 2097152
process                4126531
nofiles                1024
vmemory(kbytes)        unlimited
locks                  unlimited
rtprio                 0

```

5. sysinfo process ancestry

```

/sbin/init splash
/bin/login -p --
-bash
python3 ./run_amd_speed_aocc400_znver4_A1.py
/bin/bash ./amd_speed_aocc400_znver4_A1.sh
runcpu --config amd_speed_aocc400_znver4_A1.cfg --tune all --reportable --iterations 3 fpspeed
runcpu --configfile amd_speed_aocc400_znver4_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeed fpspeed --nopreenv --note-preenv --logfile
$SPEC/tmp/CPU2017.002/templogs/preenv.fpspeed.002.0.log --lognum 002.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017

```

6. /proc/cpuinfo

```

model name      : AMD EPYC 9554 64-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 25
model          : 17
stepping       : 1
microcode      : 0xa101148
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass rsro
TLB size      : 3584 4K pages
cpu cores     : 64
siblings      : 128
2 physical ids (chips)
256 processors (hardware threads)
physical id 0: core ids 0-63
physical id 1: core ids 0-63
physical id 0: apicids 0-127
physical id 1: apicids 128-255

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.34:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:             Little Endian
Address sizes:          52 bits physical, 57 bits virtual

```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)
(Tyrone Camarero SDA200A2N-212)
(3.10 GHz, AMD EPYC 9554)

SPECspeed®2017_fp_base = 356

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Aug-2024

Hardware Availability: Jun-2023

Software Availability: Aug-2024

Platform Notes (Continued)

```

CPU(s): 256
On-line CPU(s) list: 0-255
Thread(s) per core: 2
Core(s) per socket: 64
Socket(s): 2
NUMA node(s): 2
Vendor ID: AuthenticAMD
CPU family: 25
Model: 17
Model name: AMD EPYC 9554 64-Core Processor
Stepping: 1
Frequency boost: enabled
CPU MHz: 1500.000
CPU max MHz: 3762.9880
CPU min MHz: 1500.0000
BogoMIPS: 6190.41
Virtualization: AMD-V
L1d cache: 4 MiB
L1i cache: 4 MiB
L2 cache: 128 MiB
L3 cache: 512 MiB
NUMA node0 CPU(s): 0-63,128-191
NUMA node1 CPU(s): 64-127,192-255
Vulnerability Gather data sampling: Not affected
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf: Not affected
Vulnerability Mds: Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Reg file data sampling: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec rstack overflow: Mitigation; safe RET
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced / Automatic IBRS; IBPB conditional; STIBP
always-on; RSB filling; PBRSE-eIBRS Not affected; BHI Not affected

Vulnerability Srbds: Not affected
Vulnerability Tsx async abort: Not affected
Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat
pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb
rdtscp lm constant_tsc rep_good noopl nonstop_tsc cpuid extd_apicid
aperfmpperf rapl pni pclmulqdq monitor sse3 fma cx16 pcid sse4_1
sse4_2 x2apic movbe popcnt aes xsave avx f16c rdrand lahf_lm
cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext
perfctr_llc mwaitx cpb cat_l3 cdp_l3 invpcid_single hw_pstate ssbd
mba ibrs ibpb stibp ibrs_enhanced vmmcall fsgsbase bml avx2 smep
bmi2 erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb avx512cd sha_ni avx512bw avx512vl xsaveopt
xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local avx512_bf16 clzero irperf xsaveerptr rdpru wbnoinvd
amd_ppin cppc arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean
flushbyasid decodeassists pausefilter pthreshold avic
v_vmsave_vmload vgif v_spec_ctrl avx512vbmi umip pku ospke
avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg
avx512_vpopcntdq la57 rdpid overflow_recov succor smca fsrm flush_l1d

```

```

From lscpu --cache:
NAME ONE-SIZE ALL-SIZE WAYS TYPE LEVEL
L1d 32K 4M 8 Data 1

```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)
(Tyrone Camarero SDA200A2N-212)
(3.10 GHz, AMD EPYC 9554)

SPECspeed®2017_fp_base = 356

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Aug-2024

Hardware Availability: Jun-2023

Software Availability: Aug-2024

Platform Notes (Continued)

L1i	32K	4M	8 Instruction	1
L2	1M	128M	8 Unified	2
L3	32M	512M	16 Unified	3

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 2 nodes (0-1)
node 0 cpus: 0-63,128-191
node 0 size: 515712 MB
node 0 free: 513099 MB
node 1 cpus: 64-127,192-255
node 1 size: 516008 MB
node 1 free: 513792 MB
node distances:
node  0  1
  0: 10 32
  1: 32 10

```

9. /proc/meminfo

MemTotal: 1056481988 kB

10. who -r

run-level 3 Aug 31 11:59

11. Systemd service manager version: systemd 245 (245.4-4ubuntu3.20)

```

Default Target Status
multi-user      degraded

```

12. Failed units, from systemctl list-units --state=failed

UNIT	LOAD	ACTIVE	SUB	DESCRIPTION
* fwupd-refresh.service	loaded	failed	failed	Refresh fwupd metadata and update motd
* NetworkManager-wait-online.service	loaded	failed	failed	Network Manager Wait Online
* snapd.apparmor.service	loaded	failed	failed	Load AppArmor profiles managed internally by snapd

13. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	ModemManager NetworkManager NetworkManager-dispatcher NetworkManager-wait-online accounts-daemon anacron apparmor autovt@ avahi-daemon bluetooth console-setup cron cups cups-browsed dmesg e2scrub_reap getty@ gpu-manager grub-common grub-initrd-fallback irqbalance kerneloops keyboard-setup network-manager networkd-dispatcher ondemand openvpn pppd-dns rsync rsyslog secureboot-db setvtrgb snapd ssh sshd switcheroo-control syslog systemd-pstore systemd-resolved systemd-timesyncd thermald ua-reboot-cmds udisks2 ufw unattended-upgrades whoopsie wpa_supplicant
enabled-runtime	netplan-ovs-cleanup systemd-fsck-root systemd-remount-fs
disabled	acpid brltty console-getty debug-shell ipmievd openvpn-client@ openvpn-server@ openvpn@ rtkit-daemon serial-getty@ speech-dispatcher speech-dispatcherd systemd-boot-check-no-failures systemd-network-generator systemd-networkd systemd-networkd-wait-online systemd-time-wait-sync upower wpa_supplicant-nl80211@ wpa_supplicant-wired@ wpa_supplicant@
generated	apport ipmidrv openipmi
indirect	display-manager lightdm saned@ spice-vdagent spice-vdagentd uidd
masked	alsa-utils cryptdisks cryptdisks-early hwclock pulseaudio-enable-autospawn rc rcS saned sudo x11-common

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)
(Tyrone Camarero SDA200A2N-212)
(3.10 GHz, AMD EPYC 9554)

SPECspeed®2017_fp_base = 356

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Aug-2024

Hardware Availability: Jun-2023

Software Availability: Aug-2024

Platform Notes (Continued)

14. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=/boot/vmlinuz-5.15.0-119-generic
root=UUID=1ae71a13-cac0-48f6-b6e6-e15e5e687f57
ro
quiet
splash
vt.handoff=7
```

15. sysctl

```
kernel.numa_balancing 1
kernel.randomize_va_space 0
vm.compaction_proactiveness 20
vm.dirty_background_bytes 0
vm.dirty_background_ratio 10
vm.dirty_bytes 0
vm.dirty_expire_centisecs 3000
vm.dirty_ratio 8
vm.dirty_writeback_centisecs 500
vm.dirtytime_expire_seconds 43200
vm.extfrag_threshold 500
vm.min_unmapped_ratio 1
vm.nr_hugepages 0
vm.nr_hugepages_mempolicy 0
vm.nr_overcommit_hugepages 0
vm.swappiness 1
vm.watermark_boost_factor 15000
vm.watermark_scale_factor 10
vm.zone_reclaim_mode 1
```

16. /sys/kernel/mm/transparent_hugepage

```
defrag [always] defer defer+madvise madvise never
enabled [always] madvise never
hpage_pmd_size 2097152
shmem_enabled always within_size advise [never] deny force
```

17. /sys/kernel/mm/transparent_hugepage/khugepaged

```
alloc_sleep_millisecs 60000
defrag 1
max_ptes_none 511
max_ptes_shared 256
max_ptes_swap 64
pages_to_scan 4096
scan_sleep_millisecs 10000
```

18. OS release

```
From /etc/*-release /etc/*-version
os-release Ubuntu 20.04.4 LTS
```

19. Disk information

SPEC is set to: /home/cpu2017

```
Filesystem Type Size Used Avail Use% Mounted on
/dev/nvme0n1p2 ext4 938G 21G 870G 3% /
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Tyrone Systems
 (Test Sponsor: Netweb Technologies India Ltd)
 (Tyrone Camarero SDA200A2N-212)
 (3.10 GHz, AMD EPYC 9554)

SPECspeed®2017_fp_base = 356
SPECspeed®2017_fp_peak = 355

CPU2017 License: 6802
Test Sponsor: Netweb Technologies India Ltd
Tested by: Tyrone Systems

Test Date: Aug-2024
Hardware Availability: Jun-2023
Software Availability: Aug-2024

Platform Notes (Continued)

20. /sys/devices/virtual/dmi/id
 Vendor: Tyrone Systems
 Product: Super Server
 Product Family: SMC H13
 Serial: 0123456789

21. dmidecode
 Additional information from dmidecode 3.2 follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
 Memory:
 8x NO DIMM NO DIMM
 16x Samsung M321R8GA0BB0-CQKZJ 64 GB 2 rank 4800

22. BIOS
 (This section combines info from /sys/devices and dmidecode.)
 BIOS Vendor: American Megatrends International, LLC.
 BIOS Version: 1.9a
 BIOS Date: 06/21/2024
 BIOS Revision: 5.27

Compiler Version Notes

=====
 C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)
 =====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
 Target: x86_64-unknown-linux-gnu
 Thread model: posix
 InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
 =====

=====
 C++, C, Fortran | 607.cactuBSSN_s(base, peak)
 =====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
 Target: x86_64-unknown-linux-gnu
 Thread model: posix
 InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
 AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
 Target: x86_64-unknown-linux-gnu
 Thread model: posix
 InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
 AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
 Target: x86_64-unknown-linux-gnu
 Thread model: posix
 InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
 =====

=====
 Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)
 =====

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
 Target: x86_64-unknown-linux-gnu

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)
(Tyrone Camarero SDA200A2N-212)
(3.10 GHz, AMD EPYC 9554)

SPECspeed®2017_fp_base = 356

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Aug-2024

Hardware Availability: Jun-2023

Software Availability: Aug-2024

Compiler Version Notes (Continued)

Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)

AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin
AMD clang version 14.0.6 (CLANG: AOCC_4.0.0-Build#434 2022_10_28) (based on LLVM Mirror.Version.14.0.6)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc/aocc-compiler-4.0.0/bin

Base Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
627.cam4_s: -DSPEC_CASE_FLAG -DSPEC_LP64
628.pop2_s: -DSPEC_CASE_FLAG -Mbyteswapio -DSPEC_LP64
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)
(Tyrone Camarero SDA200A2N-212)
(3.10 GHz, AMD EPYC 9554)

SPECspeed®2017_fp_base = 356

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Aug-2024

Hardware Availability: Jun-2023

Software Availability: Aug-2024

Base Optimization Flags

C benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang
```

Fortran benchmarks:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -Mrecursive
-funroll-loops -mllvm -lsr-in-nested-loop
-mllvm -reduce-array-computations=3 -zopt -fopenmp=libomp -lomp
-lamdlibm -lamdalloc -lflang
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -O3 -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=7
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -fstrip-mining -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -mllvm -unroll-threshold=100 -finline-aggressive
-mllvm -loop-unswitch-threshold=200000 -Mrecursive -funroll-loops
-mllvm -lsr-in-nested-loop -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang
```



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)
(Tyrone Camarero SDA200A2N-212)
(3.10 GHz, AMD EPYC 9554)

SPECspeed®2017_fp_base = 356

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Aug-2024

Hardware Availability: Jun-2023

Software Availability: Aug-2024

Base Other Flags

C benchmarks:

-Wno-return-type -Wno-unused-command-line-argument

Fortran benchmarks:

-Wno-unused-command-line-argument

Benchmarks using both Fortran and C:

-Wno-return-type -Wno-unused-command-line-argument

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

Peak Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50
-fremap-arrays -fstrip-mining
-mllvm -inline-threshold=1000

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)
(Tyrone Camarero SDA200A2N-212)
(3.10 GHz, AMD EPYC 9554)

SPECspeed®2017_fp_base = 356

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Aug-2024

Hardware Availability: Jun-2023

Software Availability: Aug-2024

Peak Optimization Flags (Continued)

619.lbm_s (continued):

```
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

638.imagick_s: Same as 619.lbm_s

```
644.nab_s: -m64 -Wl,-mllvm -Wl,-region-vectorize -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

Fortran benchmarks:

```
603.bwaves_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP
-Ofast -march=znver4 -fveclib=AMDLIBM -ffast-math
-fopenmp -Mrecursive -mllvm -reduce-array-computations=3
-fvector-transform -fscalar-transform -fopenmp=libomp
-lomp -lamdlibm -lamdalloc -lflang
```

```
649.fotonik3d_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -DSPEC_OPENMP
-Ofast -march=znver4 -fveclib=AMDLIBM -ffast-math
-fopenmp -flto -Mrecursive
-mllvm -reduce-array-computations=3 -zopt -fopenmp=libomp
-lomp -lamdlibm -lamdalloc -lflang
```

654.roms_s: Same as 603.bwaves_s

Benchmarks using both Fortran and C:

```
621.wrf_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-O3 -Mrecursive -funroll-loops -mllvm -lsr-in-nested-loop
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)
(Tyrone Camarero SDA200A2N-212)
(3.10 GHz, AMD EPYC 9554)

SPECspeed®2017_fp_base = 356

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Aug-2024

Hardware Availability: Jun-2023

Software Availability: Aug-2024

Peak Optimization Flags (Continued)

```
627.cam4_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-Mrecursive -fopenmp=libomp -lomp -lamdlibm -lamdalloc
-lflang
```

```
628.pop2_s: -m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-enable-X86-prefetching -Ofast
-march=znver4 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=9 -mllvm -unroll-threshold=50
-freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -zopt
-Mrecursive -fvector-transform -fscalar-transform
-fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -Ofast -march=znver4
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=9
-mllvm -unroll-threshold=50 -freemap-arrays -fstrip-mining
-mllvm -inline-threshold=1000 -mllvm -reduce-array-computations=3
-DSPEC_OPENMP -zopt -finline-aggressive -mllvm -unroll-threshold=100
-Mrecursive -fopenmp=libomp -lomp -lamdlibm -lamdalloc -lflang
```

Peak Other Flags

C benchmarks:

```
-Wno-return-type -Wno-unused-command-line-argument
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument
```

Benchmarks using both Fortran and C:

```
-Wno-return-type -Wno-unused-command-line-argument
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Tyrone Systems

(Test Sponsor: Netweb Technologies India Ltd)
(Tyrone Camarero SDA200A2N-212)
(3.10 GHz, AMD EPYC 9554)

SPECspeed®2017_fp_base = 356

SPECspeed®2017_fp_peak = 355

CPU2017 License: 6802

Test Sponsor: Netweb Technologies India Ltd

Tested by: Tyrone Systems

Test Date: Aug-2024

Hardware Availability: Jun-2023

Software Availability: Aug-2024

Peak Other Flags (Continued)

Benchmarks using Fortran, C, and C++:

-Wno-return-type -Wno-unused-command-line-argument

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc400-flags.html>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-Genoa-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc400-flags.xml>

<http://www.spec.org/cpu2017/flags/Tyrone-Platform-Settings-V1.2-Genoa-revD.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2024-09-01 03:08:11-0400.

Report generated on 2024-09-25 09:16:22 by CPU2017 PDF formatter v6716.

Originally published on 2024-09-24.