



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2024 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR630 V3
(2.30 GHz, Intel Xeon Platinum 8568Y+)

SPECspeed®2017_int_base = 14.6

SPECspeed®2017_int_peak = Not Run

CPU2017 License: 9017

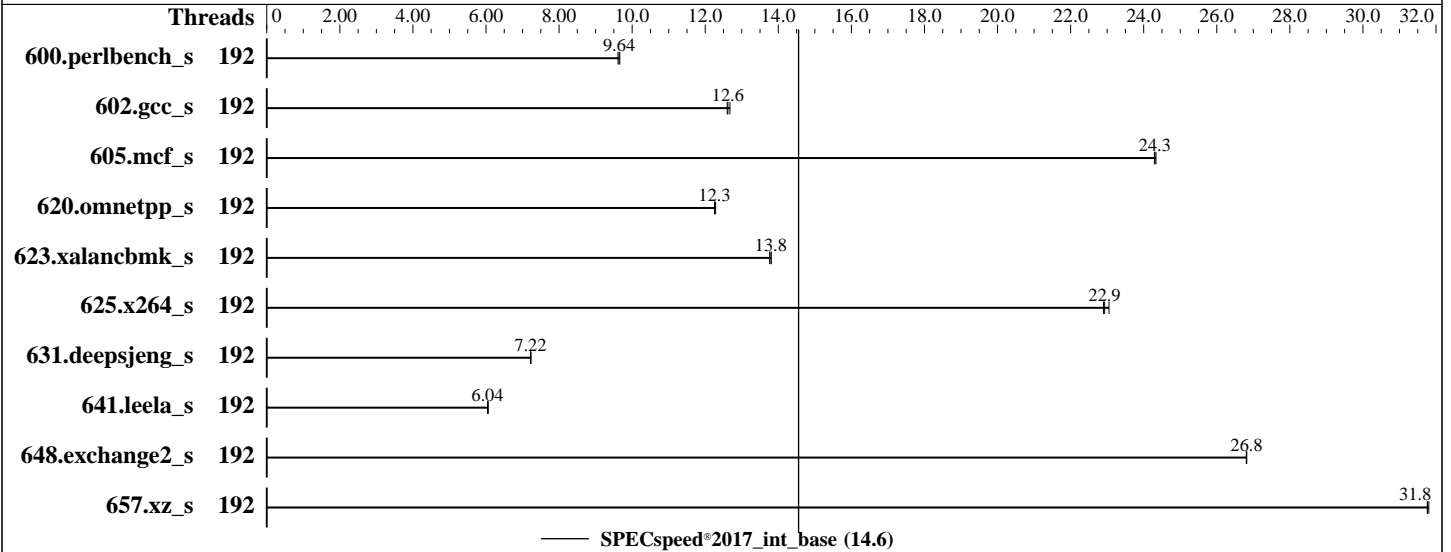
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Jan-2024

Hardware Availability: Feb-2024

Software Availability: Dec-2023



Hardware

CPU Name: Intel Xeon Platinum 8568Y+
 Max MHz: 4000
 Nominal: 2300
 Enabled: 96 cores, 2 chips, 2 threads/core
 Orderable: 1,2 chips
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 2 MB I+D on chip per core
 L3: 300 MB I+D on chip per chip
 Other: None
 Memory: 1 TB (16 x 64 GB 2Rx4 PC5-5600B-R)
 Storage: 1 x 960 GB SATA SSD
 Other: None

Software

OS: SUSE Linux Enterprise Server 15 SP4
 Kernel 5.14.21-150400.22-default
 Compiler: C/C++: Version 2023.2.3 of Intel oneAPI DPC++/C++ Compiler for Linux;
 Fortran: Version 2023.2.3 of Intel Fortran Compiler for Linux;
 Parallel: Yes
 Firmware: Lenovo BIOS Version ESE121V 3.10 released Jan-2024
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: Not Applicable
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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Results Table

Benchmark	Base								Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
600.perlbench_s	192	184	9.66	185	9.61	184	9.64									
602.gcc_s	192	314	12.7	315	12.6	316	12.6									
605.mcf_s	192	194	24.3	194	24.3	194	24.3									
620.omnetpp_s	192	133	12.3	133	12.3	133	12.3									
623.xalancbmk_s	192	103	13.8	103	13.8	103	13.8									
625.x264_s	192	76.5	23.1	76.9	22.9	77.0	22.9									
631.deepsjeng_s	192	198	7.22	198	7.23	198	7.22									
641.leela_s	192	282	6.04	281	6.06	282	6.04									
648.exchange2_s	192	110	26.8	110	26.8	110	26.8									
657.xz_s	192	194	31.8	195	31.8	195	31.8									

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Environment Variables Notes

Environment variables set by runcpu before the start of the run:

KMP_AFFINITY = "granularity=fine,scatter"

LD_LIBRARY_PATH =

"/home/cpu2017-1.1.9-ic2023.2.3/lib/intel164:/home/cpu2017-1.1.9-ic2023.2.3/lib/ia32:/home/cpu2017-1.1.9-ic2023.2.3/je5.0.1-64"

MALLOC_CONF = "retain:true"

OMP_STACKSIZE = "192M"

General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM

memory using Redhat Enterprise Linux 8.0

Transparent Huge Pages enabled by default

Prior to runcpu invocation

Filesystem page cache synced and cleared with:

sync; echo 3> /proc/sys/vm/drop_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>



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Platform Notes

BIOS configuration:

Choose Operating Mode set to Custom Mode
CPU P-State Control set to Cooperative with Legacy

Sysinfo program /home/cpu2017-1.1.9-ic2023.2.3/bin/sysinfo
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
running on test1 Tue Jan 30 17:30:49 2024

SUT (System Under Test) info as seen by some common utilities.

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- 1. uname -a
- 2. w
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- 11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
- 12. Services, from systemctl list-unit-files
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- 22. BIOS

```
1. uname -a
Linux test1 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222) x86_64
x86_64 x86_64 GNU/Linux
```

```
2. w
17:30:49 up 2 min, 1 user, load average: 2.38, 2.77, 1.20
USER      TTY      FROM          LOGIN@      IDLE        JCPU   PCPU   WHAT
root     tty1      -             17:29       7.00s      0.91s  0.00s  -bash
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size                (blocks, -f) unlimited
pending signals         (-i) 4126810
max locked memory       (kbytes, -l) 64
```

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Platform Notes (Continued)

```

max memory size      (kbytes, -m) unlimited
open files           (-n) 1024
pipe size            (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority   (-r) 0
stack size           (kbytes, -s) unlimited
cpu time             (seconds, -t) unlimited
max user processes   (-u) 4126810
virtual memory       (kbytes, -v) unlimited
file locks           (-x) unlimited

```

5. sysinfo process ancestry

```

/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
-runcpu --nobuild --action validate --define default-platform-flags -c
  ic2023.2.3-lin-sapphirerapids-speed-20231121.cfg --define cores=96 --tune base -o all --define
  intspeedaffinity --define smt-on --define drop_caches intspeed
-runcpu --nobuild --action validate --define default-platform-flags --configfile
  ic2023.2.3-lin-sapphirerapids-speed-20231121.cfg --define cores=96 --tune base --output_format all
  --define intspeedaffinity --define smt-on --define drop_caches --nopower --runmode speed --tune base
  --size refspeed intspeed --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.400/temlogs/preenv.intspeed.400.0.log --lognum 400.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-ic2023.2.3

```

6. /proc/cpuinfo

```

model name      : INTEL(R) XEON(R) PLATINUM 8568Y+
vendor_id      : GenuineIntel
cpu family     : 6
model          : 207
stepping       : 2
microcode      : 0x21000200
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores      : 48
siblings       : 96
2 physical ids (chips)
192 processors (hardware threads)
physical id 0: core ids 0-47
physical id 1: core ids 0-47
physical id 0: apicids 0-95
physical id 1: apicids 128-223

```

Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for virtualized systems. Use the above data carefully.

7. lscpu

From lscpu from util-linux 2.37.2:

```

Architecture:      x86_64
CPU op-mode(s):    32-bit, 64-bit
Address sizes:     46 bits physical, 57 bits virtual
Byte Order:        Little Endian
CPU(s):            192
On-line CPU(s) list: 0-191
Vendor ID:         GenuineIntel
Model name:        INTEL(R) XEON(R) PLATINUM 8568Y+

```

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Platform Notes (Continued)

```

CPU family:           6
Model:               207
Thread(s) per core:  2
Core(s) per socket:  48
Socket(s):           2
Stepping:            2
CPU max MHz:         4000.0000
CPU min MHz:         800.0000
BogoMIPS:            4600.00
Flags:               fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                    clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                    lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                    nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                    ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                    sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                    lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                    invpcid_single cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
                    vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 hle avx2 smep
                    bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
                    avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
                    xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
                    cqm_mbm_local avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts hwp
                    hwp_act_window hwp_epp hwp_pkg_req avx512vbmi umip pku ospke waitpkg
                    avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
                    avx512_vpoperntdq la57 rdpid bus_lock_detect cldemote movdiri movdir64b
                    enqcmd fsrm md_clear serialize tsxldtrk pconfig arch_lbr avx512_fp16
                    amx_tile flush_lld arch_capabilities
Virtualization:      VT-x
L1d cache:           4.5 MiB (96 instances)
L1i cache:           3 MiB (96 instances)
L2 cache:            192 MiB (96 instances)
L3 cache:            600 MiB (2 instances)
NUMA node(s):        2
NUMA node0 CPU(s):  0-47,96-143
NUMA node1 CPU(s):  48-95,144-191
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:  Not affected
Vulnerability Mds:   Not affected
Vulnerability Meltdown: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Srbds:  Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	4.5M	12	Data	1	64	1	64
L1i	32K	3M	8	Instruction	1	64	1	64
L2	2M	192M	16	Unified	2	2048	1	64
L3	300M	600M	20	Unified	3	245760	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```

available: 2 nodes (0-1)
node 0 cpus: 0-47,96-143
node 0 size: 515701 MB
node 0 free: 514172 MB
node 1 cpus: 48-95,144-191

```

(Continued on next page)



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Platform Notes (Continued)

```
node 1 size: 516025 MB
node 1 free: 514849 MB
node distances:
node  0  1
  0: 10 21
  1: 21 10
```

```
-----
9. /proc/meminfo
   MemTotal:      1056487768 kB
```

```
-----
10. who -r
    run-level 3 Jan 30 17:29
```

```
-----
11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
    Default Target  Status
    multi-user      running
```

```
-----
12. Services, from systemctl list-unit-files
    STATE          UNIT FILES
    enabled         YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ haveged irqbalance iscsi
                    issue-generator kbdsettings klog lvm2-monitor nscd postfix purge-kernels rollback rsyslog
                    smartd sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
    enabled-runtime systemd-remount-fs
    disabled        autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait
                    chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info
                    firewallld gpm grub2-once haveged-switch-root ipmi ipmievdev iscsi-init iscsid iscsiui
                    issue-add-ssh-keys kexec-load lunmask man-db-create multipathd nfs nfs-blkmap nmb rdisc
                    rpcbind rpmconfigcheck rsyncd serial-getty@ smartd_generate_opts smb snmpd snmptrapd
                    systemd-boot-check-no-failures systemd-network-generator systemd-sysext
                    systemd-time-wait-sync systemd-timesyncd
    generated       ntp_sync
    indirect        wickedd
```

```
-----
13. Linux kernel boot-time arguments, from /proc/cmdline
    BOOT_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default
    root=UUID=f976c541-a329-4c54-ba84-4be16556ee18
    splash=silent
    mitigations=auto
    quiet
    security=apparmor
```

```
-----
14. cpupower frequency-info
    analyzing CPU 0:
        current policy: frequency should be within 800 MHz and 4.00 GHz.
                        The governor "performance" may decide which speed to use
                        within this range.
    boost state support:
        Supported: yes
        Active: yes
```

```
-----
15. sysctl
    kernel.numa_balancing          1
    kernel.randomize_va_space     2
```

(Continued on next page)



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Platform Notes (Continued)

```

vm.compaction_proactiveness      20
vm.dirty_background_bytes        0
vm.dirty_background_ratio        10
vm.dirty_bytes                   0
vm.dirty_expire_centisecs       3000
vm.dirty_ratio                   20
vm.dirty_writeback_centisecs     500
vm.dirtytime_expire_seconds     43200
vm.extfrag_threshold             500
vm.min_unmapped_ratio           1
vm.nr_hugepages                  0
vm.nr_hugepages_mempolicy        0
vm.nr_overcommit_hugepages       0
vm.swappiness                     60
vm.watermark_boost_factor        15000
vm.watermark_scale_factor        10
vm.zone_reclaim_mode             0

```

```

-----
16. /sys/kernel/mm/transparent_hugepage
defrag          always defer+madvice [madvice] never
enabled         [always] madvice never
hpage_pmd_size 2097152
shmem_enabled   always within_size advise [never] deny force

```

```

-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000

```

```

-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP4

```

```

-----
19. Disk information
SPEC is set to: /home/cpu2017-1.1.9-ic2023.2.3
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda2       xfs   894G  232G  662G  26% /

```

```

-----
20. /sys/devices/virtual/dmi/id
Vendor:         Lenovo
Product:        ThinkSystem SR630 V3
Product Family: ThinkSystem
Serial:         1234567890

```

```

-----
21. dmidecode
Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:

```

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Platform Notes (Continued)

16x Samsung M321R8GA0PB0-CWMMKH 64 GB 2 rank 5600

22. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Lenovo
BIOS Version: ESE121V-3.10
BIOS Date: 01/09/2024
BIOS Revision: 3.10
Firmware Revision: 12.6

Compiler Version Notes

C | 600.perlbench_s(base) 602.gcc_s(base) 605.mcf_s(base) 625.x264_s(base) 657.xz_s(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

C++ | 620.omnetpp_s(base) 623.xalancbmk_s(base) 631.deepsjeng_s(base) 641.leela_s(base)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Fortran | 648.exchange2_s(base)

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.2.3 Build x
Copyright (C) 1985-2023 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifx

Base Portability Flags

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64

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Base Portability Flags (Continued)

```
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

Base Optimization Flags

C benchmarks:

```
-w -std=c11 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

C++ benchmarks:

```
-w -std=c++14 -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Fortran benchmarks:

```
-w -m64 -Wl,-z,muldefs -xsapphirerapids -O3 -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.html>
<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Eaglestream-AA.xml>
<http://www.spec.org/cpu2017/flags/Intel-ic2023p2-official-linux64.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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