



# SPEC CPU®2017 Floating Point Speed Result

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## Lenovo Global Technology

ThinkSystem SR655  
(2.00 GHz,AMD EPYC 7663P)

SPECspeed®2017\_fp\_base = 160

SPECspeed®2017\_fp\_peak = 162

CPU2017 License: 9017

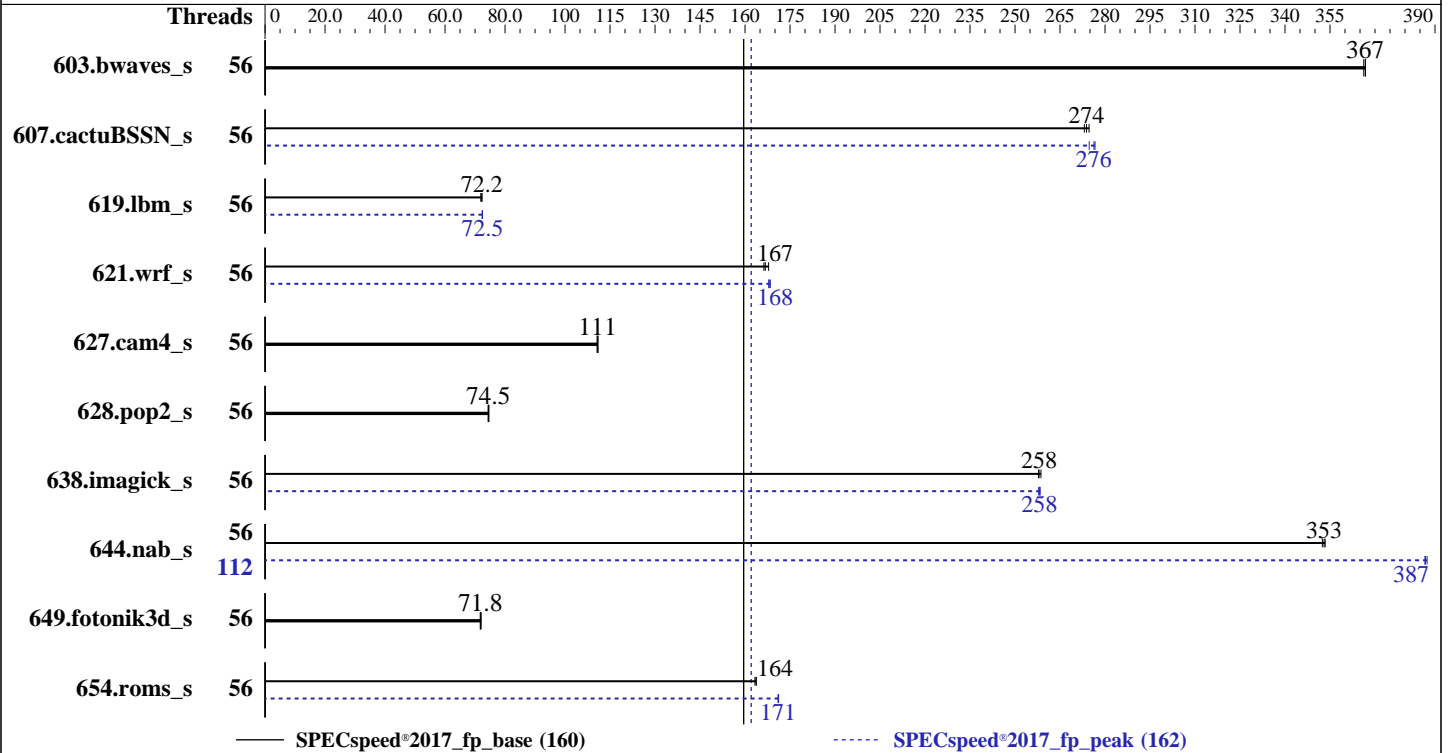
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2023

Hardware Availability: Oct-2023

Software Availability: Jun-2023



### Hardware

CPU Name: AMD EPYC 7663P  
 Max MHz: 3500  
 Nominal: 2000  
 Enabled: 56 cores, 1 chip, 2 threads/core  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 512 KB I+D on chip per core  
 L3: 256 MB I+D on chip per chip,  
 32 MB shared / 7 cores  
 Other: None  
 Memory: 256 GB (8 x 32 GB 2Rx4 PC4-3200AA-R)  
 Storage: 1 x 960 GB SATA SSD  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 15 SP5  
 Kernel 5.14.21-150500.53-default  
 Compiler: C/C++/Fortran: Version 3.2.0 of AOCC  
 Parallel: Yes  
 Firmware: Lenovo BIOS Version CFE137D 7.10 released Jun-2023  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc: jemalloc memory allocator library v5.1.0  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	56	161	367	<b>161</b>	<b>367</b>	161	366	56	161	367	<b>161</b>	<b>367</b>	161	366
607.cactuBSSN_s	56	61.0	273	<b>60.9</b>	<b>274</b>	60.7	275	56	60.3	277	<b>60.3</b>	<b>276</b>	60.7	275
619.lbm_s	56	72.8	71.9	<b>72.5</b>	<b>72.2</b>	72.4	72.3	56	72.4	72.4	72.3	72.5	<b>72.3</b>	<b>72.5</b>
621.wrf_s	56	79.5	166	78.8	168	<b>79.3</b>	<b>167</b>	56	78.8	168	78.5	168	<b>78.7</b>	<b>168</b>
627.cam4_s	56	79.9	111	80.1	111	<b>79.9</b>	<b>111</b>	56	79.9	111	80.1	111	<b>79.9</b>	<b>111</b>
628.pop2_s	56	159	74.6	160	74.4	<b>159</b>	<b>74.5</b>	56	159	74.6	160	74.4	<b>159</b>	<b>74.5</b>
638.imagick_s	56	56.0	258	<b>55.9</b>	<b>258</b>	55.8	259	56	<b>55.9</b>	<b>258</b>	55.8	258	55.9	258
644.nab_s	56	<b>49.5</b>	<b>353</b>	49.4	353	49.6	352	112	45.2	387	45.1	387	<b>45.2</b>	<b>387</b>
649.fotonik3d_s	56	127	71.8	<b>127</b>	<b>71.8</b>	127	72.0	56	127	71.8	<b>127</b>	<b>71.8</b>	127	72.0
654.roms_s	56	96.2	164	<b>96.2</b>	<b>164</b>	96.4	163	56	92.0	171	<b>92.0</b>	<b>171</b>	92.1	171

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,  
'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.  
To enable THP only on request for peak runs of 628.pop2\_s:  
'echo madvise > /sys/kernel/mm/transparent\_hugepage/enabled' run as root.  
To disable THP for peak runs of 627.cam4\_s, 649.fotonik3d\_s, and 654.roms\_s,  
'echo never > /sys/kernel/mm/transparent\_hugepage/enabled' run as root.



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### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
GOMP_CPU_AFFINITY = "0-111"
LD_LIBRARY_PATH =
"/home/cpu2017-1.1.9-amd-milanx-aocc320-A1/amd_speed_aocc320_milanx_A_lib/lib:/home/cpu2017-1.1.9-amd-
milanx-aocc320-A1/amd_speed_aocc320_milanx_A_lib/lib32:"
LIBOMP_NUM_HIDDEN_HELPER_THREADS = "0"
MALLOC_CONF = "retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "112"
```

Environment variables set by runcpu during the 607.cactuBSSN\_s peak run:

```
GOMP_CPU_AFFINITY = "0-55"
```

Environment variables set by runcpu during the 619.lbm\_s peak run:

```
GOMP_CPU_AFFINITY = "0-55"
```

Environment variables set by runcpu during the 621.wrf\_s peak run:

```
GOMP_CPU_AFFINITY = "0-55"
```

Environment variables set by runcpu during the 638.imagick\_s peak run:

```
GOMP_CPU_AFFINITY = "0-55"
```

Environment variables set by runcpu during the 644.nab\_s peak run:

```
GOMP_CPU_AFFINITY = "0 56 1 57 2 58 3 59 4 60 5 61 6 62 7 63 8 64 9 65 10 66 11 67 12 68 13 69 14 70 15 71
16 72 17 73 18 74 19 75 20 76 21 77 22 78 23 79 24 80 25 81 26 82 27 83 28 84 29 85 30 86 31 87 32 88
33 89 34 90 35 91 36 92 37 93 38 94 39 95 40 96 41 97 42 98 43 99 44 100 45 101 46 102 47 103 48 104
49 105 50 106 51 107 52 108 53 109 54 110 55 111"
```

Environment variables set by runcpu during the 654.roms\_s peak run:

```
GOMP_CPU_AFFINITY = "0-55"
```

### General Notes

Binaries were compiled on a system with 2x AMD EPYC 7742 CPU + 1TiB Memory using openSUSE 15.2

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v4.8.2 in RHEL 7.4 (No options specified)  
jemalloc 5.1.0 is available here:  
<https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2>

### Platform Notes

BIOS configuration:  
Choose Operating Mode set to Maximum Performance  
L1 Stream HW Prefetcher set to Disabled

Sysinfo program /home/cpu2017-1.1.9-amd-milanx-aocc320-A1/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197

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### Platform Notes (Continued)

running on localhost Thu Aug 10 11:35:02 2023

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

- 1. uname -a
- 2. w
- 3. Username
- 4. ulimit -a
- 5. sysinfo process ancestry
- 6. /proc/cpuinfo
- 7. lscpu
- 8. numactl --hardware
- 9. /proc/meminfo
- 10. who -r
- 11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)
- 12. Services, from systemctl list-unit-files
- 13. Linux kernel boot-time arguments, from /proc/cmdline
- 14. cpupower frequency-info
- 15. sysctl
- 16. /sys/kernel/mm/transparent\_hugepage
- 17. /sys/kernel/mm/transparent\_hugepage/khugepaged
- 18. OS release
- 19. Disk information
- 20. /sys/devices/virtual/dmi/id
- 21. dmidecode
- 22. BIOS

-----  
1. uname -a  
Linux localhost 5.14.21-150500.53-default #1 SMP PREEMPT\_DYNAMIC Wed May 10 07:56:26 UTC 2023 (b630043)  
x86\_64 x86\_64 x86\_64 GNU/Linux

-----  
2. w  
11:35:02 up 1 min, 1 user, load average: 0.13, 0.03, 0.01  
USER TTY FROM LOGIN@ IDLE JCPU PCPU WHAT  
root ttyl - 11:33 13.00s 1.26s 0.05s /bin/bash ./amd\_speed\_aocc320\_milanx\_A1.sh

-----  
3. Username  
From environment variable \$USER: root

-----  
4. ulimit -a  
core file size (blocks, -c) unlimited  
data seg size (kbytes, -d) unlimited  
scheduling priority (-e) 0  
file size (blocks, -f) unlimited  
pending signals (-i) 1025862  
max locked memory (kbytes, -l) 2097152  
max memory size (kbytes, -m) unlimited  
open files (-n) 1024  
pipe size (512 bytes, -p) 8  
POSIX message queues (bytes, -q) 819200  
real-time priority (-r) 0  
stack size (kbytes, -s) unlimited

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### Platform Notes (Continued)

```
cpu time                (seconds, -t) unlimited
max user processes      (-u) 1025862
virtual memory          (kbytes, -v) unlimited
file locks              (-x) unlimited
```

```
-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
login -- root
-bash
-bash
/bin/bash ./Run036-compliant-amd-speedfp.sh
python3 ./run_amd_speed_aocc320_milanx_A1.py
/bin/bash ./amd_speed_aocc320_milanx_A1.sh
runcpu --config amd_speed_aocc320_milanx_A1.cfg --tune all --reportable --iterations 3 fpspeed
runcpu --configfile amd_speed_aocc320_milanx_A1.cfg --tune all --reportable --iterations 3 --nopower
--runmode speed --tune base:peak --size test:train:refspeed fpspeed --nopreenv --note-preenv --logfile
  $SPEC/tmp/CPU2017.127/templots/preenv.fpspeed.127.0.log --lognum 127.0 --from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/cpu2017-1.1.9-amd-milanx-aocc320-A1
-----
```

```
-----
6. /proc/cpuinfo
model name      : AMD EPYC 7663P 56-Core Processor
vendor_id      : AuthenticAMD
cpu family     : 25
model          : 1
stepping       : 1
microcode      : 0xa0011ce
bugs           : sysret_ss_attrs spectre_v1 spectre_v2 spec_store_bypass
TLB size       : 2560 4K pages
cpu cores      : 56
siblings       : 112
1 physical ids (chips)
112 processors (hardware threads)
physical id 0: core ids 0-6,8-14,16-22,24-30,32-38,40-46,48-54,56-62
physical id 0: apicids 0-13,16-29,32-45,48-61,64-77,80-93,96-109,112-125
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
-----
```

```
-----
7. lscpu

From lscpu from util-linux 2.37.4:
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:         48 bits physical, 48 bits virtual
Byte Order:            Little Endian
CPU(s):                112
On-line CPU(s) list:  0-111
Vendor ID:             AuthenticAMD
Model name:            AMD EPYC 7663P 56-Core Processor
CPU family:            25
Model:                 1
Thread(s) per core:    2
Core(s) per socket:    56
Socket(s):             1
Stepping:              1
Frequency boost:       enabled
CPU max MHz:           3541.0149
```

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### Platform Notes (Continued)

```

CPU min MHz:          1500.0000
BogoMIPS:             3992.41
Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                    clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
                    constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf rapl
                    pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 movbe popcnt aes
                    xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a
                    misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core
                    perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3 invpcid_single
                    hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmi1 avx2 smep bmi2
                    invpcid cqm rdt_a rdseed adx smap clflushopt clwb sha_ni xsaveopt xsavec
                    xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local clzero
                    irperf xsaveerptr rdpru wbnoinvd amd_ppin brs arat npt lbrv svm_lock
                    nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter
                    pfthreshold v_vmsave_vmload vgif v_spec_ctrl umip pku ospke vaes
                    vpclmulqdq rdpid overflow_recov succor smca
Virtualization:      AMD-V
L1d cache:           1.8 MiB (56 instances)
L1i cache:           1.8 MiB (56 instances)
L2 cache:            28 MiB (56 instances)
L3 cache:            256 MiB (8 instances)
NUMA node(s):        1
NUMA node0 CPU(s):  0-111
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:  Not affected
Vulnerability Mds:   Not affected
Vulnerability Meltdown: Not affected
Vulnerability Mmio stale data: Not affected
Vulnerability Retbleed: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Retpolines, IBPB conditional, IBRS_FW, STIBP always-on, RSB
                    filling, PBR SB-eIBRS Not affected
Vulnerability Srbds:  Not affected
Vulnerability Tsx async abort: Not affected

```

```

From lscpu --cache:
NAME ONE-SIZE ALL-SIZE WAYS TYPE          LEVEL SETS PHY-LINE COHERENCY-SIZE
L1d   32K      1.8M    8 Data          1    64      1           64
L1i   32K      1.8M    8 Instruction   1    64      1           64
L2    512K     28M     8 Unified       2  1024     1           64
L3    32M     256M   16 Unified       3 32768     1           64

```

```

8. numactl --hardware
NOTE: a numactl 'node' might or might not correspond to a physical chip.
available: 1 nodes (0)
node 0 cpus: 0-111
node 0 size: 256495 MB
node 0 free: 255197 MB
node distances:
node 0
0: 10

```

```

9. /proc/meminfo
MemTotal:          262651472 kB

```

10. who -r

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### Platform Notes (Continued)

run-level 3 Aug 10 11:33

-----  
11. Systemd service manager version: systemd 249 (249.16+suse.171.gdad0071f15)  
Default Target Status  
multi-user running

-----  
12. Services, from systemctl list-unit-files  
STATE UNIT FILES  
enabled YaST2-Firstboot YaST2-Second-Stage apparmor auditd cron getty@ irqbalance issue-generator  
kbdsettings klog lvm2-monitor nscd postfix purge-kernels rollback rsyslog smartd sshd  
systemd-pstore wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny  
enabled-runtime systemd-remount-fs  
disabled autofs autoyast-initscripts blk-availability boot-sysctl ca-certificates chrony-wait  
chronyd console-getty cups cups-browsed debug-shell ebttables exchange-bmc-os-info  
firewalld gpm grub2-once haveged haveged-switch-root ipmi ipmievd issue-add-ssh-keys  
kexec-load lunmask man-db-create multipathd nfs nfs-blkmap rpcbind rpmconfigcheck rsyncd  
serial-getty@ smartd\_generate\_opts snmpd snmptrapd systemd-boot-check-no-failures  
systemd-network-generator systemd-sysexit systemd-time-wait-sync systemd-timesyncd  
indirect wickedd

-----  
13. Linux kernel boot-time arguments, from /proc/cmdline  
BOOT\_IMAGE=/boot/vmlinuz-5.14.21-150500.53-default  
root=UUID=24d1e11b-665b-4592-9918-8c0c6c3eb850  
splash=silent  
quiet  
security=apparmor  
mitigations=auto

-----  
14. cpupower frequency-info  
analyzing CPU 0:  
current policy: frequency should be within 1.50 GHz and 2.00 GHz.  
The governor "performance" may decide which speed to use  
within this range.  
boost state support:  
Supported: yes  
Active: yes

-----  
15. sysctl  
kernel.numa\_balancing 0  
kernel.randomize\_va\_space 0  
vm.compaction\_proactiveness 20  
vm.dirty\_background\_bytes 0  
vm.dirty\_background\_ratio 10  
vm.dirty\_bytes 0  
vm.dirty\_expire\_centisecs 3000  
vm.dirty\_ratio 8  
vm.dirty\_writeback\_centisecs 500  
vm.dirtytime\_expire\_seconds 43200  
vm.extfrag\_threshold 500  
vm.min\_unmapped\_ratio 1  
vm.nr\_hugepages 0  
vm.nr\_hugepages\_mempolicy 0  
vm.nr\_overcommit\_hugepages 0  
vm.swappiness 1  
vm.watermark\_boost\_factor 15000

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### Platform Notes (Continued)

```
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           1
```

```
-----
16. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvise [madvise] never
enabled         always [madvise] never
hpage_pmd_size 2097152
shmem_enabled   always within_size advise [never] deny force
-----
```

```
-----
17. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs  10000
-----
```

```
-----
18. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP5
-----
```

```
-----
19. Disk information
SPEC is set to: /home/cpu2017-1.1.9-amd-milanx-aocc320-A1
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda3 xfs 893G 26G 868G 3% /
-----
```

```
-----
20. /sys/devices/virtual/dmi/id
Vendor:      Lenovo
Product:     ThinkSystem SR655 -[7Y00000000]-
Product Family: ThinkSystem
Serial:      0123456789
-----
```

```
-----
21. dmidecode
Additional information from dmidecode 3.4 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
8x Samsung M393A4K40DB3-CWE 32 GB 2 rank 3200
-----
```

```
-----
22. BIOS
(This section combines info from /sys/devices and dmidecode.)
BIOS Vendor:      Lenovo
BIOS Version:     CFE137D
BIOS Date:        06/28/2023
BIOS Revision:    7.10
-----
```





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### Compiler Version Notes

=====  
C | 619.lbm\_s(base, peak) 638.imagick\_s(base, peak) 644.nab\_s(base, peak)  
=====

AMD clang version 13.0.0 (CLANG: AOCC\_3.2.0-Build#128 2021\_11\_12) (based on LLVM Mirror.Version.13.0.0)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin  
=====

=====  
C++, C, Fortran | 607.cactuBSSN\_s(base, peak)  
=====

AMD clang version 13.0.0 (CLANG: AOCC\_3.2.0-Build#128 2021\_11\_12) (based on LLVM Mirror.Version.13.0.0)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin  
AMD clang version 13.0.0 (CLANG: AOCC\_3.2.0-Build#128 2021\_11\_12) (based on LLVM Mirror.Version.13.0.0)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin  
AMD clang version 13.0.0 (CLANG: AOCC\_3.2.0-Build#128 2021\_11\_12) (based on LLVM Mirror.Version.13.0.0)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin  
=====

=====  
Fortran | 603.bwaves\_s(base, peak) 649.fotonik3d\_s(base, peak) 654.roms\_s(base, peak)  
=====

AMD clang version 13.0.0 (CLANG: AOCC\_3.2.0-Build#128 2021\_11\_12) (based on LLVM Mirror.Version.13.0.0)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin  
=====

=====  
Fortran, C | 621.wrf\_s(base, peak) 627.cam4\_s(base, peak) 628.pop2\_s(base, peak)  
=====

AMD clang version 13.0.0 (CLANG: AOCC\_3.2.0-Build#128 2021\_11\_12) (based on LLVM Mirror.Version.13.0.0)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin  
AMD clang version 13.0.0 (CLANG: AOCC\_3.2.0-Build#128 2021\_11\_12) (based on LLVM Mirror.Version.13.0.0)  
Target: x86\_64-unknown-linux-gnu  
Thread model: posix  
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin  
=====

### Base Compiler Invocation

C benchmarks:  
clang

Fortran benchmarks:  
flang

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# SPEC CPU®2017 Floating Point Speed Result

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Lenovo Global Technology

SPECspeed®2017\_fp\_base = 160

ThinkSystem SR655  
(2.00 GHz,AMD EPYC 7663P)

SPECspeed®2017\_fp\_peak = 162

CPU2017 License: 9017

Test Date: Aug-2023

Test Sponsor: Lenovo Global Technology

Hardware Availability: Oct-2023

Tested by: Lenovo Global Technology

Software Availability: Jun-2023

## Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64  
607.cactuBSSN\_s: -DSPEC\_LP64  
619.lbm\_s: -DSPEC\_LP64  
621.wrf\_s: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
627.cam4\_s: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
628.pop2\_s: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
638.imagick\_s: -DSPEC\_LP64  
644.nab\_s: -DSPEC\_LP64  
649.fotonik3d\_s: -DSPEC\_LP64  
654.roms\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-region-vectorize  
-Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3  
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=5  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000  
-fremap-arrays -mllvm -function-specialize -flv-function-specialization  
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true  
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3 -z muldefs  
-DSPEC\_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-enable-X86-prefetching  
-Wl,-mllvm -Wl,-enable-licm-vrp -Wl,-mllvm -Wl,-region-vectorize  
-Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Hz,1,0x1 -O3  
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp -Mrecursive  
-mllvm -fuse-tile-inner-loop -funroll-loops  
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop

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## Lenovo Global Technology

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**CPU2017 License:** 9017

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**Tested by:** Lenovo Global Technology

**Test Date:** Aug-2023

**Hardware Availability:** Oct-2023

**Software Availability:** Jun-2023

## Base Optimization Flags (Continued)

Fortran benchmarks (continued):

```
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-loopinterchange
-mllvm -compute-interchange-order -z muldefs -DSPEC_OPENMP
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=5
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3 -Hz,1,0x1
-Mrecursive -mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop
-mllvm -enable-loopinterchange -mllvm -compute-interchange-order
-z muldefs -DSPEC_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc
-lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=5
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-partial-unswitch -mllvm -unroll-threshold=100
-finline-aggressive -mllvm -loop-unswitch-threshold=200000
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -extra-vectorizer-passes -mllvm -convert-pow-exp-to-int=false
-Hz,1,0x1 -Mrecursive -mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -enable-loopinterchange
-mllvm -compute-interchange-order -z muldefs -DSPEC_OPENMP
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
```



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**Tested by:** Lenovo Global Technology

**Test Date:** Aug-2023

**Hardware Availability:** Oct-2023

**Software Availability:** Jun-2023

## Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument -Wno-return-type

Fortran benchmarks:

-Wno-unused-command-line-argument -Wno-return-type

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument -Wno-return-type

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument -Wno-return-type

## Peak Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

619.lbm\_s: -m64 -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp  
-flto -fstruct-layout=5 -mllvm -unroll-threshold=50  
-fremap-arrays -flv-function-specialization

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Tested by: Lenovo Global Technology

Software Availability: Jun-2023

## Peak Optimization Flags (Continued)

619.lbm\_s (continued):

```
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
```

638.imagick\_s: -m64 -Wl,-allow-multiple-definition

```
-Wl,-mllvm -Wl,-enable-licm-vrp
-Wl,-mllvm -Wl,-do-block-reorder=aggressive
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=5 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -fremap-arrays
-mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -do-block-reorder=aggressive -DSPEC_OPENMP
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
```

644.nab\_s: Same as 638.imagick\_s

Fortran benchmarks:

603.bwaves\_s: basepeak = yes

649.fotonik3d\_s: basepeak = yes

654.roms\_s: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching

```
-Wl,-mllvm -Wl,-enable-licm-vrp
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp
-Mrecursive -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-licm-vrp
-DSPEC_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc
-lflang
```

Benchmarks using both Fortran and C:

621.wrf\_s: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching

```
-Wl,-mllvm -Wl,-enable-licm-vrp
```

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Software Availability: Jun-2023

## Peak Optimization Flags (Continued)

621.wrf\_s (continued):

```

-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=5 -mllvm -unroll-threshold=50
-freemap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -Hz,1,0x1 -Mrecursive
-mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop
-mllvm -enable-loopinterchange
-mllvm -compute-interchange-order -DSPEC_OPENMP
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang

```

627.cam4\_s: basepeak = yes

628.pop2\_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

```

-m64 -Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-enable-licm-vrp
-Wl,-mllvm -Wl,-do-block-reorder=aggressive
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast -march=znver3
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=5
-mllvm -unroll-threshold=50 -freemap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true -mllvm -function-specialize
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-fininline-aggressive -mllvm -unroll-threshold=100 -mllvm -reroll-loops
-mllvm -aggressive-loop-unswitch -Mrecursive
-mllvm -do-block-reorder=aggressive -DSPEC_OPENMP -fopenmp=libomp
-lomp -lamdlibm -ljemalloc -lflang

```

## Peak Other Flags

C benchmarks:

```

-Wno-unused-command-line-argument -Wno-return-type

```

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## Peak Other Flags (Continued)

Fortran benchmarks:

-Wno-unused-command-line-argument -Wno-return-type

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument -Wno-return-type

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument -Wno-return-type

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Genoa-T.html>

<http://www.spec.org/cpu2017/flags/aocc320-flags-A1.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-Genoa-T.xml>

<http://www.spec.org/cpu2017/flags/aocc320-flags-A1.xml>

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