



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL320 Gen11

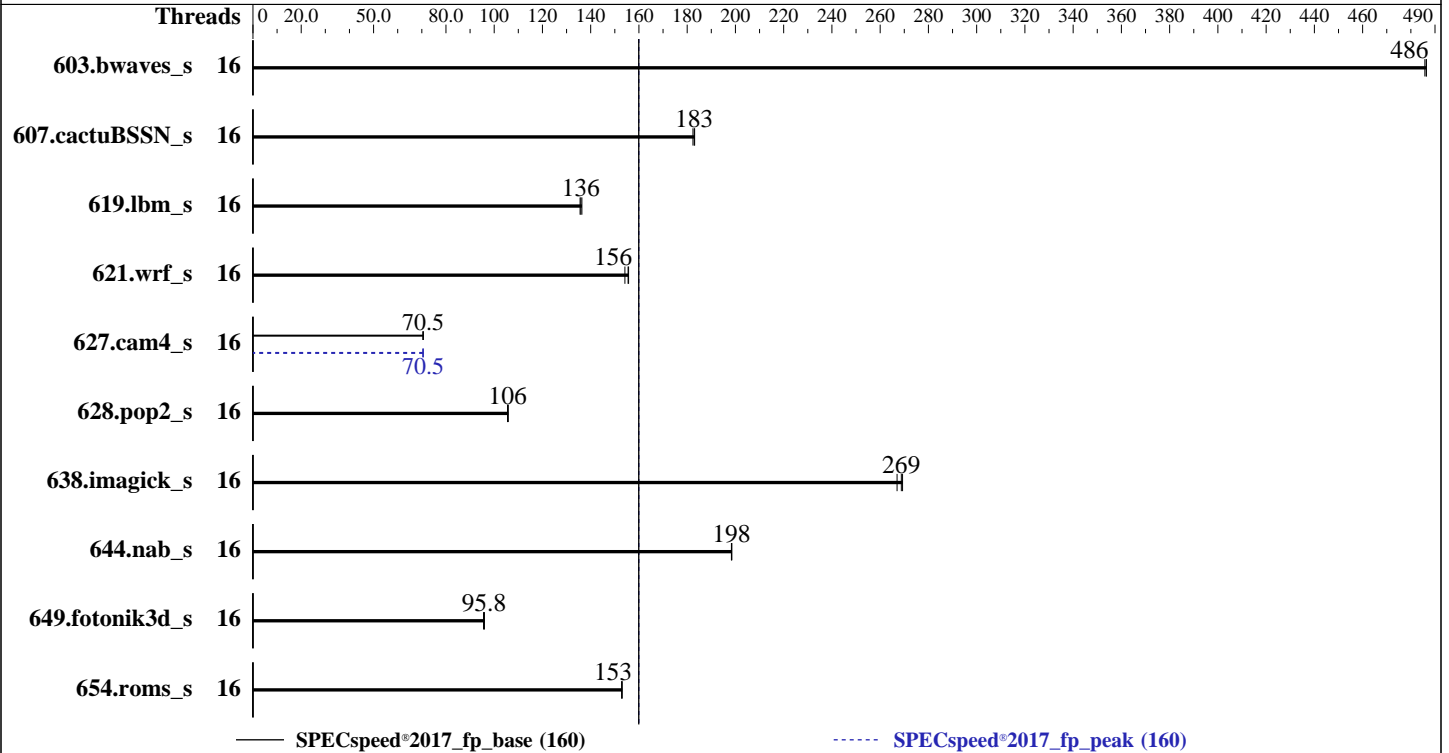
(2.50 GHz, Intel Xeon Gold 6426Y)

SPECspeed®2017_fp_base = 160

SPECspeed®2017_fp_peak = 160

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Jun-2023
Hardware Availability: May-2023
Software Availability: Dec-2022



Hardware

CPU Name: Intel Xeon Gold 6426Y
Max MHz: 4100
Nominal: 2500
Enabled: 16 cores, 1 chip
Orderable: 1 Chip
Cache L1: 32 KB I + 48 KB D on chip per core
L2: 2 MB I+D on chip per core
L3: 37.5 MB I+D on chip per chip
Other: None
Memory: 256 GB (8 x 32 GB 2Rx8 PC5-4800B-R)
Storage: 1 x 480 GB SATA SSD
Other: None

Software

OS: Red Hat Enterprise Linux 9.0 (Plow)
Kernel 5.14.0-70.13.1.el9_0.x86_64
Compiler: C/C++: Version 2023.0 of Intel oneAPI DPC++/C++ Compiler for Linux;
Fortran: Version 2023.0 of Intel Fortran Compiler for Linux;
C/C++: Version 2023.0 of Intel C/C++ Compiler for Linux
Parallel: Yes
Firmware: HPE BIOS Version v1.40 05/18/2023 released May-2023
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc memory allocator V5.0.1
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL320 Gen11

(2.50 GHz, Intel Xeon Gold 6426Y)

SPECspeed®2017_fp_base = 160

SPECspeed®2017_fp_peak = 160

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Jun-2023
Hardware Availability: May-2023
Software Availability: Dec-2022

Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	16	121	486	121	486	121	486	16	121	486	121	486	121	486
607.cactuBSSN_s	16	91.1	183	91.0	183	91.4	182	16	91.1	183	91.0	183	91.4	182
619.lbm_s	16	38.4	136	38.5	136	38.6	136	16	38.4	136	38.5	136	38.6	136
621.wrf_s	16	85.8	154	84.9	156	85.1	156	16	85.8	154	84.9	156	85.1	156
627.cam4_s	16	126	70.6	126	70.5	126	70.5	16	126	70.6	126	70.5	126	70.5
628.pop2_s	16	112	106	112	106	113	106	16	112	106	112	106	113	106
638.imagick_s	16	53.7	269	53.6	269	54.0	267	16	53.7	269	53.6	269	54.0	267
644.nab_s	16	88.1	198	88.0	199	88.1	198	16	88.1	198	88.0	199	88.1	198
649.fotonik3d_s	16	95.4	95.6	95.2	95.8	95.0	95.9	16	95.4	95.6	95.2	95.8	95.0	95.9
654.roms_s	16	103	153	103	153	103	153	16	103	153	103	153	103	153

SPECspeed®2017_fp_base = 160

SPECspeed®2017_fp_peak = 160

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
IRQ balance service was stopped using "systemctl stop irqbalance.service"
tuned-adm profile was set to Throughput-Performance using "tuned-adm profile throughput-performance"
perf-bias for all the CPUs is set using "cpupower set -b 0"
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/je5.0.1-64"
MALLOCCONF = "retain:true"
OMP_STACKSIZE = "192M"
```

General Notes

```
Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM
memory using Redhat Enterprise Linux 8.0
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
sources available from jemalloc.net or https://github.com/jemalloc/jemalloc/releases
```



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL320 Gen11

(2.50 GHz, Intel Xeon Gold 6426Y)

SPECspeed®2017_fp_base = 160

SPECspeed®2017_fp_peak = 160

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2023

Hardware Availability: May-2023

Software Availability: Dec-2022

Platform Notes

The system ROM used for this result contains Intel microcode version 0x2b0004a1 for the Intel Xeon Gold 6426Y processor.

BIOS Configuration:

Workload Profile set to General Peak Frequency Compute
 Thermal Configuration set to Maximum Cooling
 Intel Hyper-Threading set to Disabled
 Memory Patrol Scrubbing set to Disabled
 Last Level Cache (LLC) Prefetch set to Enabled
 Last Level Cache (LLC) Dead Line Allocation set to Disabled
 Enhanced Processor Performance Profile set to Aggressive
 Dead Block Predictor set to Enabled
 Workload Profile set to Custom
 Intel DMI Link Frequency set to Gen2 Speed
 Adjacent Sector Prefetch set to Disabled
 Minimum Processor Idle Power Package C-State set to No Package State

Sysinfo program /home/CPU2017/bin/sysinfo
 Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197
 running on localhost.localdomain Thu Jun 8 09:42:23 2023

SUT (System Under Test) info as seen by some common utilities.

Table of contents

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo
10. who -r
11. Systemd service manager version: systemd 250 (250-6.el9_0)
12. Services, from systemctl list-unit-files
13. Linux kernel boot-time arguments, from /proc/cmdline
14. cpupower frequency-info
15. tuned-adm active
16. sysctl
17. /sys/kernel/mm/transparent_hugepage
18. /sys/kernel/mm/transparent_hugepage/khugepaged
19. OS release
20. Disk information
21. /sys/devices/virtual/dmi/id
22. dmidecode
23. BIOS

 1. uname -a
 Linux localhost.localdomain 5.14.0-70.13.1.el9_0.x86_64 #1 SMP PREEMPT Thu Apr 14 12:42:38 EDT 2022 x86_64
 x86_64 x86_64 GNU/Linux

 2. w
 09:42:23 up 3 min, 0 users, load average: 0.08, 0.03, 0.00
 USER TTY LOGIN@ IDLE JCPU PCPU WHAT

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL320 Gen11

(2.50 GHz, Intel Xeon Gold 6426Y)

SPECspeed®2017_fp_base = 160

SPECspeed®2017_fp_peak = 160

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Jun-2023
Hardware Availability: May-2023
Software Availability: Dec-2022

Platform Notes (Continued)

3. Username

From environment variable \$USER: root

4. ulimit -a

```
real-time non-blocking time (microseconds, -R) unlimited
core file size (blocks, -c) 0
data seg size (kbytes, -d) unlimited
scheduling priority (-e) 0
file size (blocks, -f) unlimited
pending signals (-i) 1029838
max locked memory (kbytes, -l) 64
max memory size (kbytes, -m) unlimited
open files (-n) 1024
pipe size (512 bytes, -p) 8
POSIX message queues (bytes, -q) 819200
real-time priority (-r) 0
stack size (kbytes, -s) unlimited
cpu time (seconds, -t) unlimited
max user processes (-u) 1029838
virtual memory (kbytes, -v) unlimited
file locks (-x) unlimited
```

5. sysinfo process ancestry

```
/usr/lib/systemd/systemd --switched-root --system --deserialize 30
sshd: /usr/sbin/sshd -D [listener] 0 of 10-100 startups
sshd: root [priv]
sshd: root@notty
bash -c cd $SPEC/ && $SPEC/fpspeed.sh
runcpu --nobuild --action validate --define default-platform-flags -c
ic2023.0-lin-sapphirerapids-speed-20221201.cfg --define cores=16 --tune base,peak -o all --define
drop_caches fpspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
ic2023.0-lin-sapphirerapids-speed-20221201.cfg --define cores=16 --tune base,peak --output_format all
--define drop_caches --nopower --runmode speed --tune base:peak --size refspeed fpspeed --nopreenv
--note-preenv --logfile $SPEC/tmp/CPU2017.001/templogs/preenv.fpspeed.001.0.log --lognum 001.0
--from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /home/CPU2017
```

6. /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) Gold 6426Y
vendor_id      : GenuineIntel
cpu family     : 6
model          : 143
stepping       : 8
microcode     : 0x2b0004a1
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores      : 16
siblings       : 16
1 physical ids (chips)
16 processors (hardware threads)
physical id 0: core ids 0-15
physical id 0: apicids 0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
virtualized systems. Use the above data carefully.
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL320 Gen11

(2.50 GHz, Intel Xeon Gold 6426Y)

SPECspeed®2017_fp_base = 160

SPECspeed®2017_fp_peak = 160

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Jun-2023
Hardware Availability: May-2023
Software Availability: Dec-2022

Platform Notes (Continued)

7. lscpu

From lscpu from util-linux 2.37.4:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          46 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 16
On-line CPU(s) list:   0-15
Vendor ID:              GenuineIntel
BIOS Vendor ID:        Intel(R) Corporation
Model name:             Intel(R) Xeon(R) Gold 6426Y
BIOS Model name:       Intel(R) Xeon(R) Gold 6426Y
CPU family:             6
Model:                  143
Thread(s) per core:    1
Core(s) per socket:    16
Socket(s):              1
Stepping:               8
BogoMIPS:               5000.00
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
                        clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
                        lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
                        nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
                        ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1
                        sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand
                        lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3
                        invpcid_single cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow
                        vnmi flexpriority ept vpid ept_ad fsgsbase tsc_adjust bmi1 avx2 smep bmi2
                        erms invpcid cqm rdt_a avx512f avx512dq rdseed adx smap avx512ifma
                        clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl xsaveopt xsavec
                        xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
                        split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida arat pln pts
                        avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes vpclmulqdq
                        avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid bus_lock_detect
                        cldemote movdiri movdir64b enqcmd fsrm md_clear serialize tsxldtrk pconfig
                        arch_lbr avx512_fp16 amx_tile flush_lld arch_capabilities
Virtualization:         VT-x
L1d cache:              768 KiB (16 instances)
L1i cache:              512 KiB (16 instances)
L2 cache:               32 MiB (16 instances)
L3 cache:               37.5 MiB (1 instance)
NUMA node(s):          1
NUMA node0 CPU(s):     0-15
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:     Not affected
Vulnerability Mds:      Not affected
Vulnerability Meltdown: Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl
Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling
Vulnerability Srbds:    Not affected
Vulnerability Tsx async abort: Not affected

```

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	768K	12	Data	1	64	1	64
L1i	32K	512K	8	Instruction	1	64	1	64
L2	2M	32M	16	Unified	2	2048	1	64

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL320 Gen11

(2.50 GHz, Intel Xeon Gold 6426Y)

SPECspeed®2017_fp_base = 160

SPECspeed®2017_fp_peak = 160

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Jun-2023
Hardware Availability: May-2023
Software Availability: Dec-2022

Platform Notes (Continued)

L3 37.5M 37.5M 15 Unified 3 40960 1 64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 1 nodes (0)
node 0 cpus: 0-15
node 0 size: 257499 MB
node 0 free: 256721 MB
node distances:
node 0
0: 10
```

9. /proc/meminfo

```
MemTotal: 263679376 kB
```

10. who -r

```
run-level 3 Jun 8 09:38
```

11. Systemd service manager version: systemd 250 (250-6.el9_0)

```
Default Target Status
multi-user running
```

12. Services, from systemctl list-unit-files

```
STATE UNIT FILES
enabled NetworkManager NetworkManager-dispatcher NetworkManager-wait-online auditd chronyd crond
dbus-broker firewalld getty@ irqbalance kdump lvm2-monitor mdmonitor microcode
nis-domainname rhsmcertd rsyslog selinux-autorelabel-mark sshd sssd
systemd-network-generator tuned udisks2
enabled-runtime systemd-remount-fs
disabled blk-availability chronty-wait console-getty cpupower debug-shell kvm_stat
man-db-restart-cache-update nftables powertop rdisc rhsm rhsm-facts rpmdb-rebuild
serial-getty@ sshd-keygen@ systemd-boot-check-no-failures systemd-pstore systemd-sysext
indirect sssd-autofs sssd-kcm sssd-nss sssd-pac sssd-pam sssd-ssh sssd-sudo
```

13. Linux kernel boot-time arguments, from /proc/cmdline

```
BOOT_IMAGE=(hd0,gpt2)/vmlinuz-5.14.0-70.13.1.el9_0.x86_64
root=/dev/mapper/rhel-root
ro
resume=/dev/mapper/rhel-swap
rd.lvm.lv=rhel/root
rd.lvm.lv=rhel/swap
```

14. cpupower frequency-info

```
analyzing CPU 0:
Unable to determine current policy
boost state support:
Supported: yes
Active: yes
```

15. tuned-adm active

```
Current active profile: throughput-performance
```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL320 Gen11

(2.50 GHz, Intel Xeon Gold 6426Y)

SPECspeed®2017_fp_base = 160

SPECspeed®2017_fp_peak = 160

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2023

Hardware Availability: May-2023

Software Availability: Dec-2022

Platform Notes (Continued)

```

-----
16. sysctl
kernel.numa_balancing          0
kernel.randomize_va_space     2
vm.compaction_proactiveness    20
vm.dirty_background_bytes     0
vm.dirty_background_ratio     10
vm.dirty_bytes                 0
vm.dirty_expire_centisecs     3000
vm.dirty_ratio                 40
vm.dirty_writeback_centisecs   500
vm.dirtytime_expire_seconds   43200
vm.extfrag_threshold           500
vm.min_unmapped_ratio         1
vm.nr_hugepages                0
vm.nr_hugepages_mempolicy     0
vm.nr_overcommit_hugepages    0
vm.swappiness                   10
vm.watermark_boost_factor     15000
vm.watermark_scale_factor     10
vm.zone_reclaim_mode          0
-----
17. /sys/kernel/mm/transparent_hugepage
defrag          always defer+madvice [madvice] never
enabled         [always] madvice never
hpage_pmd_size 2097152
shmem_enabled   always within_size advise [never] deny force
-----
18. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                  1
max_ptes_none           511
max_ptes_shared         256
max_ptes_swap           64
pages_to_scan           4096
scan_sleep_millisecs   10000
-----
19. OS release
From /etc/*-release /etc/*-version
os-release      Red Hat Enterprise Linux 9.0 (Plow)
redhat-release  Red Hat Enterprise Linux release 9.0 (Plow)
system-release  Red Hat Enterprise Linux release 9.0 (Plow)
-----
20. Disk information
SPEC is set to: /home/CPU2017
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs      372G  181G  191G  49% /home
-----
21. /sys/devices/virtual/dmi/id
Vendor:          HPE
Product:         ProLiant DL320 Gen11
Product Family: ProLiant
Serial:          CNX2210H28
-----

```

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL320 Gen11

(2.50 GHz, Intel Xeon Gold 6426Y)

SPECspeed®2017_fp_base = 160

SPECspeed®2017_fp_peak = 160

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2023

Hardware Availability: May-2023

Software Availability: Dec-2022

Platform Notes (Continued)

22. dmidecode

Additional information from dmidecode 3.3 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

4x Hynix HMC88AEBRA168N 32 GB 2 rank 4800
4x Hynix HMC88MEBRA113N 32 GB 2 rank 4800

23. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: HPE
BIOS Version: 1.40
BIOS Date: 05/18/2023
BIOS Revision: 1.40
Firmware Revision: 1.20

Compiler Version Notes

=====
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
=====

=====
C++, C, Fortran | 607.cactuBSSN_s(base, peak)
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
=====

=====
Fortran | 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base, peak)
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
=====

=====
Fortran, C | 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2023.0.0 Build 20221201
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.
=====



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL320 Gen11

(2.50 GHz, Intel Xeon Gold 6426Y)

SPECspeed®2017_fp_base = 160

SPECspeed®2017_fp_peak = 160

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2023

Hardware Availability: May-2023

Software Availability: Dec-2022

Base Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Base Portability Flags

603.bwaves_s: -DSPEC_LP64

607.cactuBSSN_s: -DSPEC_LP64

619.ibm_s: -DSPEC_LP64

621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian

627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG

628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian

-assume byterecl

638.imagick_s: -DSPEC_LP64

644.nab_s: -DSPEC_LP64

649.fotonik3d_s: -DSPEC_LP64

654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-m64 -std=c11 -Wl,-z,muldefs -xsaphirerapids -Ofast -ffast-math

-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp

-DSPEC_OPENMP -Wno-implicit-int -mprefer-vector-width=512

-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Fortran benchmarks:

-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xsaphirerapids -Ofast

-ffast-math -flto -mfpmath=sse -funroll-loops

-qopt-mem-layout-trans=4 -fiopenmp -nostandard-realloc-lhs

-align array32byte -auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

Benchmarks using both Fortran and C:

-m64 -std=c11 -Wl,-z,muldefs -xsaphirerapids -Ofast -ffast-math

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL320 Gen11

(2.50 GHz, Intel Xeon Gold 6426Y)

SPECspeed®2017_fp_base = 160

SPECspeed®2017_fp_peak = 160

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Jun-2023
Hardware Availability: May-2023
Software Availability: Dec-2022

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C (continued):

```
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c++14 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP -Wno-implicit-int
-mprefer-vector-width=512 -nostandard-realloc-lhs -align array32byte
-auto -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Peak Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes

(Continued on next page)



SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL320 Gen11

(2.50 GHz, Intel Xeon Gold 6426Y)

SPECspeed®2017_fp_base = 160

SPECspeed®2017_fp_peak = 160

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2023

Hardware Availability: May-2023

Software Availability: Dec-2022

Peak Optimization Flags (Continued)

644.nab_s: basepeak = yes

Fortran benchmarks:

603.bwaves_s: basepeak = yes

649.fotonik3d_s: basepeak = yes

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf_s: basepeak = yes

627.cam4_s: -m64 -std=c11 -Wl,-z,muldefs -xsapphirerapids -Ofast
-ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -fiopenmp -DSPEC_OPENMP
-Wno-implicit-int -mprefer-vector-width=512
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

628.pop2_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-SPR-rev2.1.html>

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-SPR-rev2.1.xml>

<http://www.spec.org/cpu2017/flags/Intel-ic2023-official-linux64.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.9 on 2023-06-08 00:12:22-0400.

Report generated on 2023-07-05 11:05:43 by CPU2017 PDF formatter v6716.

Originally published on 2023-07-04.