



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2023 Standard Performance Evaluation Corporation

## Dell Inc.

SPECspeed®2017\_fp\_base = 277

### PowerEdge MX760c (Intel Xeon Platinum 8452Y)

SPECspeed®2017\_fp\_peak = 277

CPU2017 License: 6573

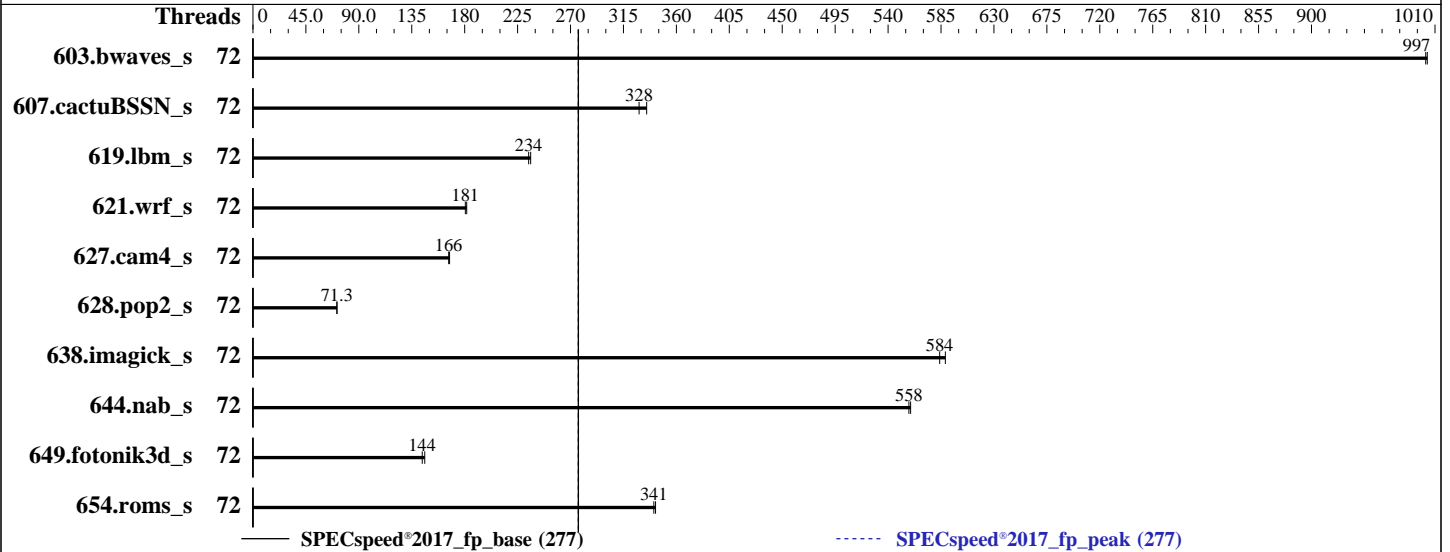
Test Date: Jan-2023

Test Sponsor: Dell Inc.

Hardware Availability: Feb-2023

Tested by: Dell Inc.

Software Availability: Jun-2022



### Hardware

CPU Name: Intel Xeon Platinum 8452Y  
 Max MHz: 3200  
 Nominal: 2000  
 Enabled: 72 cores, 2 chips  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 2 MB I+D on chip per core  
 L3: 67.5 MB I+D on chip per chip  
 Other: None  
 Memory: 1 TB (16 x 64 GB 2Rx4 PC5-4800B-R)  
 Storage: 125 GB on tmpfs  
 Other: None

### Software

OS: SUSE Linux Enterprise Server 15 SP4  
 5.14.21-150400.22-default  
 Compiler: C/C++: Version 2022.1 of Intel oneAPI DPC++/C++  
 Compiler for Linux;  
 Fortran: Version 2022.1 of Intel Fortran Compiler  
 for Linux;  
 Parallel: Yes  
 Firmware: Version 1.0.1 released Dec-2022  
 File System: tmpfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance  
 at the cost of additional power usage.



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## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	72	59.1	998	<u>59.2</u>	<u>997</u>			72	59.1	998	<u>59.2</u>	<u>997</u>		
607.cactuBSSN_s	72	<u>50.8</u>	<u>328</u>	49.8	335			72	<u>50.8</u>	<u>328</u>	49.8	335		
619.lbm_s	72	22.2	236	<u>22.3</u>	<u>234</u>			72	22.2	236	<u>22.3</u>	<u>234</u>		
621.wrf_s	72	<u>73.2</u>	<u>181</u>	72.8	182			72	<u>73.2</u>	<u>181</u>	72.8	182		
627.cam4_s	72	<u>53.3</u>	<u>166</u>	53.0	167			72	<u>53.3</u>	<u>166</u>	53.0	167		
628.pop2_s	72	<u>166</u>	<u>71.3</u>	166	71.5			72	<u>166</u>	<u>71.3</u>	166	71.5		
638.imagick_s	72	<u>24.7</u>	<u>584</u>	24.5	589			72	<u>24.7</u>	<u>584</u>	24.5	589		
644.nab_s	72	<u>31.3</u>	<u>558</u>	31.2	559			72	<u>31.3</u>	<u>558</u>	31.2	559		
649.fotonik3d_s	72	62.4	146	<u>63.3</u>	<u>144</u>			72	62.4	146	<u>63.3</u>	<u>144</u>		
654.roms_s	72	<u>46.2</u>	<u>341</u>	46.0	342			72	<u>46.2</u>	<u>341</u>	46.0	342		

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

```
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH =
  "/mnt/ramdisk/cpu2017-1.1.9-ic2022.1/lib/intel64:/mnt/ramdisk/cpu2017-1.1.9-ic2022.1/je5.0.1-64"
MALLOCONF = "retain:true"
OMP_STACKSIZE = "192M"
```

## General Notes

Binaries compiled on a system with 2x Intel Xeon Platinum 8280M CPU + 384GB RAM memory using Redhat Enterprise Linux 8.0  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches  
jemalloc, a general purpose malloc implementation built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

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## General Notes (Continued)

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

Benchmark run from a 125 GB ramdisk created with the cmd: "mount -t tmpfs -o size=125G tmpfs /mnt/ramdisk"

## Platform Notes

BIOS settings:

ADDDC Setting : Disabled  
DIMM Self Healing on  
Uncorrectable Memory Error : Disabled  
Virtualization Technology : Disabled  
Logical Processor : Disabled  
Sub NUMA Cluster : 2-way Clustering  
Optimizer Mode : Enabled  
  
System Profile : Custom  
CPU Power Management : Maximum Performance  
C1E : Disabled  
C States : Autonomous  
Memory Patrol Scrub : Disabled  
Energy Efficiency Policy : Performance  
PCI ASPM L1 Link  
Power Management : Disabled

Sysinfo program /mnt/ramdisk/cpu2017-1.1.9-ic2022.1/bin/sysinfo  
Rev: r6732 of 2022-11-07 fe91c89b7ed5c36ae2c92cc097bec197  
running on localhost Fri Jan 13 16:22:28 2023

SUT (System Under Test) info as seen by some common utilities.

-----  
Table of contents  
-----

1. uname -a
2. w
3. Username
4. ulimit -a
5. sysinfo process ancestry
6. /proc/cpuinfo
7. lscpu
8. numactl --hardware
9. /proc/meminfo

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## Platform Notes (Continued)

- 10. who -r
- 11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)
- 12. Services, from systemctl list-unit-files
- 13. Linux kernel boot-time arguments, from /proc/cmdline
- 14. sysctl
- 15. /sys/kernel/mm/transparent\_hugepage
- 16. /sys/kernel/mm/transparent\_hugepage/khugepaged
- 17. OS release
- 18. Disk information
- 19. /sys/devices/virtual/dmi/id
- 20. dmidecode
- 21. BIOS

```
1. uname -a
Linux localhost 5.14.21-150400.22-default #1 SMP PREEMPT_DYNAMIC Wed May 11 06:57:18 UTC 2022 (49db222)
x86_64 x86_64 x86_64 GNU/Linux
```

```
2. w
16:22:28 up 2:50, 1 user, load average: 5.54, 5.80, 3.41
USER      TTY      FROM          LOGIN@      IDLE        JCPU   PCPU   WHAT
root      tty1    -             13:41      2:41m    1.59s  0.00s /bin/bash ./dell-norun-specspeed.sh
--iterations 2 --output_format csv,html,pdf,txt --define Dell-BIOS-inc=Dell-BIOS_Xeon-4.inc
```

```
3. Username
From environment variable $USER: root
```

```
4. ulimit -a
core file size          (blocks, -c) unlimited
data seg size           (kbytes, -d) unlimited
scheduling priority     (-e) 0
file size                (blocks, -f) unlimited
pending signals         (-i) 4124615
max locked memory       (kbytes, -l) 64
max memory size         (kbytes, -m) unlimited
open files               (-n) 1024
pipe size                (512 bytes, -p) 8
POSIX message queues    (bytes, -q) 819200
real-time priority      (-r) 0
stack size               (kbytes, -s) unlimited
cpu time                 (seconds, -t) unlimited
max user processes      (-u) 4124615
virtual memory           (kbytes, -v) unlimited
```

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## Platform Notes (Continued)

file locks (-x) unlimited

```

-----
5. sysinfo process ancestry
/usr/lib/systemd/systemd --switched-root --system --deserialize 29
login -- root
-bash
/bin/bash ./DELL_speed.sh
/bin/bash ./dell-norun-main.sh speed
/bin/bash ./dell-norun-main.sh speed
/bin/bash ./dell-norun-specspeed.sh --iterations 2 --output_format csv,html,pdf,txt --define
Dell-BIOS-inc=Dell-BIOS_Xeon-4.inc
/bin/bash ./dell-norun-specspeed.sh --iterations 2 --output_format csv,html,pdf,txt --define
Dell-BIOS-inc=Dell-BIOS_Xeon-4.inc
runcpu --nobuild --action validate --define default-platform-flags -c
ic2022.1-lin-core-avx512-speed-20220316.cfg --define cores=72 --tune base,peak -o all --define drop_caches
--iterations 2 --output_format csv,html,pdf,txt --define Dell-BIOS-inc=Dell-BIOS_Xeon-4.inc fpspeed
runcpu --nobuild --action validate --define default-platform-flags --configfile
ic2022.1-lin-core-avx512-speed-20220316.cfg --define cores=72 --tune base,peak --output_format all
--define drop_caches --iterations 2 --output_format csv,html,pdf,txt --define
Dell-BIOS-inc=Dell-BIOS_Xeon-4.inc --nopower --runmode speed --tune base:peak --size refspeed fpspeed
--nopreenv --note-preenv --logfile $SPEC/tmp/CPU2017.002/temlogs/preenv.fpspeed.002.0.log --lognum 002.0
--from_runcpu 2
specperl $SPEC/bin/sysinfo
$SPEC = /mnt/ramdisk/cpu2017-1.1.9-ic2022.1
-----

```

```

-----
6. /proc/cpuinfo
model name      : Intel(R) Xeon(R) Platinum 8452Y
vendor_id      : GenuineIntel
cpu family     : 6
model          : 143
stepping       : 8
microcode      : 0x2b000161
bugs           : spectre_v1 spectre_v2 spec_store_bypass swapgs
cpu cores      : 36
siblings       : 36
2 physical ids (chips)
72 processors (hardware threads)
physical id 0: core ids 0-35
physical id 1: core ids 0-35
physical id 0: apicids
0,2,4,6,8,10,12,14,16,18,20,22,24,26,28,30,32,34,36,38,40,42,44,46,48,50,52,54,56,58,60,62,64,66,68,70
physical id 1: apicids
128,130,132,134,136,138,140,142,144,146,148,150,152,154,156,158,160,162,164,166,168,170,172,174,176,178,1
80,182,184,186,188,190,192,194,196,198
Caution: /proc/cpuinfo data regarding chips, cores, and threads is not necessarily reliable, especially for
-----

```

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## Platform Notes (Continued)

virtualized systems. Use the above data carefully.

### 7. lscpu

From lscpu from util-linux 2.37.2:

```

Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Address sizes:          46 bits physical, 57 bits virtual
Byte Order:             Little Endian
CPU(s):                 72
On-line CPU(s) list:   0-71
Vendor ID:              GenuineIntel
Model name:             Intel(R) Xeon(R) Platinum 8452Y
CPU family:             6
Model:                  143
Thread(s) per core:    1
Core(s) per socket:    36
Socket(s):              2
Stepping:               8
BogoMIPS:               4000.00

```

Flags:

```

fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36
clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology
nonstop_tsc cpuid aperfmperf tsc_known_freq pni pclmulqdq dtes64 monitor
ds_cpl smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2
x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm
abm 3dnowprefetch cpuid_fault epb cat_l3 cat_l2 cdp_l3 invpcid_single
cdp_l2 ssbd mba ibrs ibpb stibp ibrs_enhanced fsgsbase tsc_adjust bmi1 hle
avx2 smep bmi2 erms invpcid rtm cqm rdt_a avx512f avx512dq rdseed adx smap
avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw avx512vl
xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local split_lock_detect avx_vnni avx512_bf16 wbnoinvd dtherm ida
arat pln pts avx512vbmi umip pku ospke waitpkg avx512_vbmi2 gfni vaes
vpclmulqdq avx512_vnni avx512_bitalg tme avx512_vpopcntdq la57 rdpid
bus_lock_detect cldemote movdiri movdir64b enqcmd fsrm md_clear serialize
tsxldtrk pconfig arch_lbr avx512_fp16 amx_tile flush_l1d arch_capabilities

```

```

L1d cache:             3.4 MiB (72 instances)
L1i cache:             2.3 MiB (72 instances)
L2 cache:              144 MiB (72 instances)
L3 cache:              135 MiB (2 instances)
NUMA node(s):         4
NUMA node0 CPU(s):    0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,60,64,68
NUMA node1 CPU(s):    2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,62,66,70
NUMA node2 CPU(s):    1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61,65,69
NUMA node3 CPU(s):    3,7,11,15,19,23,27,31,35,39,43,47,51,55,59,63,67,71
Vulnerability Itlb multihit: Not affected

```

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## Platform Notes (Continued)

Vulnerability L1tf: Not affected  
 Vulnerability Mds: Not affected  
 Vulnerability Meltdown: Not affected  
 Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp  
 Vulnerability Spectre v1: Mitigation; usercopy/swapgs barriers and \_\_user pointer sanitization  
 Vulnerability Spectre v2: Mitigation; Enhanced IBRS, IBPB conditional, RSB filling  
 Vulnerability Srbds: Not affected  
 Vulnerability Tsx async abort: Not affected

From lscpu --cache:

NAME	ONE-SIZE	ALL-SIZE	WAYS	TYPE	LEVEL	SETS	PHY-LINE	COHERENCY-SIZE
L1d	48K	3.4M	12	Data	1	64	1	64
L1i	32K	2.3M	8	Instruction	1	64	1	64
L2	2M	144M	16	Unified	2	2048	1	64
L3	67.5M	135M	15	Unified	3	73728	1	64

8. numactl --hardware

NOTE: a numactl 'node' might or might not correspond to a physical chip.

```
available: 4 nodes (0-3)
node 0 cpus: 0,4,8,12,16,20,24,28,32,36,40,44,48,52,56,60,64,68
node 0 size: 257423 MB
node 0 free: 249162 MB
node 1 cpus: 2,6,10,14,18,22,26,30,34,38,42,46,50,54,58,62,66,70
node 1 size: 258043 MB
node 1 free: 257667 MB
node 2 cpus: 1,5,9,13,17,21,25,29,33,37,41,45,49,53,57,61,65,69
node 2 size: 258008 MB
node 2 free: 257864 MB
node 3 cpus: 3,7,11,15,19,23,27,31,35,39,43,47,51,55,59,63,67,71
node 3 size: 257700 MB
node 3 free: 250788 MB
node distances:
node  0  1  2  3
0:  10 12 21 21
1:  12 10 21 21
2:  21 21 10 12
3:  21 21 12 10
```

9. /proc/meminfo

MemTotal: 1055924084 kB

10. who -r

run-level 3 Jan 13 13:32

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## Platform Notes (Continued)

11. Systemd service manager version: systemd 249 (249.11+suse.124.g2bc0b2c447)

Default Target	Status
multi-user	running

12. Services, from systemctl list-unit-files

STATE	UNIT FILES
enabled	apparmor auditd cron firewalld getty@ haveged irqbalance issue-generator kbdsettings kdump kdump-early lvm2-monitor postfix purge-kernels rollback sshd wicked wickedd-auto4 wickedd-dhcp4 wickedd-dhcp6 wickedd-nanny
enabled-runtime	systemd-remount-fs
disabled	blk-availability boot-sysctl ca-certificates chrony-wait chronyd console-getty debug-shell ebtables grub2-once haveged-switch-root issue-add-ssh-keys kexec-load lunmask nfs nfs-blkmap rpcbind rpmconfigcheck rsyncd serial-getty@ systemd-boot-check-no-failures systemd-network-generator systemd-sysext systemd-time-wait-sync systemd-timesyncd
indirect	wickedd

13. Linux kernel boot-time arguments, from /proc/cmdline

BOOT\_IMAGE=/boot/vmlinuz-5.14.21-150400.22-default  
root=UUID=4068cb7a-41a3-4eea-ab49-bc6f15bea24c  
splash=silent  
mitigations=auto  
quiet  
security=apparmor  
crashkernel=314M,high  
crashkernel=72M,low

14. sysctl

kernel.numa_balancing	1
kernel.randomize_va_space	2
vm.compaction_proactiveness	20
vm.dirty_background_bytes	0
vm.dirty_background_ratio	10
vm.dirty_bytes	0
vm.dirty_expire_centisecs	3000
vm.dirty_ratio	20
vm.dirty_writeback_centisecs	500
vm.dirtytime_expire_seconds	43200
vm.extfrag_threshold	500
vm.min_unmapped_ratio	1
vm.nr_hugepages	0
vm.nr_hugepages_mempolicy	0
vm.nr_overcommit_hugepages	0
vm.swappiness	60

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## Platform Notes (Continued)

```

vm.watermark_boost_factor      15000
vm.watermark_scale_factor      10
vm.zone_reclaim_mode           0

```

```

-----
15. /sys/kernel/mm/transparent_hugepage
defrag          always defer defer+madvise [madvise] never
enabled         [always] madvise never
hpage_pmd_size  2097152
shmem_enabled   always within_size advise [never] deny force

```

```

-----
16. /sys/kernel/mm/transparent_hugepage/khugepaged
alloc_sleep_millisecs  60000
defrag                 1
max_ptes_none          511
max_ptes_shared        256
max_ptes_swap          64
pages_to_scan          4096
scan_sleep_millisecs   10000

```

```

-----
17. OS release
From /etc/*-release /etc/*-version
os-release SUSE Linux Enterprise Server 15 SP4

```

```

-----
18. Disk information
SPEC is set to: /mnt/ramdisk/cpu2017-1.1.9-ic2022.1
Filesystem      Type      Size  Used Avail Use% Mounted on
tmpfs           tmpfs    125G  9.6G  116G   8% /mnt/ramdisk

```

```

-----
19. /sys/devices/virtual/dmi/id
Vendor:          Dell Inc.
Product:         PowerEdge MX760c
Product Family: PowerEdge
Serial:          SL6M701

```

```

-----
20. dmidecode
Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you interpret this section.
The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately
determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the
"DMTF SMBIOS" standard.
Memory:
12x 002C00B3002C MTC40F2046S1RC48BA1 64 GB 2 rank 4800

```

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## Platform Notes (Continued)

1x 002C0632002C MTC40F2046S1RC48BA1 64 GB 2 rank 4800  
3x 002C069D002C MTC40F2046S1RC48BA1 64 GB 2 rank 4800

### 21. BIOS

(This section combines info from /sys/devices and dmidecode.)

BIOS Vendor: Dell Inc.  
BIOS Version: 1.0.1  
BIOS Date: 12/27/2022  
BIOS Revision: 1.0

## Compiler Version Notes

=====  
C | 619.lbm\_s(base, peak) 638.imagick\_s(base, peak)  
| 644.nab\_s(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2022.1.0 Build 20220316  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
=====

=====  
C++, C, Fortran | 607.cactuBSSN\_s(base, peak)  
=====

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2022.1.0 Build 20220316  
Copyright (C) 1985-2022 Intel Corporation. All rights reserved.  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2022.1.0 Build 20220316  
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Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version  
2022.1.0 Build 20220316  
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=====

=====  
Fortran | 603.bwaves\_s(base, peak) 649.fotonik3d\_s(base, peak)  
| 654.roms\_s(base, peak)  
=====

Intel(R) Fortran Compiler for applications running on Intel(R) 64, Version  
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# SPEC CPU®2017 Floating Point Speed Result

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Dell Inc.

SPECspeed®2017\_fp\_base = 277

PowerEdge MX760c (Intel Xeon Platinum 8452Y)

SPECspeed®2017\_fp\_peak = 277

CPU2017 License: 6573  
Test Sponsor: Dell Inc.  
Tested by: Dell Inc.

Test Date: Jan-2023  
Hardware Availability: Feb-2023  
Software Availability: Jun-2022

## Compiler Version Notes (Continued)

```

=====
Fortran, C      | 621.wrf_s(base, peak) 627.cam4_s(base, peak)
                | 628.pop2_s(base, peak)
=====

```

```

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```

## Base Compiler Invocation

C benchmarks:  
icx

Fortran benchmarks:  
ifx

Benchmarks using both Fortran and C:  
ifx icx

Benchmarks using Fortran, C, and C++:  
icpx icx ifx

## Base Portability Flags

```

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

```



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## Base Optimization Flags

C benchmarks:

```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Fortran benchmarks:

```
-m64 -Wl,-z,muldefs -DSPEC_OPENMP -xCORE-AVX512 -Ofast -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using both Fortran and C:

```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

Benchmarks using Fortran, C, and C++:

```
-m64 -std=c11 -Wl,-z,muldefs -xCORE-AVX512 -Ofast -ffast-math -flto
-mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4 -fiopenmp
-DSPEC_OPENMP -nostandard-realloc-lhs -align array32byte -auto
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

## Peak Compiler Invocation

C benchmarks:

icx

Fortran benchmarks:

ifx

Benchmarks using both Fortran and C:

ifx icx

Benchmarks using Fortran, C, and C++:

icpx icx ifx

## Peak Portability Flags

Same as Base Portability Flags



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SPECspeed®2017\_fp\_peak = 277

CPU2017 License: 6573

Test Sponsor: Dell Inc.

Tested by: Dell Inc.

Test Date: Jan-2023

Hardware Availability: Feb-2023

Software Availability: Jun-2022

## Peak Optimization Flags

C benchmarks:

619.lbm\_s: basepeak = yes

638.imagick\_s: basepeak = yes

644.nab\_s: basepeak = yes

Fortran benchmarks:

603.bwaves\_s: basepeak = yes

649.fotonik3d\_s: basepeak = yes

654.roms\_s: basepeak = yes

Benchmarks using both Fortran and C:

621.wrf\_s: basepeak = yes

627.cam4\_s: basepeak = yes

628.pop2\_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN\_s: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64-revB.html>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.3.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic2022-official-linux64-revB.xml>

<http://www.spec.org/cpu2017/flags/Dell-Platform-Flags-PowerEdge-Intel-Xeon-v1.3.xml>

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