



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR665  
3.05 GHz, AMD EPYC 7373X

SPECspeed®2017\_fp\_base = 195

SPECspeed®2017\_fp\_peak = 207

CPU2017 License: 9017

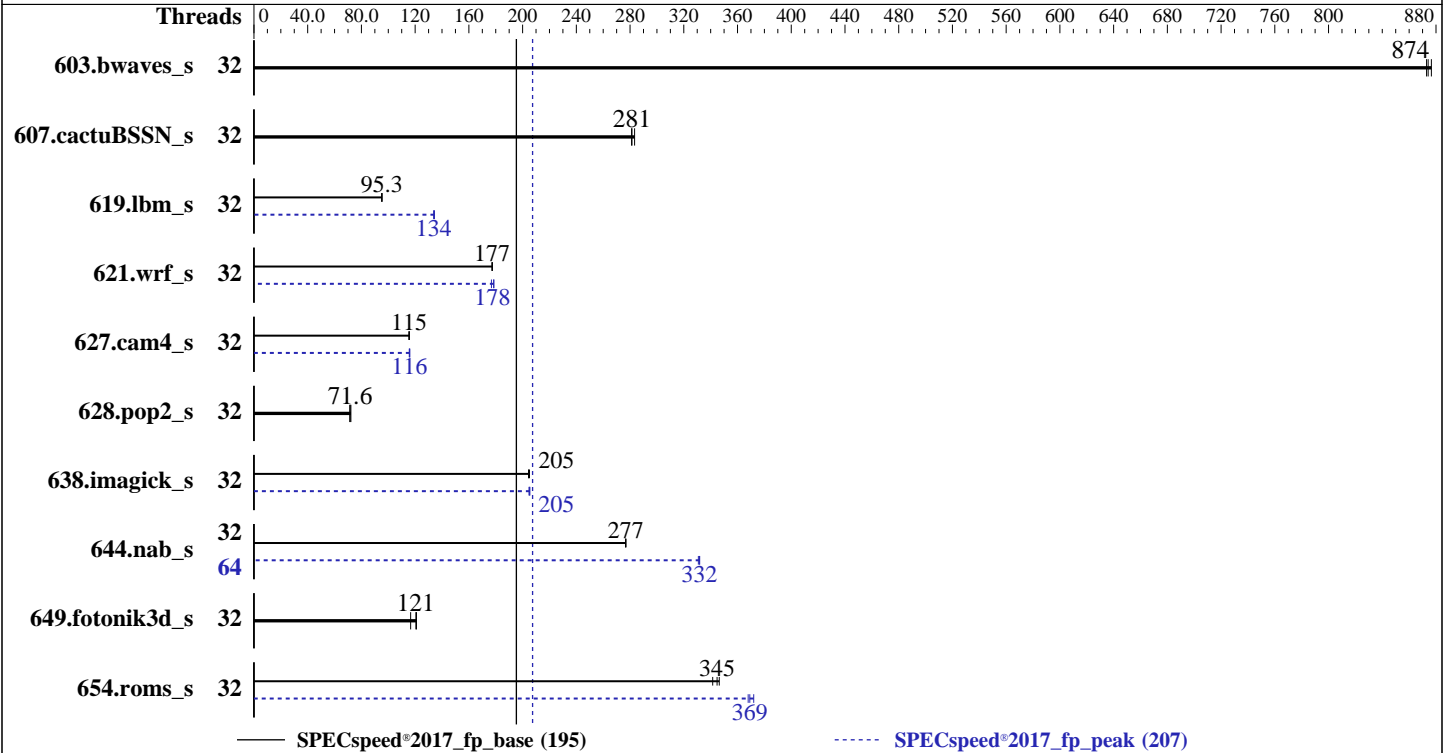
Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2022

Hardware Availability: Aug-2022

Software Availability: Feb-2022



### Hardware

CPU Name: AMD EPYC 7373X  
 Max MHz: 3800  
 Nominal: 3050  
 Enabled: 32 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 512 KB I+D on chip per core  
 L3: 768 MB I+D on chip per chip,  
 96 MB shared / 2 cores  
 Other: None  
 Memory: 512 GB (16 x 32 GB 2Rx4 PC4-3200AA-R)  
 Storage: 1 x 960 GB SATA SSD  
 Other: None

### Software

OS: Red Hat Enterprise Linux 8.5 (Ootpa)  
 Kernel 4.18.0-348.el8.x86\_64  
 Compiler: C/C++/Fortran: Version 3.2.0 of AOCC  
 Parallel: Yes  
 Firmware: Lenovo BIOS Version D8E125H 2.41 released Jul-2022  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc: jemalloc memory allocator library v5.1.0  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR665  
3.05 GHz, AMD EPYC 7373X

SPECSpeed®2017\_fp\_base = 195

SPECSpeed®2017\_fp\_peak = 207

CPU2017 License: 9017  
Test Sponsor: Lenovo Global Technology  
Tested by: Lenovo Global Technology

Test Date: Aug-2022  
Hardware Availability: Aug-2022  
Software Availability: Feb-2022

## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	32	<b>67.5</b>	<b>874</b>	67.3	877	67.6	873	32	<b>67.5</b>	<b>874</b>	67.3	877	67.6	873
607.cactuBSSN_s	32	<b>59.2</b>	<b>281</b>	58.8	283	59.3	281	32	<b>59.2</b>	<b>281</b>	58.8	283	59.3	281
619.lbm_s	32	<b>55.0</b>	<b>95.3</b>	54.9	95.3	55.1	95.1	32	39.1	134	<b>39.1</b>	<b>134</b>	39.0	134
621.wrf_s	32	74.5	177	<b>74.5</b>	<b>177</b>	74.6	177	32	<b>74.1</b>	<b>178</b>	74.0	179	74.8	177
627.cam4_s	32	76.6	116	76.8	115	<b>76.7</b>	<b>115</b>	32	76.4	116	76.6	116	<b>76.6</b>	<b>116</b>
628.pop2_s	32	165	72.2	<b>166</b>	<b>71.6</b>	166	71.3	32	165	72.2	<b>166</b>	<b>71.6</b>	166	71.3
638.imagick_s	32	<b>70.5</b>	<b>205</b>	70.3	205	70.6	204	32	70.2	205	<b>70.4</b>	<b>205</b>	70.4	205
644.nab_s	32	63.1	277	<b>63.1</b>	<b>277</b>	63.1	277	64	<b>52.7</b>	<b>332</b>	52.7	332	52.8	331
649.fotonik3d_s	32	78.1	117	75.3	121	<b>75.6</b>	<b>121</b>	32	78.1	117	75.3	121	<b>75.6</b>	<b>121</b>
654.roms_s	32	45.5	346	46.1	342	<b>45.7</b>	<b>345</b>	32	42.8	368	42.3	372	<b>42.7</b>	<b>369</b>

SPECSpeed®2017\_fp\_base = **195**

SPECSpeed®2017\_fp\_peak = **207**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

'ulimit -s unlimited' was used to set environment stack size limit  
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:  
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty\_ratio=8' run as root.  
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.  
To free node-local memory and avoid remote memory usage,  
'sysctl -w vm.zone\_reclaim\_mode=1' run as root.  
To clear filesystem caches, 'sync; sysctl -w vm.drop\_caches=3' run as root.  
To disable address space layout randomization (ASLR) to reduce run-to-run  
variability, 'sysctl -w kernel.randomize\_va\_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR665  
3.05 GHz, AMD EPYC 7373X

SPECspeed®2017\_fp\_base = 195

SPECspeed®2017\_fp\_peak = 207

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Aug-2022

**Hardware Availability:** Aug-2022

**Software Availability:** Feb-2022

### Operating System Notes (Continued)

'echo always > /sys/kernel/mm/transparent\_hugepage/enabled' and  
'echo always > /sys/kernel/mm/transparent\_hugepage/defrag' run as root.  
To enable THP only on request for peak runs of 628.pop2\_s:  
'echo madvise > /sys/kernel/mm/transparent\_hugepage/enabled' run as root.  
To disable THP for peak runs of 627.cam4\_s, 649.fotonik3d\_s, and 654.roms\_s,  
'echo never > /sys/kernel/mm/transparent\_hugepage/enabled' run as root.

### Environment Variables Notes

Environment variables set by runcpu before the start of the run:

GOMP\_CPU\_AFFINITY = "0-63"

LD\_LIBRARY\_PATH =

"/home/cpu2017-1.1.8-amd-milanx-aocc320-A1/amd\_speed\_aocc320\_milanx\_A\_li  
b/lib;/home/cpu2017-1.1.8-amd-milanx-aocc320-A1/amd\_speed\_aocc320\_milanx  
\_A\_lib/lib32:"

LIBOMP\_NUM\_HIDDEN\_HELPER\_THREADS = "0"

MALLOC\_CONF = "retain:true"

OMP\_DYNAMIC = "false"

OMP\_SCHEDULE = "static"

OMP\_STACKSIZE = "128M"

OMP\_THREAD\_LIMIT = "64"

Environment variables set by runcpu during the 619.lbm\_s peak run:

GOMP\_CPU\_AFFINITY = "0-31"

Environment variables set by runcpu during the 621.wrf\_s peak run:

GOMP\_CPU\_AFFINITY = "0-31"

Environment variables set by runcpu during the 627.cam4\_s peak run:

GOMP\_CPU\_AFFINITY = "0-31"

Environment variables set by runcpu during the 638.imagick\_s peak run:

GOMP\_CPU\_AFFINITY = "0-31"

Environment variables set by runcpu during the 644.nab\_s peak run:

GOMP\_CPU\_AFFINITY = "0-63"

Environment variables set by runcpu during the 654.roms\_s peak run:

GOMP\_CPU\_AFFINITY = "0-31"

### General Notes

Binaries were compiled on a system with 2x AMD EPYC 7742 CPU + 1TiB Memory using opensUSE 15.2

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR665  
3.05 GHz, AMD EPYC 7373X

SPECspeed®2017\_fp\_base = 195

SPECspeed®2017\_fp\_peak = 207

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Aug-2022

**Hardware Availability:** Aug-2022

**Software Availability:** Feb-2022

## General Notes (Continued)

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v4.8.2 in RHEL 7.4 (No options specified)

jemalloc 5.1.0 is available here:

<https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2>

## Platform Notes

BIOS configuration:

Operating Mode set to Maximum Performance and then set it to Custom Mode

SOC P-states set to P0

DLWM Support set to Disabled

sysinfo program /home/cpu2017-1.1.8-amd-milanx-aocc320-A1/bin/sysinfo

Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d

running on localhost.localdomain Fri Aug 26 20:57:44 2022

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : AMD EPYC 7373X 16-Core Processor

2 "physical id"s (chips)

64 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 16

siblings : 32

physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu from util-linux 2.32.1:

Architecture: x86\_64

CPU op-mode(s): 32-bit, 64-bit

Byte Order: Little Endian

CPU(s): 64

On-line CPU(s) list: 0-63

Thread(s) per core: 2

Core(s) per socket: 16

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR665  
3.05 GHz, AMD EPYC 7373X

SPECspeed®2017\_fp\_base = 195

SPECspeed®2017\_fp\_peak = 207

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Aug-2022  
**Hardware Availability:** Aug-2022  
**Software Availability:** Feb-2022

### Platform Notes (Continued)

```

Socket(s):                2
NUMA node(s):            2
Vendor ID:                AuthenticAMD
BIOS Vendor ID:          Advanced Micro Devices, Inc.
CPU family:              25
Model:                   1
Model name:               AMD EPYC 7373X 16-Core Processor
BIOS Model name:         AMD EPYC 7373X 16-Core Processor
Stepping:                 2
CPU MHz:                  3050.000
CPU max MHz:              3830.3711
CPU min MHz:              1500.0000
BogoMIPS:                 6088.77
Virtualization:          AMD-V
L1d cache:                32K
L1i cache:                32K
L2 cache:                 512K
L3 cache:                 98304K
NUMA node0 CPU(s):       0-15,32-47
NUMA node1 CPU(s):       16-31,48-63
Flags:                    fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf pni pclmulqdq
monitor ssse3 fma cx16 pcid sse4_1 sse4_2 movbe popcnt aes xsave avx f16c rdrand
lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw
ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb
cat_l3 cdp_l3 invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase
bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a rdseed adx smap clflushopt clwb sha_ni
xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local
clzero irperf xsaveerptr wbnoinvd amd_ppin arat npt lbrv svm_lock nrip_save
tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold
v_vmsave_vmload vgif v_spec_ctrl umip pku ospke vaes vpclmulqdq rdpid overflow_recov
succor smca fsrm sme sev sev_es

```

```
/proc/cpuinfo cache data
cache size : 512 KB
```

From numactl --hardware

WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 2 nodes (0-1)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 32 33 34 35 36 37 38 39 40 41 42 43
44 45 46 47
node 0 size: 257775 MB
node 0 free: 257086 MB
node 1 cpus: 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 48 49 50 51 52 53 54 55 56
57 58 59 60 61 62 63
node 1 size: 257999 MB

```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR665  
3.05 GHz, AMD EPYC 7373X

SPECspeed®2017\_fp\_base = 195

SPECspeed®2017\_fp\_peak = 207

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Aug-2022  
**Hardware Availability:** Aug-2022  
**Software Availability:** Feb-2022

### Platform Notes (Continued)

```
node 1 free: 257482 MB
node distances:
node 0 1
  0: 10 32
  1: 32 10
```

```
From /proc/meminfo
MemTotal: 528153404 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
/sbin/tuned-adm active
Current active profile: throughput-performance
```

```
/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
performance
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.5 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.5"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.5 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.5 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.5 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8::baseos
```

```
uname -a:
Linux localhost.localdomain 4.18.0-348.el8.x86_64 #1 SMP Mon Oct 4 12:17:22 EDT 2021
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):	Not affected
CVE-2018-3620 (L1 Terminal Fault):	Not affected
Microarchitectural Data Sampling:	Not affected
CVE-2017-5754 (Meltdown):	Not affected
CVE-2018-3639 (Speculative Store Bypass):	Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):	Mitigation: usercopy/swaps barriers and __user pointer sanitization

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR665  
3.05 GHz, AMD EPYC 7373X

SPECspeed®2017\_fp\_base = 195

SPECspeed®2017\_fp\_peak = 207

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Aug-2022  
**Hardware Availability:** Aug-2022  
**Software Availability:** Feb-2022

### Platform Notes (Continued)

CVE-2017-5715 (Spectre variant 2): Mitigation: Full AMD retpoline, IBPB: conditional, IBRS\_FW, STIBP: always-on, RSB filling  
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected  
CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 3 Aug 26 18:25

SPEC is set to: /home/cpu2017-1.1.8-amd-milanx-aocc320-A1  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/sda4 xfs 819G 351G 468G 43% /home

From /sys/devices/virtual/dmi/id  
Vendor: Lenovo  
Product: ThinkSystem SR665-2S  
Product Family: ThinkSystem  
Serial: 1234567890

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:  
16x Samsung M393A4K40DB3-CWE 32 GB 2 rank 3200

BIOS:  
BIOS Vendor: Lenovo  
BIOS Version: D8E125H-2.41  
BIOS Date: 07/08/2022  
BIOS Revision: 2.41  
Firmware Revision: 4.12

(End of data from sysinfo program)

### Compiler Version Notes

```
=====
C | 619.lbm_s(base, peak) 638.imagick_s(base, peak)
  | 644.nab_s(base, peak)
=====
AMD clang version 13.0.0 (CLANG: AOCC_3.2.0-Build#128 2021_11_12) (based on
LLVM Mirror.Version.13.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin
=====
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR665  
3.05 GHz, AMD EPYC 7373X

SPECspeed®2017\_fp\_base = 195

SPECspeed®2017\_fp\_peak = 207

**CPU2017 License:** 9017  
**Test Sponsor:** Lenovo Global Technology  
**Tested by:** Lenovo Global Technology

**Test Date:** Aug-2022  
**Hardware Availability:** Aug-2022  
**Software Availability:** Feb-2022

### Compiler Version Notes (Continued)

=====  
C++, C, Fortran | 607.cactuBSSN\_s(base, peak)  
-----

AMD clang version 13.0.0 (CLANG: AOCC\_3.2.0-Build#128 2021\_11\_12) (based on LLVM Mirror.Version.13.0.0)

Target: x86\_64-unknown-linux-gnu  
Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

AMD clang version 13.0.0 (CLANG: AOCC\_3.2.0-Build#128 2021\_11\_12) (based on LLVM Mirror.Version.13.0.0)

Target: x86\_64-unknown-linux-gnu  
Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

AMD clang version 13.0.0 (CLANG: AOCC\_3.2.0-Build#128 2021\_11\_12) (based on LLVM Mirror.Version.13.0.0)

Target: x86\_64-unknown-linux-gnu  
Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin  
-----

=====  
Fortran | 603.bwaves\_s(base, peak) 649.fotonik3d\_s(base, peak)  
654.roms\_s(base, peak)

AMD clang version 13.0.0 (CLANG: AOCC\_3.2.0-Build#128 2021\_11\_12) (based on LLVM Mirror.Version.13.0.0)

Target: x86\_64-unknown-linux-gnu  
Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin  
-----

=====  
Fortran, C | 621.wrf\_s(base, peak) 627.cam4\_s(base, peak)  
628.pop2\_s(base, peak)

AMD clang version 13.0.0 (CLANG: AOCC\_3.2.0-Build#128 2021\_11\_12) (based on LLVM Mirror.Version.13.0.0)

Target: x86\_64-unknown-linux-gnu  
Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin

AMD clang version 13.0.0 (CLANG: AOCC\_3.2.0-Build#128 2021\_11\_12) (based on LLVM Mirror.Version.13.0.0)

Target: x86\_64-unknown-linux-gnu  
Thread model: posix

InstalledDir: /opt/AMD/aocc-compiler-3.2.0/bin  
-----





# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR665  
3.05 GHz, AMD EPYC 7373X

SPECspeed®2017\_fp\_base = 195

SPECspeed®2017\_fp\_peak = 207

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2022

Hardware Availability: Aug-2022

Software Availability: Feb-2022

## Base Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Base Portability Flags

603.bwaves\_s: -DSPEC\_LP64  
607.cactuBSSN\_s: -DSPEC\_LP64  
619.ibm\_s: -DSPEC\_LP64  
621.wrf\_s: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
627.cam4\_s: -DSPEC\_CASE\_FLAG -DSPEC\_LP64  
628.pop2\_s: -DSPEC\_CASE\_FLAG -Mbyteswapio -DSPEC\_LP64  
638.imagick\_s: -DSPEC\_LP64  
644.nab\_s: -DSPEC\_LP64  
649.fotonik3d\_s: -DSPEC\_LP64  
654.roms\_s: -DSPEC\_LP64

## Base Optimization Flags

C benchmarks:

-m64 -Wl,-mllvm -Wl,-region-vectorize  
-Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3  
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=5  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000  
-fremap-arrays -mllvm -function-specialize -flv-function-specialization  
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true  
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3 -z muldefs  
-DSPEC\_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang

Fortran benchmarks:

-m64 -Wl,-mllvm -Wl,-enable-X86-prefetching  
-Wl,-mllvm -Wl,-enable-licm-vrp -Wl,-mllvm -Wl,-region-vectorize

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR665  
3.05 GHz, AMD EPYC 7373X

SPECspeed®2017\_fp\_base = 195

SPECspeed®2017\_fp\_peak = 207

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Aug-2022

**Hardware Availability:** Aug-2022

**Software Availability:** Feb-2022

## Base Optimization Flags (Continued)

Fortran benchmarks (continued):

```
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Hz,1,0x1 -O3
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp -Mrecursive
-mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-loopinterchange
-mllvm -compute-interchange-order -z muldefs -DSPEC_OPENMP
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
```

Benchmarks using both Fortran and C:

```
-m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=5
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3 -Hz,1,0x1
-Mrecursive -mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop
-mllvm -enable-loopinterchange -mllvm -compute-interchange-order
-z muldefs -DSPEC_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc
-lflang
```

Benchmarks using Fortran, C, and C++:

```
-m64 -Wl,-mllvm -Wl,-x86-use-vzeroupper=false
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -fopenmp -flto -fstruct-layout=5
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-freemap-arrays -mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -enable-partial-unswitch -mllvm -unroll-threshold=100
-finline-aggressive -mllvm -loop-unswitch-threshold=200000
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
-mllvm -extra-vectorizer-passes -mllvm -convert-pow-exp-to-int=false
-Hz,1,0x1 -Mrecursive -mllvm -fuse-tile-inner-loop -funroll-loops
-mllvm -lsr-in-nested-loop -mllvm -enable-loopinterchange
-mllvm -compute-interchange-order -z muldefs -DSPEC_OPENMP
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

**Lenovo Global Technology**

ThinkSystem SR665  
3.05 GHz, AMD EPYC 7373X

SPECspeed®2017\_fp\_base = 195

SPECspeed®2017\_fp\_peak = 207

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2022

Hardware Availability: Aug-2022

Software Availability: Feb-2022

## Base Optimization Flags (Continued)

Benchmarks using Fortran, C, and C++ (continued):

-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang

## Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument -Wno-return-type

Fortran benchmarks:

-Wno-unused-command-line-argument -Wno-return-type

Benchmarks using both Fortran and C:

-Wno-unused-command-line-argument -Wno-return-type

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument -Wno-return-type

## Peak Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

flang

Benchmarks using both Fortran and C:

flang clang

Benchmarks using Fortran, C, and C++:

clang++ clang flang

## Peak Portability Flags

Same as Base Portability Flags



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECspeed®2017\_fp\_base = 195

ThinkSystem SR665  
3.05 GHz, AMD EPYC 7373X

SPECspeed®2017\_fp\_peak = 207

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2022

Hardware Availability: Aug-2022

Software Availability: Feb-2022

## Peak Optimization Flags

C benchmarks:

```
619.lbm_s: -m64 -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=5 -mllvm -unroll-threshold=50
-freemap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
```

```
638.imagick_s: -m64 -Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-enable-licm-vrp
-Wl,-mllvm -Wl,-do-block-reorder=aggressive
-Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp
-flto -fstruct-layout=5 -mllvm -unroll-threshold=50
-mllvm -inline-threshold=1000 -freemap-arrays
-mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3
-mllvm -do-block-reorder=aggressive -DSPEC_OPENMP
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
```

644.nab\_s: Same as 638.imagick\_s

Fortran benchmarks:

603.bwaves\_s: basepeak = yes

649.fotonik3d\_s: basepeak = yes

```
654.roms_s: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching
-Wl,-mllvm -Wl,-enable-licm-vrp
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp
-Mrecursive -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -enable-licm-vrp
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

## Lenovo Global Technology

ThinkSystem SR665  
3.05 GHz, AMD EPYC 7373X

SPECspeed®2017\_fp\_base = 195

SPECspeed®2017\_fp\_peak = 207

CPU2017 License: 9017

Test Sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test Date: Aug-2022

Hardware Availability: Aug-2022

Software Availability: Feb-2022

## Peak Optimization Flags (Continued)

654.roms\_s (continued):

```
-DSPEC_OPENMP -fopenmp=libomp -lomp -lamdlibm -ljemalloc  
-lflang
```

Benchmarks using both Fortran and C:

```
621.wrf_s: -m64 -Wl,-mllvm -Wl,-enable-X86-prefetching  
-Wl,-mllvm -Wl,-enable-licm-vrp  
-Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast  
-march=znver3 -fveclib=AMDLIBM -ffast-math -fopenmp  
-flto -fstruct-layout=5 -mllvm -unroll-threshold=50  
-fremap-arrays -flv-function-specialization  
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist  
-mllvm -global-vectorize-slp=true  
-mllvm -function-specialize -mllvm -enable-licm-vrp  
-mllvm -reduce-array-computations=3 -Hz,1,0x1 -Mrecursive  
-mllvm -fuse-tile-inner-loop -funroll-loops  
-mllvm -extra-vectorizer-passes -mllvm -lsr-in-nested-loop  
-mllvm -enable-loopinterchange  
-mllvm -compute-interchange-order -DSPEC_OPENMP  
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
```

627.cam4\_s: Same as 621.wrf\_s

628.pop2\_s: basepeak = yes

Benchmarks using Fortran, C, and C++:

607.cactuBSSN\_s: basepeak = yes

## Peak Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument -Wno-return-type
```

Fortran benchmarks:

```
-Wno-unused-command-line-argument -Wno-return-type
```

Benchmarks using both Fortran and C:

```
-Wno-unused-command-line-argument -Wno-return-type
```

(Continued on next page)



# SPEC CPU®2017 Floating Point Speed Result

Copyright 2017-2022 Standard Performance Evaluation Corporation

**Lenovo Global Technology**

ThinkSystem SR665  
3.05 GHz, AMD EPYC 7373X

SPECspeed®2017\_fp\_base = 195

SPECspeed®2017\_fp\_peak = 207

**CPU2017 License:** 9017

**Test Sponsor:** Lenovo Global Technology

**Tested by:** Lenovo Global Technology

**Test Date:** Aug-2022

**Hardware Availability:** Aug-2022

**Software Availability:** Feb-2022

## Peak Other Flags (Continued)

Benchmarks using Fortran, C, and C++:

-Wno-unused-command-line-argument -Wno-return-type

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-MilanX-L.html>

<http://www.spec.org/cpu2017/flags/aocc320-flags-A1.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Lenovo-Platform-SPECcpu2017-Flags-V1.2-MilanX-L.xml>

<http://www.spec.org/cpu2017/flags/aocc320-flags-A1.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.8 on 2022-08-26 08:57:44-0400.

Report generated on 2022-09-13 16:58:26 by CPU2017 PDF formatter v6442.

Originally published on 2022-09-13.