



# SPEC CPU®2017 Integer Speed Result

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## Supermicro

Microcloud SuperServer SYS-530MT-H8TNR  
(X12STD-F, Intel Xeon E-2378G)

SPECspeed®2017\_int\_base = 15.7

SPECspeed®2017\_int\_peak = 16.1

CPU2017 License: 001176

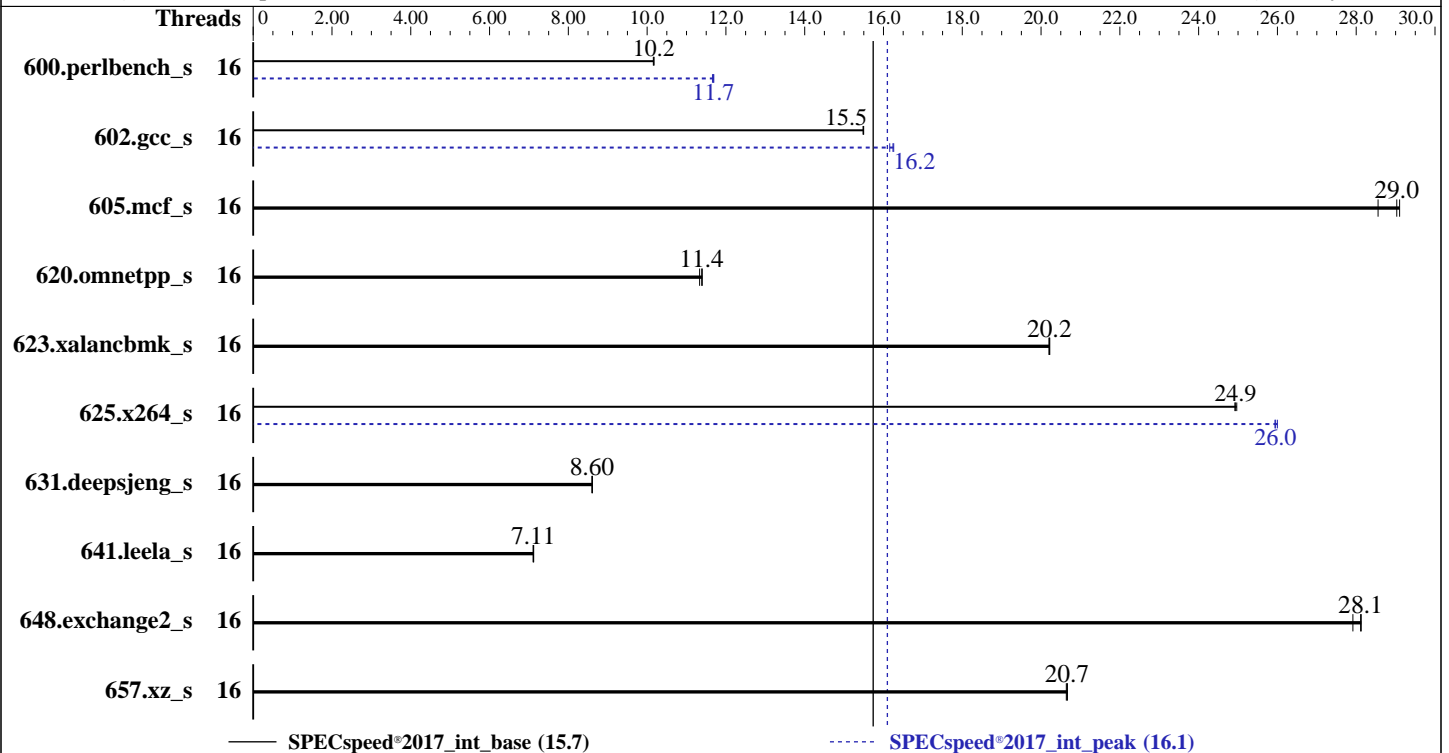
Test Sponsor: Supermicro

Tested by: Supermicro

Test Date: Nov-2021

Hardware Availability: Sep-2021

Software Availability: May-2021



### Hardware

CPU Name: Intel Xeon E-2378G  
 Max MHz: 5100  
 Nominal: 2800  
 Enabled: 8 cores, 1 chip, 2 threads/core  
 Orderable: 1 chip  
 Cache L1: 32 KB I + 48 KB D on chip per core  
 L2: 512 KB I+D on chip per core  
 L3: 16 MB I+D on chip per chip  
 Other: None  
 Memory: 128 GB (4 x 32 GB 2Rx8 PC4-3200AA-E, running at 2933)  
 Storage: 1 x 200 GB SATA III SSD  
 Other: None

### Software

OS: Red Hat Enterprise Linux release 8.4  
 Kernel 4.18.0-305.el8.x86\_64  
 Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;  
 Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;  
 C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux  
 Parallel: Yes  
 Firmware: Version 1.0 released Aug-2021  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: OS set to prefer performance at the cost of additional power usage.



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## Results Table

| Benchmark       | Base    |                    |                    |                   |                    |                   |                    | Peak    |                    |                    |                   |                    |                    |                    |
|-----------------|---------|--------------------|--------------------|-------------------|--------------------|-------------------|--------------------|---------|--------------------|--------------------|-------------------|--------------------|--------------------|--------------------|
|                 | Threads | Seconds            | Ratio              | Seconds           | Ratio              | Seconds           | Ratio              | Threads | Seconds            | Ratio              | Seconds           | Ratio              | Seconds            | Ratio              |
| 600.perlbench_s | 16      | 175                | 10.2               | <b><u>175</u></b> | <b><u>10.2</u></b> | 174               | 10.2               | 16      | <b><u>152</u></b>  | <b><u>11.7</u></b> | 152               | 11.7               | 152                | 11.7               |
| 602.gcc_s       | 16      | <b><u>257</u></b>  | <b><u>15.5</u></b> | 257               | 15.5               | 257               | 15.5               | 16      | 246                | 16.2               | <b><u>245</u></b> | <b><u>16.2</u></b> | 245                | 16.3               |
| 605.mcf_s       | 16      | 165                | 28.6               | <b><u>163</u></b> | <b><u>29.0</u></b> | 162               | 29.1               | 16      | 165                | 28.6               | <b><u>163</u></b> | <b><u>29.0</u></b> | 162                | 29.1               |
| 620.omnetpp_s   | 16      | 144                | 11.3               | 143               | 11.4               | <b><u>143</u></b> | <b><u>11.4</u></b> | 16      | 144                | 11.3               | 143               | 11.4               | <b><u>143</u></b>  | <b><u>11.4</u></b> |
| 623.xalancbmk_s | 16      | <b><u>70.1</u></b> | <b><u>20.2</u></b> | 70.1              | 20.2               | 70.1              | 20.2               | 16      | <b><u>70.1</u></b> | <b><u>20.2</u></b> | 70.1              | 20.2               | 70.1               | 20.2               |
| 625.x264_s      | 16      | <b><u>70.8</u></b> | <b><u>24.9</u></b> | 70.7              | 25.0               | 70.8              | 24.9               | 16      | 67.8               | 26.0               | 68.0              | 25.9               | <b><u>68.0</u></b> | <b><u>26.0</u></b> |
| 631.deepsjeng_s | 16      | 167                | 8.60               | 166               | 8.61               | <b><u>167</u></b> | <b><u>8.60</u></b> | 16      | 167                | 8.60               | 166               | 8.61               | <b><u>167</u></b>  | <b><u>8.60</u></b> |
| 641.leela_s     | 16      | 240                | 7.12               | <b><u>240</u></b> | <b><u>7.11</u></b> | 240               | 7.11               | 16      | 240                | 7.12               | <b><u>240</u></b> | <b><u>7.11</u></b> | 240                | 7.11               |
| 648.exchange2_s | 16      | 105                | 28.1               | <b><u>105</u></b> | <b><u>28.1</u></b> | 105               | 27.9               | 16      | 105                | 28.1               | <b><u>105</u></b> | <b><u>28.1</u></b> | 105                | 27.9               |
| 657.xz_s        | 16      | <b><u>299</u></b>  | <b><u>20.7</u></b> | 299               | 20.7               | 299               | 20.6               | 16      | <b><u>299</u></b>  | <b><u>20.7</u></b> | 299               | 20.7               | 299                | 20.6               |

SPECspeed®2017\_int\_base = **15.7**

SPECspeed®2017\_int\_peak = **16.1**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH = "/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-64"  
MALLOC\_CONF = "retain:true"  
OMP\_STACKSIZE = "192M"

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM  
memory using Redhat Enterprise Linux 8.0  
Transparent Huge Pages enabled by default  
Prior to runcpu invocation  
Filesystem page cache synced and cleared with:  
sync; echo 3> /proc/sys/vm/drop\_caches

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.  
jemalloc, a general purpose malloc implementation

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### General Notes (Continued)

built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5  
sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

### Platform Notes

Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d  
running on 135-170-143.engtw Tue Nov 2 16:47:39 2021

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
model name : Intel(R) Xeon(R) E-2378G CPU @ 2.80GHz  
1 "physical id"s (chips)  
16 "processors"  
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)  
cpu cores : 8  
siblings : 16  
physical 0: cores 0 1 2 3 4 5 6 7

From lscpu from util-linux 2.32.1:  
Architecture: x86\_64  
CPU op-mode(s): 32-bit, 64-bit  
Byte Order: Little Endian  
CPU(s): 16  
On-line CPU(s) list: 0-15  
Thread(s) per core: 2  
Core(s) per socket: 8  
Socket(s): 1  
NUMA node(s): 1  
Vendor ID: GenuineIntel  
BIOS Vendor ID: Intel(R) Corporation  
CPU family: 6  
Model: 167  
Model name: Intel(R) Xeon(R) E-2378G CPU @ 2.80GHz  
BIOS Model name: Intel(R) Xeon(R) E-2378G CPU @ 2.80GHz  
Stepping: 1  
CPU MHz: 4643.636  
CPU max MHz: 2801.0000  
CPU min MHz: 800.0000  
BogoMIPS: 5616.00  
Virtualization: VT-x

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### Platform Notes (Continued)

L1d cache: 48K  
L1i cache: 32K  
L2 cache: 512K  
L3 cache: 16384K  
NUMA node0 CPU(s): 0-15

Flags: fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant\_tsc art arch\_perfmon pebs bts rep\_good nopl xtopology nonstop\_tsc cpuid aperfmperf tsc\_known\_freq pni pclmulqdq dtes64 monitor ds\_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid sse4\_1 sse4\_2 x2apic movbe popcnt tsc\_deadline\_timer aes xsave avx f16c rdrand lahf\_lm abm 3dnowprefetch cpuid\_fault epb invpcid\_single ssbd ibrs ibpb stibp ibrs\_enhanced tpr\_shadow vnmi flexpriority ept vpid ept\_ad fsgsbase tsc\_adjust bmi1 avx2 smep bmi2 erms invpcid mpx avx512f avx512dq rdseed adx smap avx512ifma clflushopt intel\_pt avx512cd sha\_ni avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves dtherm ida arat pln pts avx512vbmi umip pku ospke avx512\_vbmi2 gfni vaes vpclmulqdq avx512\_vnni avx512\_bitalg avx512\_vpopcntdq rdpid fsrm md\_clear flush\_lld arch\_capabilities

```
/proc/cpuinfo cache data
cache size : 16384 KB
```

```
From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 1 nodes (0)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
node 0 size: 128815 MB
node 0 free: 128050 MB
node distances:
node 0
0: 10
```

```
From /proc/meminfo
MemTotal: 131907500 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
/sbin/tuned-adm active
Current active profile: throughput-performance
```

```
/sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
performance
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.4 (Ootpa)"
ID="rhel"
```

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### Platform Notes (Continued)

```
ID_LIKE="fedora"
VERSION_ID="8.4"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.4 (Ootpa)"
ANSI_COLOR="0;31"
```

```
redhat-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.4:ga
```

```
uname -a:
Linux 135-170-143.engtw 4.18.0-305.el8.x86_64 #1 SMP Thu Apr 29 08:54:30 EDT 2021
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

```
CVE-2018-12207 (iTLB Multihit): Not affected
CVE-2018-3620 (L1 Terminal Fault): Not affected
Microarchitectural Data Sampling: Not affected
CVE-2017-5754 (Meltdown): Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store
Bypass disabled via prctl and
seccomp
CVE-2017-5753 (Spectre variant 1): Mitigation: usercopy/swapgs
barriers and __user pointer
sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS, IBPB:
conditional, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort): Not affected
```

```
run-level 3 Nov 2 16:40
```

```
SPEC is set to: /home/cpu2017
Filesystem Type Size Used Avail Use% Mounted on
/dev/mapper/rhel-root xfs 182G 8.8G 173G 5% /
```

```
From /sys/devices/virtual/dmi/id
Vendor: Supermicro
Product: Super Server
Serial: 0123456789
```

Additional information from dmidecode 3.2 follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
Memory:
4x Micron Technology 18ADF4G72AZ-3G2B3 32 GB 2 rank 3200, configured at 2933
```

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### Platform Notes (Continued)

BIOS:

BIOS Vendor: American Megatrends International, LLC.  
BIOS Version: 1.0  
BIOS Date: 08/31/2021  
BIOS Revision: 5.22

(End of data from sysinfo program)

### Compiler Version Notes

=====  
C | 600.perlbench\_s(peak)  
-----

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C | 600.perlbench\_s(base) 602.gcc\_s(base, peak) 605.mcf\_s(base, peak)  
| 625.x264\_s(base, peak) 657.xz\_s(base, peak)  
-----

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C | 600.perlbench\_s(peak)  
-----

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112\_000000  
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-----

=====  
C | 600.perlbench\_s(base) 602.gcc\_s(base, peak) 605.mcf\_s(base, peak)  
| 625.x264\_s(base, peak) 657.xz\_s(base, peak)  
-----

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113  
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=====

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### Compiler Version Notes (Continued)

C++ | 620.omnetpp\_s(base, peak) 623.xalancbmk\_s(base, peak)  
| 631.deepsjeng\_s(base, peak) 641.leela\_s(base, peak)

-----  
Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,  
Version 2021.1 Build 20201113  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
Fortran | 648.exchange2\_s(base, peak)

-----  
Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on  
Intel(R) 64, Version 2021.1 Build 20201112\_000000  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

### Base Compiler Invocation

C benchmarks:  
icx

C++ benchmarks:  
icpx

Fortran benchmarks:  
ifort

### Base Portability Flags

600.perlbench\_s: -DSPEC\_LP64 -DSPEC\_LINUX\_X64  
602.gcc\_s: -DSPEC\_LP64  
605.mcf\_s: -DSPEC\_LP64  
620.omnetpp\_s: -DSPEC\_LP64  
623.xalancbmk\_s: -DSPEC\_LP64 -DSPEC\_LINUX  
625.x264\_s: -DSPEC\_LP64  
631.deepsjeng\_s: -DSPEC\_LP64  
641.leela\_s: -DSPEC\_LP64  
648.exchange2\_s: -DSPEC\_LP64  
657.xz\_s: -DSPEC\_LP64



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## Base Optimization Flags

### C benchmarks:

```
-DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512  
-O3 -ffast-math -flto -mfpmath=sse -funroll-loops  
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries  
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

### C++ benchmarks:

```
-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math  
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4  
-mbranches-within-32B-boundaries  
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/  
-lqkmallocc
```

### Fortran benchmarks:

```
-m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4  
-nostandard-realloc-lhs -align array32byte -auto  
-mbranches-within-32B-boundaries
```

## Peak Compiler Invocation

### C benchmarks (except as noted below):

icx

600.perlbench\_s: icc

### C++ benchmarks:

icpx

### Fortran benchmarks:

ifort

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

### C benchmarks:

(Continued on next page)





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## Peak Optimization Flags (Continued)

```
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
605.mcf_s: basepeak = yes
```

```
625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs
-xCORE-AVX512 -flto -O3 -ffast-math
-qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
657.xz_s: basepeak = yes
```

C++ benchmarks:

```
620.omnetpp_s: basepeak = yes
```

```
623.xalancbmk_s: basepeak = yes
```

```
631.deepsjeng_s: basepeak = yes
```

```
641.leela_s: basepeak = yes
```

Fortran benchmarks:

```
648.exchange2_s: basepeak = yes
```

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.html](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.html)

[http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-RKL\\_revA.html](http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-RKL_revA.html)

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64\\_revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml)

[http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-RKL\\_revA.xml](http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-RKL_revA.xml)



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For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.8 on 2021-11-02 04:47:39-0400.  
Report generated on 2021-11-24 11:19:07 by CPU2017 PDF formatter v6442.  
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