SPEC CPU®2017 Integer Speed Result

Supermicro

A+ Server 1024US-TRT
(H12DSU-iN , AMD EPYC 7543)

$SPECspeed^{®}2017\_int\_base = 12.5$

$SPECspeed^{®}2017\_int\_peak = 12.6$

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Oct-2021
Hardware Availability: Mar-2021
Software Availability: May-2021

Threads		Result
600.perlbench_s	64	6.41
	1	6.72
602.gcc_s	64	13.6
	1	13.7
605.mcf_s	64	20.4
620.omnetpp_s	64	8.97
623.xalancbmk_s	64	14.3
625.x264_s	64	17.4
	1	17.5
631.deepsjeng_s	64	6.48
641.leela_s	64	5.89
648.exchange2_s	64	23.7
	1	23.7
657.xz_s	64	26.2
	1	26.3

Scale: 0 2.00 4.00 6.00 8.00 10.0 12.0 14.0 16.0 18.0 20.0 22.0 24.0 27.0

—— SPECspeed®2017_int_base (12.5)   ----- SPECspeed®2017_int_peak (12.6)

Hardware

CPU Name: AMD EPYC 7543
 Max MHz: 3700
 Nominal: 2800
Enabled: 64 cores, 2 chips, 2 threads/core
Orderable: 1,2 chips
Cache L1: 32 KB I + 32 KB D on chip per core
 L2: 512 KB I+D on chip per core
 L3: 256 MB I+D on chip per chip, 32 MB shared / 4 cores
 Other: None
Memory: 512 GB (16 x 32 GB 2Rx4 PC4-3200AA-R)
Storage: 1 x 200 GB SATA III SSD
Other: None

Software

OS: Red Hat Enterprise Linux 8.4
 Kernel 4.18.0-305.el8.x86_64
Compiler: C/C++/Fortran: Version 3.0.0 of AOCC
Parallel: Yes
Firmware: Version 2.3 released Oct-2021
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other: jemalloc: jemalloc memory allocator library v5.1.0
Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.

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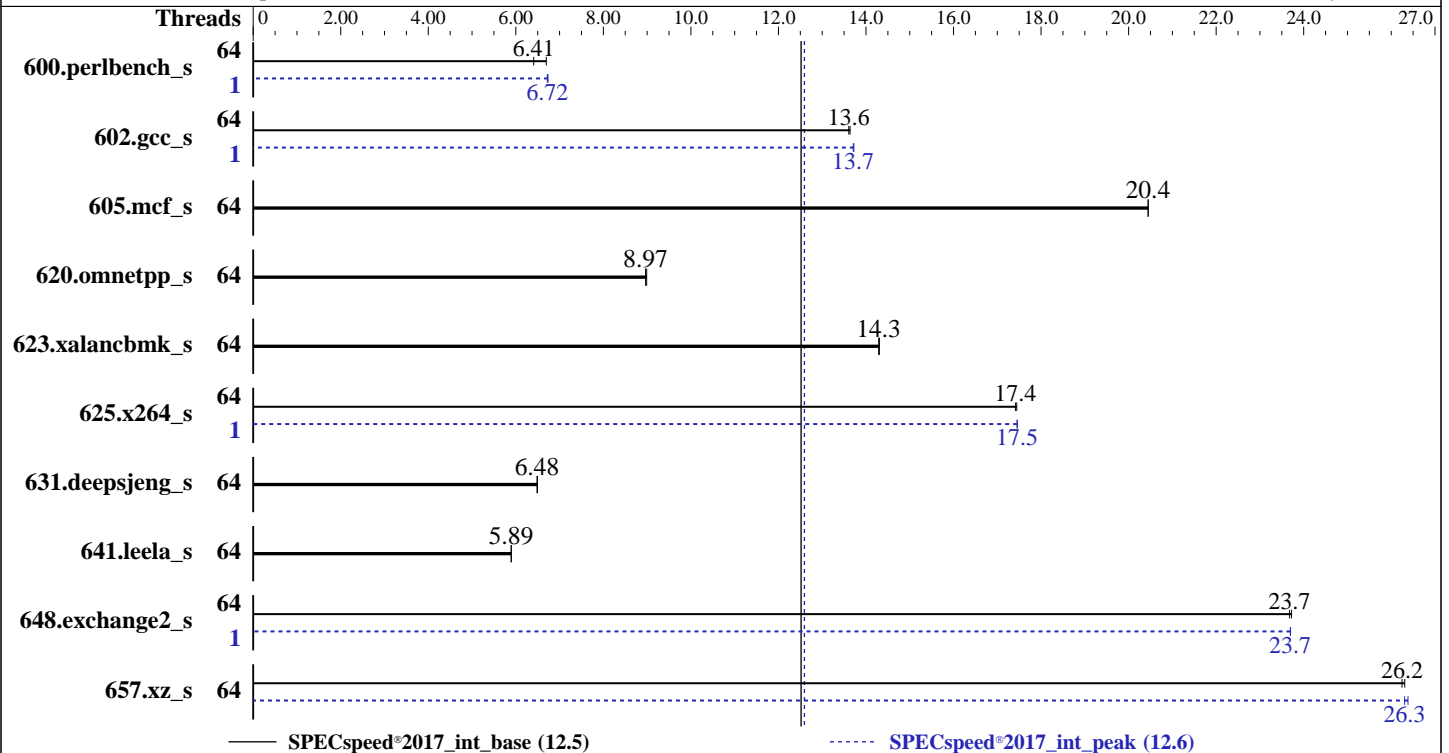
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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	64	**277**	**6.41**	265	6.70			1	**264**	**6.72**	264	6.74		
602.gcc_s	64	292	13.6	**293**	**13.6**			1	290	13.7	**290**	**13.7**		
605.mcf_s	64	231	20.4	**231**	**20.4**			64	231	20.4	**231**	**20.4**		
620.omnetpp_s	64	181	8.99	**182**	**8.97**			64	181	8.99	**182**	**8.97**		
623.xalancbmk_s	64	**99.2**	**14.3**	99.0	14.3			64	**99.2**	**14.3**	99.0	14.3		
625.x264_s	64	**101**	**17.4**	101	17.4			1	**101**	**17.5**	101	17.5		
631.deepsjeng_s	64	221	6.49	**221**	**6.48**			64	221	6.49	**221**	**6.48**		
641.leela_s	64	289	5.90	**289**	**5.89**			64	289	5.90	**289**	**5.89**		
648.exchange2_s	64	**124**	**23.7**	124	23.7			1	124	23.7	**124**	**23.7**		
657.xz_s	64	**236**	**26.2**	235	26.3			64	234	26.4	**235**	**26.3**		
SPECspeed®2017_int_base =		**12.5**												
SPECspeed®2017_int_peak =		**12.6**												

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

```
The AMD64 AOCC Compiler Suite is available at
http://developer.amd.com/amd-aocc/
```

Submit Notes

```
The config file option 'submit' was used.
'numactl' was used to bind copies to the cores.
See the configuration file for details.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size limit
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>

To limit dirty cache to 8% of memory, 'sysctl -w vm.dirty_ratio=8' run as root.
To limit swap usage to minimum necessary, 'sysctl -w vm.swappiness=1' run as root.
To free node-local memory and avoid remote memory usage,
'sysctl -w vm.zone_reclaim_mode=1' run as root.
To clear filesystem caches, 'sync; sysctl -w vm.drop_caches=3' run as root.
To disable address space layout randomization (ASLR) to reduce run-to-run
variability, 'sysctl -w kernel.randomize_va_space=0' run as root.

To enable Transparent Hugepages (THP) for all allocations,
```

(Continued on next page)

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Operating System Notes (Continued)

```
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root.
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
GOMP_CPU_AFFINITY = "0-127"
LD_LIBRARY_PATH =
     "/home/cpu2017/amd_speed_aocc300_milan_B_lib/lib;/home/cpu2017/amd_speed
     _aocc300_milan_B_lib/lib32:"
MALLOC_CONF = "retain:true"
OMP_DYNAMIC = "false"
OMP_SCHEDULE = "static"
OMP_STACKSIZE = "128M"
OMP_THREAD_LIMIT = "128"

Environment variables set by runcpu during the 600.perlbench_s peak run:
GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 602.gcc_s peak run:
GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 625.x264_s peak run:
GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 648.exchange2_s peak run:
GOMP_CPU_AFFINITY = "0"

Environment variables set by runcpu during the 657.xz_s peak run:
GOMP_CPU_AFFINITY = "0-63"
```

General Notes

```
Binaries were compiled on a system with 2x AMD EPYC 7742 CPU + 1TiB Memory using openSUSE 15.2

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v4.8.2 in RHEL 7.4 (No options specified)
jemalloc 5.1.0 is available here:
```

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Hardware Availability: Mar-2021
Software Availability: May-2021

General Notes (Continued)

https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2

Platform Notes

```
BIOS Settings:
Determinism Control = Manual
Determinism Slider = Power
cTDP Control = Manual
cTDP = 240
Package Power Limit Control = Manual
Package Power Limit = 240
APBDIS = 1
NUMA Nodes Per Socket = NPS4

 Sysinfo program /home/cpu2017/bin/sysinfo
 Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d
 running on H12DSU-7543 Sat Oct 30 02:17:33 2021

 SUT (System Under Test) info as seen by some common utilities.
 For more information on this section, see
    https://www.spec.org/cpu2017/Docs/config.html#sysinfo

 From /proc/cpuinfo
    model name : AMD EPYC 7543 32-Core Processor
       2  "physical id"s (chips)
       128 "processors"
    cores, siblings (Caution: counting these is hw and system dependent. The following
    excerpts from /proc/cpuinfo might not be reliable.  Use with caution.)
       cpu cores : 32
       siblings  : 64
       physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
       25 26 27 28 29 30 31
       physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24
       25 26 27 28 29 30 31

 From lscpu from util-linux 2.32.1:
       Architecture:        x86_64
       CPU op-mode(s):      32-bit, 64-bit
       Byte Order:          Little Endian
       CPU(s):              128
       On-line CPU(s) list: 0-127
       Thread(s) per core:  2
       Core(s) per socket:  32
       Socket(s):           2
       NUMA node(s):        16
```

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CPU2017 License: 001176   **Test Date:**   Oct-2021
Test Sponsor: Supermicro   **Hardware Availability:** Mar-2021
Tested by: Supermicro   **Software Availability:** May-2021

Platform Notes (Continued)

```
        Vendor ID:              AuthenticAMD
        BIOS Vendor ID:         Advanced Micro Devices, Inc.
        CPU family:             25
        Model:                  1
        Model name:             AMD EPYC 7543 32-Core Processor
        BIOS Model name:        AMD EPYC 7543 32-Core Processor
        Stepping:               1
        CPU MHz:                2453.100
        CPU max MHz:            2800.0000
        CPU min MHz:            1500.0000
        BogoMIPS:               5600.16
        Virtualization:         AMD-V
        L1d cache:              32K
        L1i cache:              32K
        L2 cache:               512K
        L3 cache:               32768K
        NUMA node0 CPU(s):      0-3,64-67
        NUMA node1 CPU(s):      4-7,68-71
        NUMA node2 CPU(s):      8-11,72-75
        NUMA node3 CPU(s):      12-15,76-79
        NUMA node4 CPU(s):      16-19,80-83
        NUMA node5 CPU(s):      20-23,84-87
        NUMA node6 CPU(s):      24-27,88-91
        NUMA node7 CPU(s):      28-31,92-95
        NUMA node8 CPU(s):      32-35,96-99
        NUMA node9 CPU(s):      36-39,100-103
        NUMA node10 CPU(s):     40-43,104-107
        NUMA node11 CPU(s):     44-47,108-111
        NUMA node12 CPU(s):     48-51,112-115
        NUMA node13 CPU(s):     52-55,116-119
        NUMA node14 CPU(s):     56-59,120-123
        NUMA node15 CPU(s):     60-63,124-127
        Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
    pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
    constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf pni pclmulqdq
    monitor ssse3 fma cx16 pcid sse4_1 sse4_2 movbe popcnt aes xsave avx f16c rdrand
    lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw
    ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb
    cat_l3 cdp_l3 invpcid_single hw_pstate sme ssbd mba sev ibrs ibpb stibp vmmcall
    sev_es fsgsbase bmi1 avx2 smep bmi2 invpcid cqm rdt_a rdseed adx smap clflushopt
    clwb sha_ni xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
    cqm_mbm_local clzero irperf xsaveerptr wbnoinvd amd_ppin arat npt lbrv svm_lock
    nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold
    v_vmsave_vmload vgif umip pku ospke vaes vpclmulqdq rdpid overflow_recov succor smca

 /proc/cpuinfo cache data
    cache size : 512 KB
```

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Platform Notes (Continued)

```
 From numactl --hardware
 WARNING: a numactl 'node' might or might not correspond to a physical chip.
   available: 16 nodes (0-15)
   node 0 cpus: 0 1 2 3 64 65 66 67
   node 0 size: 32062 MB
   node 0 free: 31945 MB
   node 1 cpus: 4 5 6 7 68 69 70 71
   node 1 size: 32252 MB
   node 1 free: 32150 MB
   node 2 cpus: 8 9 10 11 72 73 74 75
   node 2 size: 32254 MB
   node 2 free: 32170 MB
   node 3 cpus: 12 13 14 15 76 77 78 79
   node 3 size: 32253 MB
   node 3 free: 32176 MB
   node 4 cpus: 16 17 18 19 80 81 82 83
   node 4 size: 32254 MB
   node 4 free: 32112 MB
   node 5 cpus: 20 21 22 23 84 85 86 87
   node 5 size: 32253 MB
   node 5 free: 32163 MB
   node 6 cpus: 24 25 26 27 88 89 90 91
   node 6 size: 32216 MB
   node 6 free: 32119 MB
   node 7 cpus: 28 29 30 31 92 93 94 95
   node 7 size: 32241 MB
   node 7 free: 32102 MB
   node 8 cpus: 32 33 34 35 96 97 98 99
   node 8 size: 32254 MB
   node 8 free: 32167 MB
   node 9 cpus: 36 37 38 39 100 101 102 103
   node 9 size: 32253 MB
   node 9 free: 32162 MB
   node 10 cpus: 40 41 42 43 104 105 106 107
   node 10 size: 32254 MB
   node 10 free: 32176 MB
   node 11 cpus: 44 45 46 47 108 109 110 111
   node 11 size: 32253 MB
   node 11 free: 32170 MB
   node 12 cpus: 48 49 50 51 112 113 114 115
   node 12 size: 32254 MB
   node 12 free: 32182 MB
   node 13 cpus: 52 53 54 55 116 117 118 119
   node 13 size: 32253 MB
   node 13 free: 32181 MB
   node 14 cpus: 56 57 58 59 120 121 122 123
```

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Tested by: Supermicro   **Software Availability:** May-2021

Platform Notes (Continued)

```
   node 14 size: 32254 MB
   node 14 free: 32055 MB
   node 15 cpus: 60 61 62 63 124 125 126 127
   node 15 size: 32252 MB
   node 15 free: 32027 MB
   node distances:
   node   0   1   2   3   4   5   6   7   8   9  10  11  12  13  14  15
     0:  10  11  12  12  12  12  12  12  32  32  32  32  32  32  32  32
     1:  11  10  12  12  12  12  12  12  32  32  32  32  32  32  32  32
     2:  12  12  10  11  12  12  12  12  32  32  32  32  32  32  32  32
     3:  12  12  11  10  12  12  12  12  32  32  32  32  32  32  32  32
     4:  12  12  12  12  10  11  12  12  32  32  32  32  32  32  32  32
     5:  12  12  12  12  11  10  12  12  32  32  32  32  32  32  32  32
     6:  12  12  12  12  12  12  10  11  32  32  32  32  32  32  32  32
     7:  12  12  12  12  12  12  11  10  32  32  32  32  32  32  32  32
     8:  32  32  32  32  32  32  32  32  10  11  12  12  12  12  12  12
     9:  32  32  32  32  32  32  32  32  11  10  12  12  12  12  12  12
    10:  32  32  32  32  32  32  32  32  12  12  10  11  12  12  12  12
    11:  32  32  32  32  32  32  32  32  12  12  11  10  12  12  12  12
    12:  32  32  32  32  32  32  32  32  12  12  12  12  10  11  12  12
    13:  32  32  32  32  32  32  32  32  12  12  12  12  11  10  12  12
    14:  32  32  32  32  32  32  32  32  12  12  12  12  12  12  10  11
    15:  32  32  32  32  32  32  32  32  12  12  12  12  12  12  11  10

 From /proc/meminfo
    MemTotal:       528195564 kB
    HugePages_Total:        0
    Hugepagesize:        2048 kB

 /sbin/tuned-adm active
     Current active profile: throughput-performance

 /sys/devices/system/cpu/cpu*/cpufreq/scaling_governor has
    performance

 From /etc/*release* /etc/*version*
    os-release:
      NAME="Red Hat Enterprise Linux"
      VERSION="8.4 (Ootpa)"
      ID="rhel"
      ID_LIKE="fedora"
      VERSION_ID="8.4"
      PLATFORM_ID="platform:el8"
      PRETTY_NAME="Red Hat Enterprise Linux 8.4 (Ootpa)"
      ANSI_COLOR="0;31"
    redhat-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
    system-release: Red Hat Enterprise Linux release 8.4 (Ootpa)
```

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Platform Notes (Continued)

```
   system-release-cpe: cpe:/o:redhat:enterprise_linux:8.4:ga


uname -a:
   Linux H12DSU-7543 4.18.0-305.el8.x86_64 #1 SMP Thu Apr 29 08:54:30 EDT 2021 x86_64
   x86_64 x86_64 GNU/Linux


Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):                   Not affected
CVE-2018-3620 (L1 Terminal Fault):                Not affected
Microarchitectural Data Sampling:                 Not affected
CVE-2017-5754 (Meltdown):                         Not affected
CVE-2018-3639 (Speculative Store Bypass):         Mitigation: Speculative Store
                                                  Bypass disabled via prctl and
                                                  seccomp
CVE-2017-5753 (Spectre variant 1):                Mitigation: usercopy/swapgs
                                                  barriers and __user pointer
                                                  sanitization
CVE-2017-5715 (Spectre variant 2):                Mitigation: Full AMD retpoline,
                                                 IBPB: conditional, IBRS_FW, STIBP:
                                                  always-on, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected
CVE-2019-11135 (TSX Asynchronous Abort):          Not affected


run-level 3 Oct 30 01:47


SPEC is set to: /home/cpu2017
   Filesystem      Type  Size  Used Avail Use% Mounted on
   /dev/sda4       xfs   184G   19G  166G  10% /


From /sys/devices/virtual/dmi/id
    Vendor:          Supermicro
    Product:         Super Server
    Serial:          0123456789


Additional information from dmidecode 3.2 follows.  WARNING: Use caution when you
interpret this section. The 'dmidecode' program reads system data which is "intended to
allow hardware to be accurately determined", but the intent may not be met, as there are
frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.
  Memory:
    16x SK Hynix HMAA4GR7AJR8N-XN 32 GB 2 rank 3200


BIOS:
    BIOS Vendor:       American Megatrends Inc.
    BIOS Version:      2.3
    BIOS Date:         10/12/2021
    BIOS Revision:     5.22
```

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Platform Notes (Continued)

```
  (End of data from sysinfo program)
```

Compiler Version Notes

```
==============================================================================
C        | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base,
         | peak) 625.x264_s(base, peak) 657.xz_s(base, peak)
------------------------------------------------------------------------------
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
  LLVM Mirror.Version.12.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin
------------------------------------------------------------------------------


==============================================================================
C++      | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)
         | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)
------------------------------------------------------------------------------
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
  LLVM Mirror.Version.12.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin
------------------------------------------------------------------------------


==============================================================================
Fortran | 648.exchange2_s(base, peak)
------------------------------------------------------------------------------
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
  LLVM Mirror.Version.12.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin
------------------------------------------------------------------------------
```

Base Compiler Invocation

C benchmarks:
```
clang
```

C++ benchmarks:
```
clang++
```

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Base Compiler Invocation (Continued)

Fortran benchmarks:
`flang`

Base Portability Flags

600.perlbench_s: `-DSPEC_LINUX_X64 -DSPEC_LP64`
602.gcc_s: `-DSPEC_LP64`
605.mcf_s: `-DSPEC_LP64`
620.omnetpp_s: `-DSPEC_LP64`
623.xalancbmk_s: `-DSPEC_LINUX -DSPEC_LP64`
625.x264_s: `-DSPEC_LP64`
631.deepsjeng_s: `-DSPEC_LP64`
641.leela_s: `-DSPEC_LP64`
648.exchange2_s: `-DSPEC_LP64`
657.xz_s: `-DSPEC_LP64`

Base Optimization Flags

C benchmarks:
```
-m64 -mno-adx -mno-sse4a -Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-enable-licm-vrp -Wl,-mllvm -Wl,-region-vectorize
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=5
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000
-fremap-arrays -mllvm -function-specialize -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3 -z muldefs
-DSPEC_OPENMP -fopenmp -fopenmp=libomp -lomp -lamdlibm -ljemalloc
-lflang -lflangrti
```

C++ benchmarks:
```
-m64 -std=c++98 -mno-adx -mno-sse4a
-Wl,-mllvm -Wl,-do-block-reorder=aggressive
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -flto -mllvm -enable-partial-unswitch
-mllvm -unroll-threshold=100 -finline-aggressive
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch
```

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Supermicro

A+ Server 1024US-TRT
(H12DSU-iN , AMD EPYC 7543)

SPECspeed®2017_int_base = 12.5

SPECspeed®2017_int_peak = 12.6

CPU2017 License: 001176
Test Sponsor: Supermicro
Tested by: Supermicro

Test Date: Oct-2021
Hardware Availability: Mar-2021
Software Availability: May-2021

Base Optimization Flags (Continued)

C++ benchmarks (continued):
```
-mllvm -extra-vectorizer-passes -mllvm -reduce-array-computations=3
-mllvm -global-vectorize-slp=true -mllvm -convert-pow-exp-to-int=false
-z muldefs -mllvm -do-block-reorder=aggressive
-fvirtual-function-elimination -fvisibility=hidden -DSPEC_OPENMP
-fopenmp -fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
-lflangrti
```

Fortran benchmarks:
```
-m64 -mno-adx -mno-sse4a -Wl,-mllvm -Wl,-inline-recursion=4
-Wl,-mllvm -Wl,-lsr-in-nested-loop -Wl,-mllvm -Wl,-enable-iv-split
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
-fveclib=AMDLIBM -ffast-math -flto -z muldefs
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -DSPEC_OPENMP
-fopenmp -fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
-lflangrti
```

Base Other Flags

C benchmarks:
```
-Wno-unused-command-line-argument -Wno-return-type
```

C++ benchmarks:
```
-Wno-unused-command-line-argument -Wno-return-type
```

Fortran benchmarks:
```
-Wno-return-type
```

Peak Compiler Invocation

C benchmarks:
```
clang
```

C++ benchmarks:
```
clang++
```

Fortran benchmarks:
```
flang
```

SPEC CPU®2017 Integer Speed Result

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Supermicro

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Peak Portability Flags

```
Same as Base Portability Flags
```

Peak Optimization Flags

C benchmarks:

```
 600.perlbench_s: -m64 -mno-adx -mno-sse4a -Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-enable-licm-vrp
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=5 -mllvm -unroll-threshold=50
-fremap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -fopenmp
-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang
```

```
 602.gcc_s: Same as 600.perlbench_s
```

```
 605.mcf_s: basepeak = yes
```

```
 625.x264_s: Same as 600.perlbench_s
```

```
 657.xz_s: Same as 600.perlbench_s
```

C++ benchmarks:

```
 620.omnetpp_s: basepeak = yes
```

```
 623.xalancbmk_s: basepeak = yes
```

```
 631.deepsjeng_s: basepeak = yes
```

```
 641.leela_s: basepeak = yes
```

Fortran benchmarks:
```
-m64 -mno-adx -mno-sse4a -Wl,-mllvm -Wl,-inline-recursion=4
-Wl,-mllvm -Wl,-lsr-in-nested-loop -Wl,-mllvm -Wl,-enable-iv-split
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3
```

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Supermicro

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Peak Optimization Flags (Continued)

Fortran benchmarks (continued):

```
-fveclib=AMDLIBM -ffast-math -flto -mllvm -unroll-aggressive
-mllvm -unroll-threshold=150 -DSPEC_OPENMP -fopenmp -fopenmp=libomp
-lomp -lamdlibm -ljemalloc -lflang
```

Peak Other Flags

C benchmarks:

```
-Wno-unused-command-line-argument -Wno-return-type
```

C++ benchmarks:

```
-Wno-unused-command-line-argument -Wno-return-type
```

Fortran benchmarks:

```
-Wno-return-type
```

The flags files that were used to format this result can be browsed at

```
http://www.spec.org/cpu2017/flags/aocc300-flags-B2.html
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Milan-revC.html
```

You can also download the XML flags sources by saving the following links:

```
http://www.spec.org/cpu2017/flags/aocc300-flags-B2.xml
http://www.spec.org/cpu2017/flags/Supermicro-Platform-Settings-V1.2-Milan-revC.xml
```

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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