



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL110 Gen10 Plus

(2.40 GHz, Intel Xeon Silver 4314)

SPECspeed®2017_int_base = 11.1

SPECspeed®2017_int_peak = 11.4

CPU2017 License: 3

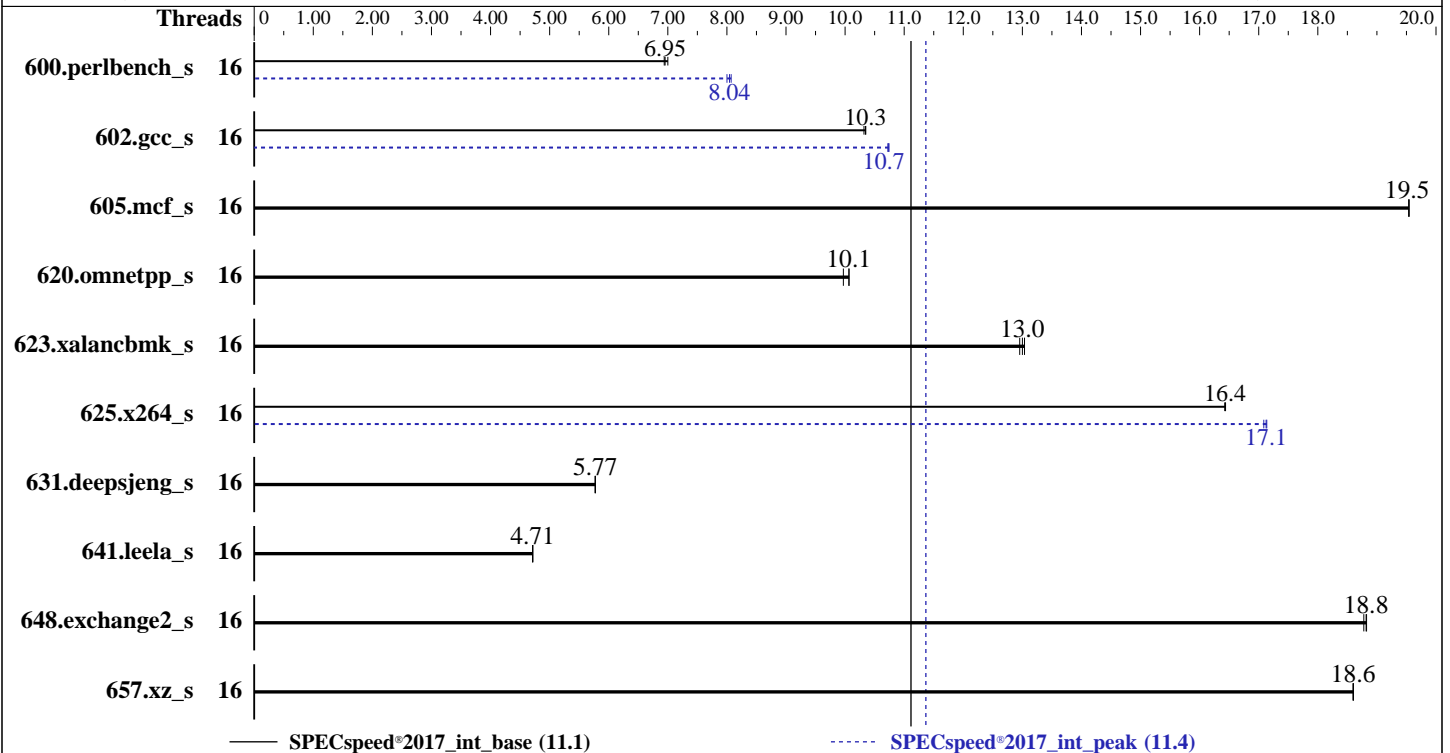
Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2021

Hardware Availability: Jun-2021

Software Availability: Jun-2021



Hardware

CPU Name: Intel Xeon Silver 4314
 Max MHz: 3400
 Nominal: 2400
 Enabled: 16 cores, 1 chip
 Orderable: 1 chip
 Cache L1: 32 KB I + 48 KB D on chip per core
 L2: 1.25 MB I+D on chip per core
 L3: 24 MB I+D on chip per chip
 Other: None
 Memory: 512 GB (8 x 64 GB 2Rx4 PC4-3200AA-R, running at 2666)
 Storage: 1 x 480 GB NVMe SSD, RAID 0
 Other: None

Software

OS: Red Hat Enterprise Linux 8.3 (Ootpa)
 Kernel 4.18.0-240.el8.x86_64
 Compiler: C/C++: Version 2021.1 of Intel oneAPI DPC++/C++ Compiler Build 20201113 for Linux;
 Fortran: Version 2021.1 of Intel Fortran Compiler Classic Build 20201112 for Linux;
 C/C++: Version 2021.1 of Intel C/C++ Compiler Classic Build 20201112 for Linux
 Parallel: Yes
 Firmware: HPE BIOS Version U56 v1.50 05/13/2021 released May-2021
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: jemalloc memory allocator V5.0.1
 Power Management: BIOS set to prefer performance at the cost of additional power usage



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL110 Gen10 Plus

(2.40 GHz, Intel Xeon Silver 4314)

SPECspeed®2017_int_base = 11.1

SPECspeed®2017_int_peak = 11.4

CPU2017 License: 3
Test Sponsor: HPE
Tested by: HPE

Test Date: Jun-2021
Hardware Availability: Jun-2021
Software Availability: Jun-2021

Results Table

| Benchmark | Base | | | | | | Peak | | | | | | | |
|-----------------|---------|------------|-------------|------------|-------------|------------|-------------|---------|---------|-------|------------|-------------|------------|-------------|
| | Threads | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Threads | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 600.perlbench_s | 16 | 255 | 6.95 | 254 | 7.00 | 256 | 6.94 | 16 | 222 | 8.00 | 220 | 8.07 | 221 | 8.04 |
| 602.gcc_s | 16 | 385 | 10.4 | 385 | 10.3 | 386 | 10.3 | 16 | 371 | 10.7 | 371 | 10.7 | 371 | 10.7 |
| 605.mcf_s | 16 | 242 | 19.5 | 242 | 19.5 | 242 | 19.5 | 16 | 242 | 19.5 | 242 | 19.5 | 242 | 19.5 |
| 620.omnetpp_s | 16 | 162 | 10.1 | 164 | 9.97 | 162 | 10.1 | 16 | 162 | 10.1 | 164 | 9.97 | 162 | 10.1 |
| 623.xalancbmk_s | 16 | 109 | 13.0 | 109 | 13.0 | 109 | 13.0 | 16 | 109 | 13.0 | 109 | 13.0 | 109 | 13.0 |
| 625.x264_s | 16 | 107 | 16.4 | 107 | 16.4 | 107 | 16.4 | 16 | 103 | 17.1 | 103 | 17.1 | 103 | 17.1 |
| 631.deepsjeng_s | 16 | 248 | 5.77 | 248 | 5.77 | 248 | 5.77 | 16 | 248 | 5.77 | 248 | 5.77 | 248 | 5.77 |
| 641.leela_s | 16 | 362 | 4.71 | 362 | 4.71 | 362 | 4.71 | 16 | 362 | 4.71 | 362 | 4.71 | 362 | 4.71 |
| 648.exchange2_s | 16 | 156 | 18.8 | 156 | 18.8 | 157 | 18.8 | 16 | 156 | 18.8 | 156 | 18.8 | 157 | 18.8 |
| 657.xz_s | 16 | 332 | 18.6 | 332 | 18.6 | 332 | 18.6 | 16 | 332 | 18.6 | 332 | 18.6 | 332 | 18.6 |

SPECspeed®2017_int_base = **11.1**

SPECspeed®2017_int_peak = **11.4**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Prior to runcpu invocation
Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches
```

Environment Variables Notes

```
Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,scatter"
LD_LIBRARY_PATH = "/cpu2017/lib/intel64:/cpu2017/je5.0.1-64"
MALLOC_CONF = "retain:true"
OMP_STACKSIZE = "192M"
```

General Notes

```
Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM
memory using Redhat Enterprise Linux 8.0
NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.
Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.
jemalloc, a general purpose malloc implementation
built with the RedHat Enterprise 7.5, and the system compiler gcc 4.8.5
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL110 Gen10 Plus

(2.40 GHz, Intel Xeon Silver 4314)

SPECspeed®2017_int_base = 11.1

SPECspeed®2017_int_peak = 11.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2021

Hardware Availability: Jun-2021

Software Availability: Jun-2021

General Notes (Continued)

sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Submitted_by: "Bhatnagar, Prateek" <prateek.bhatnagar@hpe.com>

Submitted: Mon Jun 21 10:34:24 EDT 2021

Submission: cpu2017-20210621-27594.sub

Platform Notes

The system ROM used for this result contains Intel microcode version 0xd0002a0 for the Intel Xeon Silver 4314 processor.

BIOS Configuration:

Workload Profile set to General Peak Frequency Compute

Intel Hyper-Threading set to Disabled

Thermal Configuration set to Maximum Cooling

Memory Patrol Scrubbing set to Disabled

Advanced Memory Protection set to Advanced ECC

Last Level Cache (LLC) Prefetch set to Enabled

Last Level Cache (LLC) Dead Line Allocation set to Disabled

Enhanced Processor Performance set to Enabled

Workload Profile set to Custom

Energy/Performance Bias set to Balanced Power

DCU Stream Prefetcher set to Disabled

Adjacent Sector Prefetch set to Disabled

Minimum Processor Idle Power Package C-State set to No Package State

Numa Group Size Optimization set to Flat

Sysinfo program /cpu2017/bin/sysinfo

Rev: r6622 of 2021-04-07 982a61ec0915b55891ef0e16acafc64d

running on localhost.localdomain Tue Jun 15 06:23:47 2021

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Silver 4314 CPU @ 2.40GHz

1 "physical id"s (chips)

16 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 16

siblings : 16

physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15

From lscpu from util-linux 2.32.1:

Architecture: x86_64

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL110 Gen10 Plus

(2.40 GHz, Intel Xeon Silver 4314)

SPECspeed®2017_int_base = 11.1

SPECspeed®2017_int_peak = 11.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2021

Hardware Availability: Jun-2021

Software Availability: Jun-2021

Platform Notes (Continued)

```

CPU op-mode(s):      32-bit, 64-bit
Byte Order:          Little Endian
CPU(s):              16
On-line CPU(s) list: 0-15
Thread(s) per core: 1
Core(s) per socket: 16
Socket(s):           1
NUMA node(s):       1
Vendor ID:           GenuineIntel
CPU family:          6
Model:               106
Model name:          Intel(R) Xeon(R) Silver 4314 CPU @ 2.40GHz
Stepping:            6
CPU MHz:             3388.662
BogoMIPS:            4800.00
Virtualization:      VT-x
L1d cache:           48K
L1i cache:           32K
L2 cache:            1280K
L3 cache:            24576K
NUMA node0 CPU(s):  0-15
Flags:                fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx f16c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 invpcid_single ssbd
mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid ept_ad
fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid cqm rdt_a avx512f avx512dq
rdseed adx smap avx512ifma clflushopt clwb intel_pt avx512cd sha_ni avx512bw
avx512v1 xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local split_lock_detect wbnoinvd dtherm ida arat pln pts avx512vbmi umip pku
ospke avx512_vbmi2 gfni vaes vpclmulqdq avx512_vnni avx512_bitalg tme
avx512_vpopcntdq la57 rdpid md_clear pconfig flush_lld arch_capabilities

```

```

/proc/cpuinfo cache data
cache size : 24576 KB

```

```

From numactl --hardware
WARNING: a numactl 'node' might or might not correspond to a physical chip.
available: 1 nodes (0)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
node 0 size: 501377 MB
node 0 free: 514149 MB
node distances:
node 0
0: 10

```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL110 Gen10 Plus

(2.40 GHz, Intel Xeon Silver 4314)

SPECspeed®2017_int_base = 11.1

SPECspeed®2017_int_peak = 11.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2021

Hardware Availability: Jun-2021

Software Availability: Jun-2021

Platform Notes (Continued)

From /proc/meminfo

MemTotal: 528055448 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

/sbin/tuned-adm active

Current active profile: throughput-performance

From /etc/*release* /etc/*version*

os-release:

NAME="Red Hat Enterprise Linux"

VERSION="8.3 (Ootpa)"

ID="rhel"

ID_LIKE="fedora"

VERSION_ID="8.3"

PLATFORM_ID="platform:el8"

PRETTY_NAME="Red Hat Enterprise Linux 8.3 (Ootpa)"

ANSI_COLOR="0;31"

redhat-release: Red Hat Enterprise Linux release 8.3 (Ootpa)

system-release: Red Hat Enterprise Linux release 8.3 (Ootpa)

system-release-cpe: cpe:/o:redhat:enterprise_linux:8.3:ga

uname -a:

Linux localhost.localdomain 4.18.0-240.el8.x86_64 #1 SMP Wed Sep 23 05:13:10 EDT 2020

x86_64 x86_64 x86_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-12207 (iTLB Multihit):

Not affected

CVE-2018-3620 (L1 Terminal Fault):

Not affected

Microarchitectural Data Sampling:

Not affected

CVE-2017-5754 (Meltdown):

Not affected

CVE-2018-3639 (Speculative Store Bypass):

Mitigation: Speculative Store Bypass disabled via prctl and seccomp

CVE-2017-5753 (Spectre variant 1):

Mitigation: usercopy/swapgs barriers and __user pointer sanitization

CVE-2017-5715 (Spectre variant 2):

Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

CVE-2020-0543 (Special Register Buffer Data Sampling):

Not affected

CVE-2019-11135 (TSX Asynchronous Abort):

Not affected

run-level 3 Jun 15 06:11

SPEC is set to: /cpu2017

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL110 Gen10 Plus

(2.40 GHz, Intel Xeon Silver 4314)

SPECspeed®2017_int_base = 11.1

SPECspeed®2017_int_peak = 11.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2021

Hardware Availability: Jun-2021

Software Availability: Jun-2021

Platform Notes (Continued)

| Filesystem | Type | Size | Used | Avail | Use% | Mounted on |
|----------------|------|------|------|-------|------|------------|
| /dev/nvme1nlp4 | xfs | 442G | 144G | 299G | 33% | / |

```

From /sys/devices/virtual/dmi/id
Vendor:          HPE
Product:         ProLiant DL110 Gen10 Plus
Product Family: ProLiant
Serial:          T912PP0032

```

Additional information from dmidecode 3.2 follows. **WARNING:** Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:
 8x Micron 36ASF8G72PZ-3G2B2 64 GB 2 rank 3200, configured at 2666

```

BIOS:
  BIOS Vendor:      HPE
  BIOS Version:     U56
  BIOS Date:        05/13/2021
  BIOS Revision:    1.50
  Firmware Revision: 2.40

```

(End of data from sysinfo program)

Compiler Version Notes

```

=====
C          | 600.perlbench_s(peak)
-----

```

```

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R)
64, Version 2021.1 Build 20201112_000000
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----

```

```

=====
C          | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
          | 625.x264_s(base, peak) 657.xz_s(base, peak)
-----

```

```

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64,
Version 2021.1 Build 20201113
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.
-----

```

```

=====
C          | 600.perlbench_s(peak)
-----

```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL110 Gen10 Plus

(2.40 GHz, Intel Xeon Silver 4314)

SPECspeed®2017_int_base = 11.1

SPECspeed®2017_int_peak = 11.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2021

Hardware Availability: Jun-2021

Software Availability: Jun-2021

Compiler Version Notes (Continued)

Intel(R) C Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====
C | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
| 625.x264_s(base, peak) 657.xz_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====
C++ | 620.omnetpp_s(base, peak) 623.xalancbmk_s(base, peak)
| 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)

Intel(R) oneAPI DPC++/C++ Compiler for applications running on Intel(R) 64, Version 2021.1 Build 20201113

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

=====
Fortran | 648.exchange2_s(base, peak)

Intel(R) Fortran Intel(R) 64 Compiler Classic for applications running on Intel(R) 64, Version 2021.1 Build 20201112_000000

Copyright (C) 1985-2020 Intel Corporation. All rights reserved.

Base Compiler Invocation

C benchmarks:

icx

C++ benchmarks:

icpx

Fortran benchmarks:

ifort



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL110 Gen10 Plus

(2.40 GHz, Intel Xeon Silver 4314)

SPECspeed®2017_int_base = 11.1

SPECspeed®2017_int_peak = 11.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2021

Hardware Availability: Jun-2021

Software Availability: Jun-2021

Base Portability Flags

```

600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

```

Base Optimization Flags

C benchmarks:

```

-DSPEC_OPENMP -std=c11 -m64 -fiopenmp -Wl,-z,muldefs -xCORE-AVX512
-O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-qopt-mem-layout-trans=4 -mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

```

C++ benchmarks:

```

-DSPEC_OPENMP -m64 -Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math
-flto -mfpmath=sse -funroll-loops -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/opt/intel/oneapi/compiler/2021.1.1/linux/compiler/lib/intel64_lin/
-lqkmalloc

```

Fortran benchmarks:

```

-m64 -xCORE-AVX512 -O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte -auto
-mbranches-within-32B-boundaries

```

Peak Compiler Invocation

C benchmarks (except as noted below):

icx

600.perlbench_s: icc

C++ benchmarks:

icpx

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL110 Gen10 Plus

(2.40 GHz, Intel Xeon Silver 4314)

SPECspeed®2017_int_base = 11.1

SPECspeed®2017_int_peak = 11.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2021

Hardware Availability: Jun-2021

Software Availability: Jun-2021

Peak Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -Wl,-z,muldefs -prof-gen(pass 1) -prof-use(pass 2)
-xCORE-AVX512 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=4 -fno-strict-overflow
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
602.gcc_s: -m64 -std=c11 -Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profddata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
605.mcf_s: basepeak = yes
```

```
625.x264_s: -DSPEC_OPENMP -fiopenmp -std=c11 -m64 -Wl,-z,muldefs
-xCORE-AVX512 -flto -O3 -ffast-math
-qopt-mem-layout-trans=4 -fno-alias
-mbranches-within-32B-boundaries
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

```
657.xz_s: basepeak = yes
```

C++ benchmarks:

```
620.omnetpp_s: basepeak = yes
```

```
623.xalancbmk_s: basepeak = yes
```

```
631.deepsjeng_s: basepeak = yes
```

(Continued on next page)



SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL110 Gen10 Plus

(2.40 GHz, Intel Xeon Silver 4314)

SPECspeed®2017_int_base = 11.1

SPECspeed®2017_int_peak = 11.4

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Jun-2021

Hardware Availability: Jun-2021

Software Availability: Jun-2021

Peak Optimization Flags (Continued)

641.leela_s:basepeak = yes

Fortran benchmarks:

648.exchange2_s:basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.0-ICX-revC.html>

http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.html

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.0-ICX-revC.xml>

http://www.spec.org/cpu2017/flags/Intel-ic2021-official-linux64_revA.xml

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU®2017 v1.1.8 on 2021-06-15 07:23:46-0400.

Report generated on 2021-07-06 18:45:17 by CPU2017 PDF formatter v6442.

Originally published on 2021-07-06.