



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2021 Standard Performance Evaluation Corporation

## ASUSTeK Computer Inc.

ASUS RS720A-E11(KMPP-D32) Server System  
3.70 GHz, AMD EPYC 72F3

SPECspeed®2017\_int\_base = 14.3

SPECspeed®2017\_int\_peak = 14.3

CPU2017 License: 9016

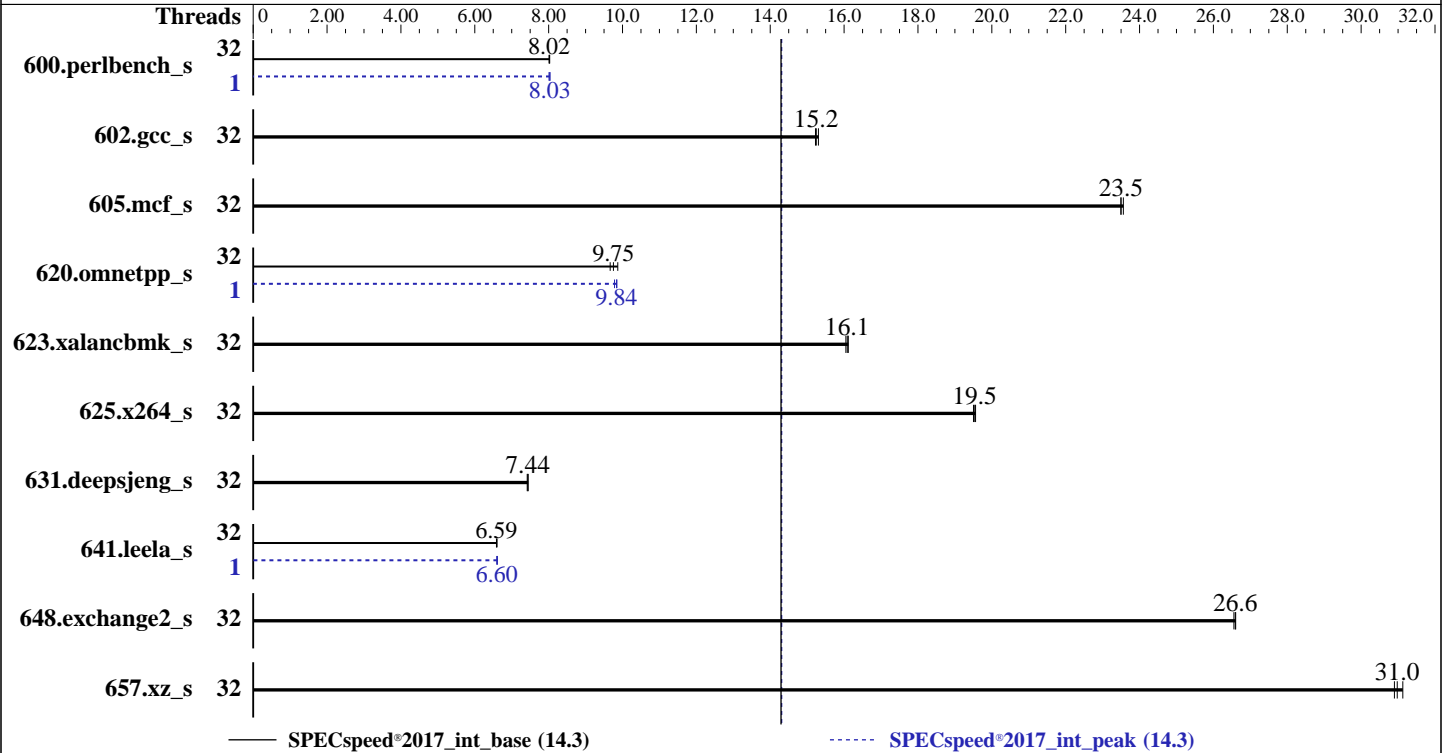
Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Feb-2021

Hardware Availability: Mar-2021

Software Availability: Mar-2021



### Hardware

CPU Name: AMD EPYC 72F3  
 Max MHz: 4100  
 Nominal: 3700  
 Enabled: 16 cores, 2 chips, 2 threads/core  
 Orderable: 1,2 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 512 KB I+D on chip per core  
 L3: 256 MB I+D on chip per chip, 32 MB per core  
 Other: None  
 Memory: 1 TB (16 x 64 GB 2Rx4 PC4-3200AA-R)  
 Storage: 1 x 1 TB SATA SSD  
 Other: None

### Software

OS: Ubuntu 20.04 LTS (x86\_64)  
 Kernel 5.4.0-59-generic  
 Compiler: C/C++/Fortran: Version 3.0.0 of AOCC  
 Parallel: Yes  
 Firmware: Version 0404 released Feb-2021  
 File System: ext4  
 System State: Run level 5 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc: jemalloc memory allocator library v5.1.0  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



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## Results Table

Benchmark	Base						Peak							
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	32	<b>221</b>	<b>8.02</b>	221	8.02	221	8.03	1	221	8.04	<b>221</b>	<b>8.03</b>	222	8.01
602.gcc_s	32	262	15.2	260	15.3	<b>261</b>	<b>15.2</b>	32	262	15.2	260	15.3	<b>261</b>	<b>15.2</b>
605.mcf_s	32	201	23.5	<b>201</b>	<b>23.5</b>	200	23.6	32	201	23.5	<b>201</b>	<b>23.5</b>	200	23.6
620.omnetpp_s	32	169	9.67	165	9.87	<b>167</b>	<b>9.75</b>	1	167	9.78	<b>166</b>	<b>9.84</b>	166	9.85
623.xalancbmk_s	32	<b>88.1</b>	<b>16.1</b>	88.3	16.0	87.9	16.1	32	<b>88.1</b>	<b>16.1</b>	88.3	16.0	87.9	16.1
625.x264_s	32	<b>90.3</b>	<b>19.5</b>	90.4	19.5	90.2	19.6	32	<b>90.3</b>	<b>19.5</b>	90.4	19.5	90.2	19.6
631.deepsjeng_s	32	<b>193</b>	<b>7.44</b>	193	7.44	193	7.42	32	<b>193</b>	<b>7.44</b>	193	7.44	193	7.42
641.leela_s	32	<b>259</b>	<b>6.59</b>	259	6.59	258	6.60	1	258	6.61	<b>258</b>	<b>6.60</b>	259	6.60
648.exchange2_s	32	<b>111</b>	<b>26.6</b>	111	26.6	111	26.6	32	<b>111</b>	<b>26.6</b>	111	26.6	111	26.6
657.xz_s	32	<b>200</b>	<b>31.0</b>	199	31.1	200	30.9	32	<b>200</b>	<b>31.0</b>	199	31.1	200	30.9

SPECspeed®2017\_int\_base = **14.3**

SPECspeed®2017\_int\_peak = **14.3**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The AMD64 AOCC Compiler Suite is available at  
<http://developer.amd.com/amd-aocc/>

## Submit Notes

The config file option 'submit' was used.  
'numactl' was used to bind copies to the cores.  
See the configuration file for details.

## Operating System Notes

```
'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit
runcpu command invoked through numactl i.e.:
numactl --interleave=all runcpu <etc>
'echo 8 > /proc/sys/vm/dirty_ratio' run as root to limit dirty cache to 8% of
memory.
'echo 1 > /proc/sys/vm/swappiness' run as root to limit swap usage to minimum
necessary.
'echo 1 > /proc/sys/vm/zone_reclaim_mode' run as root to free node-local memory
and avoid remote memory usage.
'sync; echo 3 > /proc/sys/vm/drop_caches' run as root to reset filesystem caches.
'sysctl -w kernel.randomize_va_space=0' run as root to disable address space layout
randomization (ASLR) to reduce run-to-run variability.
'echo always > /sys/kernel/mm/transparent_hugepage/enabled' and
'echo always > /sys/kernel/mm/transparent_hugepage/defrag' run as root to enable
```

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## Operating System Notes (Continued)

Transparent Hugepages (THP) for this run.

'echo madvise > /sys/kernel/mm/transparent\_hugepage/enabled' run as root for peak runs of 628.pop2\_s and 638.imagick\_s to enable THP only on request.

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:

GOMP\_CPU\_AFFINITY = "0-31"

LD\_LIBRARY\_PATH =

"/spec2017B1/amd\_speed\_aocc300\_milan\_B\_lib/64;/spec2017B1/amd\_speed\_aocc300\_milan\_B\_lib/32:"

MALLOC\_CONF = "retain:true"

OMP\_DYNAMIC = "false"

OMP\_SCHEDULE = "static"

OMP\_STACKSIZE = "128M"

OMP\_THREAD\_LIMIT = "32"

## General Notes

Binaries were compiled on a system with 2x AMD EPYC 7742 CPU + 1TiB Memory using opensUSE 15.2

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v4.8.2 in RHEL 7.4 (No options specified)

jemalloc 5.1.0 is available here:

<https://github.com/jemalloc/jemalloc/releases/download/5.1.0/jemalloc-5.1.0.tar.bz2>

## Platform Notes

BIOS Configuration:

DLWM Support = Disabled

SVM Mode = Disabled

NUMA nodes per socket = NPS2

APBDIS = 1

Fix SOC P-state = P0

Engine Boost = Enabled

Sysinfo program /spec2017B1/bin/sysinfo

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### Platform Notes (Continued)

Rev: r6538 of 2020-09-24 e8664e66d2d7080afeaa89d4b38e2f1c  
running on test2 Fri Feb 26 14:52:22 2021

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : AMD EPYC 72F3 8-Core Processor
 2 "physical id"s (chips)
32 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 8
siblings  : 16
physical 0: cores 0 1 2 3 4 5 6 7
physical 1: cores 0 1 2 3 4 5 6 7
```

From lscpu:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
Address sizes:         48 bits physical, 48 bits virtual
CPU(s):                32
On-line CPU(s) list:   0-31
Thread(s) per core:    2
Core(s) per socket:    8
Socket(s):             2
NUMA node(s):         4
Vendor ID:             AuthenticAMD
CPU family:            25
Model:                 1
Model name:            AMD EPYC 72F3 8-Core Processor
Stepping:              1
Frequency boost:       enabled
CPU MHz:               2893.809
CPU max MHz:           3700.0000
CPU min MHz:           1500.0000
BogoMIPS:              7473.54
Virtualization:        AMD-V
L1d cache:             512 KiB
L1i cache:             512 KiB
L2 cache:              8 MiB
L3 cache:              512 MiB
NUMA node0 CPU(s):    0-3,16-19
NUMA node1 CPU(s):    4-7,20-23
NUMA node2 CPU(s):    8-11,24-27
```

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### Platform Notes (Continued)

```

NUMA node3 CPU(s):          12-15,28-31
Vulnerability Itlb multihit: Not affected
Vulnerability L1tf:         Not affected
Vulnerability Mds:          Not affected
Vulnerability Meltdown:     Not affected
Vulnerability Spec store bypass: Mitigation; Speculative Store Bypass disabled via prctl and seccomp
Vulnerability Spectre v1:    Mitigation; usercopy/swapgs barriers and __user pointer sanitization
Vulnerability Spectre v2:    Mitigation; Full AMD retpoline, IBPB conditional, IBRS_FW, STIBP always-on, RSB filling
Vulnerability Srbds:         Not affected
Vulnerability Tsx async abort: Not affected
Flags:                        fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm constant_tsc rep_good nopl nonstop_tsc cpuid extd_apicid aperfmperf pni pclmulqdq monitor ssse3 fma cx16 pcid sse4_1 sse4_2 movbe popcnt aes xsave avx f16c rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch osvw ibs skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_llc mwaitx cpb cat_l3 cdp_l3 invpcid_single hw_pstate ssbd mba ibrs ibpb stibp vmmcall fsgsbase bmi1 avx2 smep bmi2 erms invpcid cqm rdt_a rdseed adx smap clflushopt clwb sha_ni xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total cqm_mbm_local clzero irperf xsaveerptr wbnoinvd arat npt lbrv svm_lock nrip_save tsc_scale vmcb_clean flushbyasid decodeassists pausefilter pfthreshold v_vmsave_vmload vgif umip pku ospke vaes vpclmulqdq rdpid overflow_recov succor smca

```

```

/proc/cpuinfo cache data
cache size : 512 KB

```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```

available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 16 17 18 19
node 0 size: 257811 MB
node 0 free: 257445 MB
node 1 cpus: 4 5 6 7 20 21 22 23
node 1 size: 258031 MB
node 1 free: 257643 MB
node 2 cpus: 8 9 10 11 24 25 26 27
node 2 size: 258043 MB
node 2 free: 257873 MB
node 3 cpus: 12 13 14 15 28 29 30 31
node 3 size: 258043 MB
node 3 free: 257875 MB
node distances:
node  0  1  2  3
0:    10  12  32  32

```

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### Platform Notes (Continued)

```

1:  12  10  32  32
2:  32  32  10  12
3:  32  32  12  10

```

From /proc/meminfo

```

MemTotal:      1056697844 kB
HugePages_Total:       0
Hugepagesize:       2048 kB

```

/sys/devices/system/cpu/cpu\*/cpufreq/scaling\_governor has performance

/usr/bin/lsb\_release -d  
Ubuntu 20.04 LTS

From /etc/\*release\* /etc/\*version\*

```

debian_version: bullseye/sid
os-release:
  NAME="Ubuntu"
  VERSION="20.04 LTS (Focal Fossa)"
  ID=ubuntu
  ID_LIKE=debian
  PRETTY_NAME="Ubuntu 20.04 LTS"
  VERSION_ID="20.04"
  HOME_URL="https://www.ubuntu.com/"
  SUPPORT_URL="https://help.ubuntu.com/"

```

uname -a:

```

Linux test2 5.4.0-59-generic #65-Ubuntu SMP Thu Dec 10 12:01:51 UTC 2020 x86_64 x86_64
x86_64 GNU/Linux

```

Kernel self-reported vulnerability status:

```

CVE-2018-12207 (iTLB Multihit):          Not affected
CVE-2018-3620 (L1 Terminal Fault):       Not affected
Microarchitectural Data Sampling:       Not affected
CVE-2017-5754 (Meltdown):                Not affected
CVE-2018-3639 (Speculative Store Bypass): Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):       Mitigation: usercopy/swapgs barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):       Mitigation: Full AMD retpoline, IBPB: conditional, IBRS_FW, STIBP: always-on, RSB filling
CVE-2020-0543 (Special Register Buffer Data Sampling): Not affected

```

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### Platform Notes (Continued)

CVE-2019-11135 (TSX Asynchronous Abort): Not affected

run-level 5 Feb 26 14:49

SPEC is set to: /spec2017B1

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/sdb2	ext4	938G	31G	859G	4%	/

```

From /sys/devices/virtual/dmi/id
Vendor:          ASUSTeK COMPUTER INC.
Product:         RS700A-E11-RS12U
Product Family: Server
Serial:          123456789ABCDEF

```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```

Memory:
 16x Samsung M393A8G40AB2-CWE 64 GB 2 rank 3200
 16x Unknown Unknown

```

```

BIOS:
  BIOS Vendor:      American Megatrends Inc.
  BIOS Version:     0404
  BIOS Date:        02/02/2021
  BIOS Revision:    4.4

```

(End of data from sysinfo program)

### Compiler Version Notes

```

=====
C      | 600.perlbench_s(base, peak) 602.gcc_s(base, peak) 605.mcf_s(base,
      | peak) 625.x264_s(base, peak) 657.xz_s(base, peak)

```

```

-----
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
  LLVM Mirror.Version.12.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin
-----

```

```

=====
C++   | 620.omnetpp_s(base, peak) 623.xalanbmk_s(base, peak)
      | 631.deepsjeng_s(base, peak) 641.leela_s(base, peak)

```

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```

-----
AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
  LLVM Mirror.Version.12.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin
-----

```

```

=====
Fortran | 648.exchange2_s(base, peak)
-----

```

```

AMD clang version 12.0.0 (CLANG: AOCC_3.0.0-Build#78 2020_12_10) (based on
  LLVM Mirror.Version.12.0.0)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /opt/AMD/aocc-compiler-3.0.0/bin
-----

```

## Base Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Base Portability Flags

```

600.perlbench_s: -DSPEC_LINUX_X64 -DSPEC_LP64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LINUX -DSPEC_LP64
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64

```





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## Base Optimization Flags

C benchmarks:

```
-m64 -mno-adx -mno-sse4a -Wl,-allow-multiple-definition  
-Wl,-mllvm -Wl,-enable-licm-vrp -Wl,-mllvm -Wl,-region-vectorize  
-Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3  
-fveclib=AMDLIBM -ffast-math -flto -fstruct-layout=5  
-mllvm -unroll-threshold=50 -mllvm -inline-threshold=1000  
-fremap-arrays -mllvm -function-specialize -flv-function-specialization  
-mllvm -enable-gvn-hoist -mllvm -global-vectorize-slp=true  
-mllvm -enable-licm-vrp -mllvm -reduce-array-computations=3 -z muldefs  
-DSPEC_OPENMP -fopenmp -fopenmp=libomp -lomp -lamdlibm -ljemalloc  
-lflang -lflangrti
```

C++ benchmarks:

```
-m64 -std=c++98 -mno-adx -mno-sse4a  
-Wl,-mllvm -Wl,-do-block-reorder=aggressive  
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3  
-fveclib=AMDLIBM -ffast-math -flto -mllvm -enable-partial-unswitch  
-mllvm -unroll-threshold=100 -finline-aggressive  
-flv-function-specialization -mllvm -loop-unswitch-threshold=200000  
-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch  
-mllvm -extra-vectorizer-passes -mllvm -reduce-array-computations=3  
-mllvm -global-vectorize-slp=true -mllvm -convert-pow-exp-to-int=false  
-z muldefs -mllvm -do-block-reorder=aggressive  
-fvirtual-function-elimination -fvisibility=hidden -DSPEC_OPENMP  
-fopenmp -fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang  
-lflangrti
```

Fortran benchmarks:

```
-m64 -mno-adx -mno-sse4a -Wl,-mllvm -Wl,-inline-recursion=4  
-Wl,-mllvm -Wl,-lsr-in-nested-loop -Wl,-mllvm -Wl,-enable-iv-split  
-Wl,-mllvm -Wl,-region-vectorize -Wl,-mllvm -Wl,-function-specialize  
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6  
-Wl,-mllvm -Wl,-reduce-array-computations=3 -O3 -march=znver3  
-fveclib=AMDLIBM -ffast-math -flto -z muldefs  
-mllvm -unroll-aggressive -mllvm -unroll-threshold=150 -DSPEC_OPENMP  
-fopenmp -fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang  
-lflangrti
```



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## Base Other Flags

C benchmarks:

-Wno-unused-command-line-argument -Wno-return-type

C++ benchmarks:

-Wno-unused-command-line-argument -Wno-return-type

Fortran benchmarks:

-Wno-return-type

## Peak Compiler Invocation

C benchmarks:

clang

C++ benchmarks:

clang++

Fortran benchmarks:

flang

## Peak Portability Flags

Same as Base Portability Flags

## Peak Optimization Flags

C benchmarks:

```
600.perlbench_s: -m64 -mno-adx -mno-sse4a -Wl,-allow-multiple-definition
-Wl,-mllvm -Wl,-enable-licm-vrp
-Wl,-mllvm -Wl,-function-specialize
-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6
-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast
-march=znver3 -fveclib=AMDLIBM -ffast-math -flto
-fstruct-layout=5 -mllvm -unroll-threshold=50
-fremap-arrays -flv-function-specialization
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-mllvm -global-vectorize-slp=true
-mllvm -function-specialize -mllvm -enable-licm-vrp
-mllvm -reduce-array-computations=3 -DSPEC_OPENMP -fopenmp
```

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# SPEC CPU®2017 Integer Speed Result

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## ASUSTeK Computer Inc.

ASUS RS720A-E11(KMPP-D32) Server System  
3.70 GHz, AMD EPYC 72F3

SPECspeed®2017\_int\_base = 14.3

SPECspeed®2017\_int\_peak = 14.3

CPU2017 License: 9016

Test Sponsor: ASUSTeK Computer Inc.

Tested by: ASUSTeK Computer Inc.

Test Date: Feb-2021

Hardware Availability: Mar-2021

Software Availability: Mar-2021

## Peak Optimization Flags (Continued)

600.perlbench\_s (continued):

-fopenmp=libomp -lomp -lamdlibm -ljemalloc -lflang

602.gcc\_s: basepeak = yes

605.mcf\_s: basepeak = yes

625.x264\_s: basepeak = yes

657.xz\_s: basepeak = yes

C++ benchmarks:

620.omnetpp\_s: -m64 -std=c++98 -mno-adx -mno-sse4a

-Wl,-mllvm -Wl,-do-block-reorder=aggressive

-Wl,-mllvm -Wl,-function-specialize

-Wl,-mllvm -Wl,-align-all-nofallthru-blocks=6

-Wl,-mllvm -Wl,-reduce-array-computations=3 -Ofast

-march=znver3 -fveclib=AMDLIBM -ffast-math -flto

-finline-aggressive -mllvm -unroll-threshold=100

-flv-function-specialization -mllvm -enable-licm-vrp

-mllvm -reroll-loops -mllvm -aggressive-loop-unswitch

-mllvm -reduce-array-computations=3

-mllvm -global-vectorize-slp=true

-mllvm -do-block-reorder=aggressive

-fvirtual-function-elimination -fvvisibility=hidden

-DSPEC\_OPENMP -fopenmp -fopenmp=libomp -lomp -lamdlibm

-ljemalloc -lflang

623.xalancbmk\_s: basepeak = yes

631.deepsjeng\_s: basepeak = yes

641.leela\_s: Same as 620.omnetpp\_s

Fortran benchmarks:

648.exchange2\_s: basepeak = yes

## Peak Other Flags

C benchmarks:

-Wno-unused-command-line-argument -Wno-return-type

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

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**ASUSTeK Computer Inc.**

ASUS RS720A-E11(KMPP-D32) Server System  
3.70 GHz, AMD EPYC 72F3

SPECspeed®2017\_int\_base = 14.3

SPECspeed®2017\_int\_peak = 14.3

**CPU2017 License:** 9016

**Test Sponsor:** ASUSTeK Computer Inc.

**Tested by:** ASUSTeK Computer Inc.

**Test Date:** Feb-2021

**Hardware Availability:** Mar-2021

**Software Availability:** Mar-2021

## Peak Other Flags (Continued)

C++ benchmarks:

-Wno-unused-command-line-argument -Wno-return-type

Fortran benchmarks:

-Wno-return-type

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-AMD-Milan-V1.2.html>

<http://www.spec.org/cpu2017/flags/aocc300-flags-A1.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/ASUSTekPlatform-Settings-AMD-Milan-V1.2.xml>

<http://www.spec.org/cpu2017/flags/aocc300-flags-A1.xml>

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Tested with SPEC CPU®2017 v1.1.5 on 2021-02-26 09:52:22-0500.

Report generated on 2021-03-16 15:30:26 by CPU2017 PDF formatter v6255.

Originally published on 2021-03-16.