



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

## Inspur Corporation

SPECspeed®2017\_int\_base = 10.8

### Inspur NF8260M5 (Intel Xeon Platinum 8260)

SPECspeed®2017\_int\_peak = 11.0

CPU2017 License: 3358

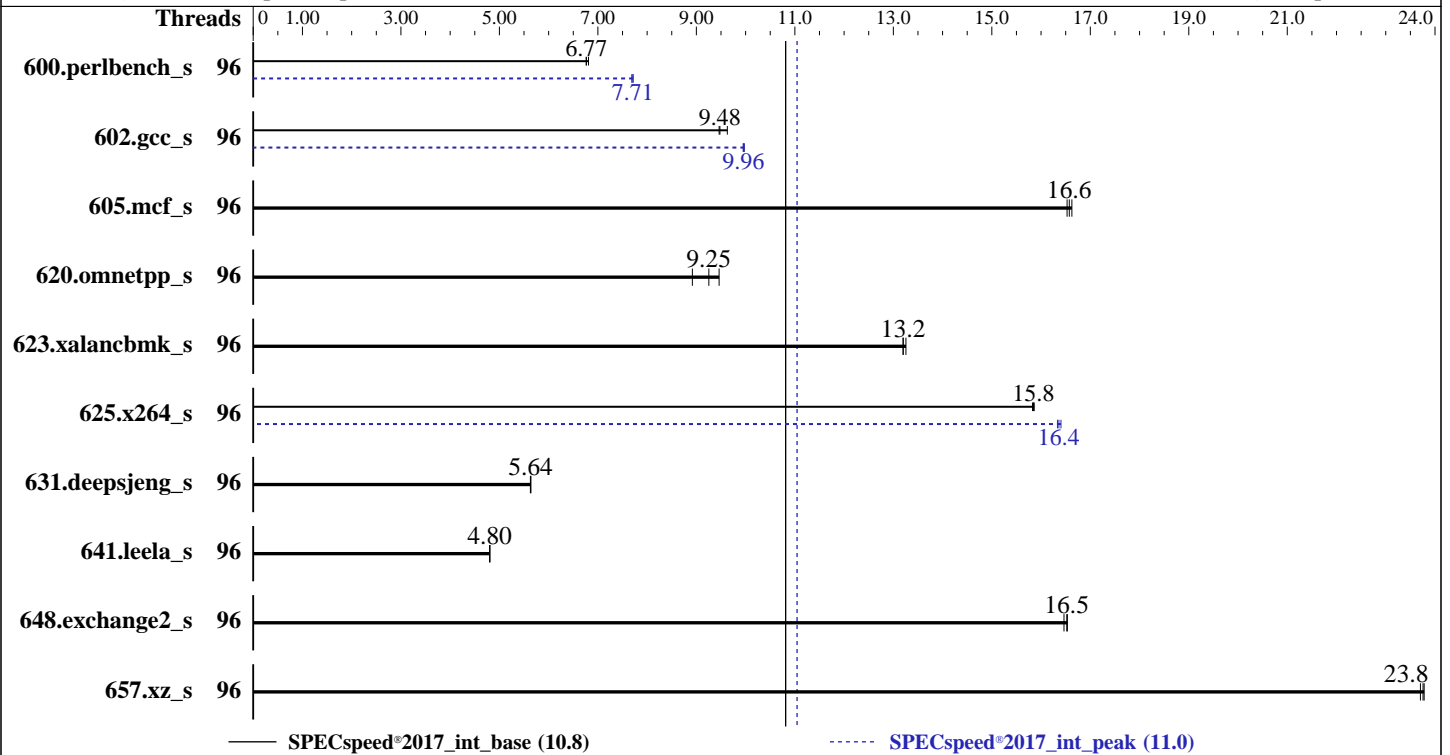
Test Sponsor: Inspur Corporation

Tested by: Inspur Corporation

Test Date: Sep-2020

Hardware Availability: Apr-2019

Software Availability: Apr-2020



### Hardware

CPU Name: Intel Xeon Platinum 8260  
 Max MHz: 3900  
 Nominal: 2400  
 Enabled: 96 cores, 4 chips  
 Orderable: 2,4 chips  
 Cache L1: 32 KB I + 32 KB D on chip per core  
 L2: 1 MB I+D on chip per core  
 L3: 35.75 MB I+D on chip per chip  
 Other: None  
 Memory: 1536 GB (48 x 32 GB 2Rx4 PC4-2933Y-R)  
 Storage: 1 x 1 TB SATA SSD  
 Other: None

### Software

OS: Red Hat Enterprise Linux release 8.1 (Ootpa)  
 4.18.0-147.el8.x86\_64  
 Compiler: C/C++: Version 19.1.1.217 of Intel C/C++  
 Compiler Build 20200306 for Linux;  
 Fortran: Version 19.1.1.217 of Intel Fortran  
 Compiler Build 20200306 for Linux  
 Parallel: Yes  
 Firmware: Version 4.1.8 released Jun-2019  
 File System: xfs  
 System State: Run level 5 (multi-user)  
 Base Pointers: 64-bit  
 Peak Pointers: 64-bit  
 Other: jemalloc memory allocator V5.0.1  
 Power Management: BIOS and OS set to prefer performance at the cost of additional power usage.



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

## Inspur Corporation

SPECSpeed®2017\_int\_base = 10.8

Inspur NF8260M5 (Intel Xeon Platinum 8260)

SPECSpeed®2017\_int\_peak = 11.0

CPU2017 License: 3358  
Test Sponsor: Inspur Corporation  
Tested by: Inspur Corporation

Test Date: Sep-2020  
Hardware Availability: Apr-2019  
Software Availability: Apr-2020

## Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
600.perlbench_s	96	<b><u>262</u></b>	<b><u>6.77</u></b>	261	6.81	262	6.76	96	230	7.72	<b><u>230</u></b>	<b><u>7.71</u></b>	231	7.69
602.gcc_s	96	414	9.63	<b><u>420</u></b>	<b><u>9.48</u></b>	421	9.46	96	399	9.98	400	9.95	<b><u>400</u></b>	<b><u>9.96</u></b>
605.mcf_s	96	<b><u>285</u></b>	<b><u>16.6</u></b>	286	16.5	284	16.6	96	<b><u>285</u></b>	<b><u>16.6</u></b>	286	16.5	284	16.6
620.omnetpp_s	96	172	9.46	183	8.92	<b><u>176</u></b>	<b><u>9.25</u></b>	96	172	9.46	183	8.92	<b><u>176</u></b>	<b><u>9.25</u></b>
623.xalancbmk_s	96	<b><u>107</u></b>	<b><u>13.2</u></b>	107	13.2	107	13.3	96	<b><u>107</u></b>	<b><u>13.2</u></b>	107	13.2	107	13.3
625.x264_s	96	111	15.9	111	15.8	<b><u>111</u></b>	<b><u>15.8</u></b>	96	108	16.4	<b><u>108</u></b>	<b><u>16.4</u></b>	108	16.3
631.deepsjeng_s	96	255	5.63	<b><u>254</u></b>	<b><u>5.64</u></b>	254	5.64	96	255	5.63	<b><u>254</u></b>	<b><u>5.64</u></b>	254	5.64
641.leela_s	96	355	4.81	<b><u>355</u></b>	<b><u>4.80</u></b>	355	4.80	96	355	4.81	<b><u>355</u></b>	<b><u>4.80</u></b>	355	4.80
648.exchange2_s	96	<b><u>178</u></b>	<b><u>16.5</u></b>	179	16.5	178	16.5	96	<b><u>178</u></b>	<b><u>16.5</u></b>	179	16.5	178	16.5
657.xz_s	96	260	23.8	261	23.7	<b><u>260</u></b>	<b><u>23.8</u></b>	96	260	23.8	261	23.7	<b><u>260</u></b>	<b><u>23.8</u></b>

SPECSpeed®2017\_int\_base = **10.8**

SPECSpeed®2017\_int\_peak = **11.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Compiler Notes

The inconsistent Compiler version information under Compiler Version section is due to a discrepancy in Intel Compiler. The correct version of C/C++ compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux  
The correct version of Fortran compiler is: Version 19.1.1.217 Build 20200306 Compiler for Linux

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
SCALING\_GOVERNOR set to Performance

## Environment Variables Notes

Environment variables set by runcpu before the start of the run:  
KMP\_AFFINITY = "granularity=fine,scatter"  
LD\_LIBRARY\_PATH = "/home/CPU2017/lib/intel64:/home/CPU2017/je5.0.1-64"  
MALLOC\_CONF = "retain:true"  
OMP\_STACKSIZE = "192M"



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

## Inspur Corporation

SPECspeed®2017\_int\_base = 10.8

Inspur NF8260M5 (Intel Xeon Platinum 8260)

SPECspeed®2017\_int\_peak = 11.0

**CPU2017 License:** 3358

**Test Sponsor:** Inspur Corporation

**Tested by:** Inspur Corporation

**Test Date:** Sep-2020

**Hardware Availability:** Apr-2019

**Software Availability:** Apr-2020

## General Notes

Binaries compiled on a system with 1x Intel Core i9-7980XE CPU + 64GB RAM  
 memory using Redhat Enterprise Linux 8.0  
 Transparent Huge Pages enabled by default  
 Prior to runcpu invocation  
 Filesystem page cache synced and cleared with:  
`sync; echo 3> /proc/sys/vm/drop_caches`  
 runcpu command invoked through numactl i.e.:  
`numactl --interleave=all runcpu <etc>`

NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.  
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.  
 Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc, a general purpose malloc implementation  
 built with the RedHat Enterprise 7.5,  
 and the system compiler gcc 4.8.5;  
 sources available from jemalloc.net or  
<https://github.com/jemalloc/jemalloc/releases>

## Platform Notes

BIOS configuration:  
 ENERGY\_PERF\_BIAS\_CFG mode set to Performance  
 Hardware Prefetch set to Disable  
 VT Support set to Disable  
 CLE Support set to Disable  
 IMC (Integrated memory controller) Interleaving set to 1-way  
 Sub NUMA Cluster (SNC) set to Enable  
 Intel Hyper Threading Technology set to Disable

Sysinfo program /home/CPU2017/bin/sysinfo  
 Rev: r6365 of 2019-08-21 295195f888a3d7edble6e46a485a0011  
 running on localhost.localdomain Fri Jun 22 07:15:33 2018

SUT (System Under Test) info as seen by some common utilities.  
 For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo  
 model name : Intel(R) Xeon(R) Platinum 8260 CPU @ 2.40GHz  
 4 "physical id"s (chips)  
 96 "processors"  
 cores, siblings (Caution: counting these is hw and system dependent. The following

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

## Inspur Corporation

SPECspeed®2017\_int\_base = 10.8

Inspur NF8260M5 (Intel Xeon Platinum 8260)

SPECspeed®2017\_int\_peak = 11.0

**CPU2017 License:** 3358  
**Test Sponsor:** Inspur Corporation  
**Tested by:** Inspur Corporation

**Test Date:** Sep-2020  
**Hardware Availability:** Apr-2019  
**Software Availability:** Apr-2020

### Platform Notes (Continued)

excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 24
siblings  : 24
physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29
physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29
physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 25 26 27 28 29
physical 3: cores 0 1 2 3 4 5 6 9 10 11 12 13 16 17 18 19 20 21 24 25 26 27 28 29
```

From lscpu:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:            Little Endian
CPU(s):                96
On-line CPU(s) list:   0-95
Thread(s) per core:    1
Core(s) per socket:    24
Socket(s):             4
NUMA node(s):         4
Vendor ID:             GenuineIntel
CPU family:            6
Model:                85
Model name:            Intel(R) Xeon(R) Platinum 8260 CPU @ 2.40GHz
Stepping:              7
CPU MHz:               999.457
CPU max MHz:           3900.0000
CPU min MHz:           1000.0000
BogoMIPS:              4800.00
Virtualization:        VT-x
L1d cache:             32K
L1i cache:             32K
L2 cache:              1024K
L3 cache:              36608K
NUMA node0 CPU(s):    0-23
NUMA node1 CPU(s):    24-47
NUMA node2 CPU(s):    48-71
NUMA node3 CPU(s):    72-95
Flags:                 fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp
lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc cpuid
aperfmpperf pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16
xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave
avx fl6c rdrand lahf_lm abm 3dnowprefetch cpuid_fault epb cat_l3 cdp_l3
invpcid_single intel_ppin ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi
flexpriority ept vpid fsgsbase tsc_adjust bmil hle avx2 smep bmi2 erms invpcid rtm
cqm mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb intel_pt avx512cd
avx512bw avx512vl xsaveopt xsavec xgetbv1 xsaves cqm_llc cqm_occup_llc cqm_mbm_total
cqm_mbm_local dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req pku
```

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

## Inspur Corporation

SPECspeed®2017\_int\_base = 10.8

Inspur NF8260M5 (Intel Xeon Platinum 8260)

SPECspeed®2017\_int\_peak = 11.0

CPU2017 License: 3358

Test Sponsor: Inspur Corporation

Tested by: Inspur Corporation

Test Date: Sep-2020

Hardware Availability: Apr-2019

Software Availability: Apr-2020

### Platform Notes (Continued)

ospke avx512\_vnni md\_clear flush\_llid arch\_capabilities

```
/proc/cpuinfo cache data
cache size : 36608 KB
```

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23
node 0 size: 385605 MB
node 0 free: 385379 MB
node 1 cpus: 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47
node 1 size: 387066 MB
node 1 free: 386707 MB
node 2 cpus: 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71
node 2 size: 387066 MB
node 2 free: 386877 MB
node 3 cpus: 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95
node 3 size: 387066 MB
node 3 free: 386621 MB
node distances:
node  0  1  2  3
 0:  10  21  21  21
 1:  21  10  21  21
 2:  21  21  10  21
 3:  21  21  21  10
```

```
From /proc/meminfo
MemTotal:      1583928912 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux"
VERSION="8.1 (Ootpa)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="8.1"
PLATFORM_ID="platform:el8"
PRETTY_NAME="Red Hat Enterprise Linux 8.1 (Ootpa)"
ANSI_COLOR="0;31"
redhat-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
system-release: Red Hat Enterprise Linux release 8.1 (Ootpa)
system-release-cpe: cpe:/o:redhat:enterprise_linux:8.1:ga
```

uname -a:

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

## Inspur Corporation

SPECspeed®2017\_int\_base = 10.8

Inspur NF8260M5 (Intel Xeon Platinum 8260)

SPECspeed®2017\_int\_peak = 11.0

**CPU2017 License:** 3358  
**Test Sponsor:** Inspur Corporation  
**Tested by:** Inspur Corporation

**Test Date:** Sep-2020  
**Hardware Availability:** Apr-2019  
**Software Availability:** Apr-2020

### Platform Notes (Continued)

Linux localhost.localdomain 4.18.0-147.el8.x86\_64 #1 SMP Thu Sep 26 15:52:44 UTC 2019  
x86\_64 x86\_64 x86\_64 GNU/Linux

Kernel self-reported vulnerability status:

CVE-2018-3620 (L1 Terminal Fault):	Not affected
Microarchitectural Data Sampling:	Not affected
CVE-2017-5754 (Meltdown):	Not affected
CVE-2018-3639 (Speculative Store Bypass):	Mitigation: Speculative Store Bypass disabled via prctl and seccomp
CVE-2017-5753 (Spectre variant 1):	Mitigation: usercopy/swaps barriers and __user pointer sanitization
CVE-2017-5715 (Spectre variant 2):	Mitigation: Enhanced IBRS, IBPB: conditional, RSB filling

run-level 5 Jun 22 07:11

SPEC is set to: /home/CPU2017

Filesystem	Type	Size	Used	Avail	Use%	Mounted on
/dev/mapper/rhel-home	xfs	838G	126G	713G	15%	/home

From /sys/devices/virtual/dmi/id

BIOS: American Megatrends Inc. 4.1.8 06/11/2019  
Vendor: Inspur  
Product: NF8260M5  
Serial: 220714936

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Memory:

48x Samsung M393A4G43AB3-CVF 32 GB 2 rank 2933

(End of data from sysinfo program)

### Compiler Version Notes

```
=====
C          | 600.perlbench_s(base) 602.gcc_s(base, peak) 605.mcf_s(base, peak)
          | 625.x264_s(base, peak) 657.xz_s(base, peak)
-----
```

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1  
NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

## Inspur Corporation

SPECspeed®2017\_int\_base = 10.8

Inspur NF8260M5 (Intel Xeon Platinum 8260)

SPECspeed®2017\_int\_peak = 11.0

CPU2017 License: 3358

Test Sponsor: Inspur Corporation

Tested by: Inspur Corporation

Test Date: Sep-2020

Hardware Availability: Apr-2019

Software Availability: Apr-2020

### Compiler Version Notes (Continued)

=====  
C | 600.perlbench\_s(peak)  
-----

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.1.1.217 Build 20200306  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C | 600.perlbench\_s(base) 602.gcc\_s(base, peak) 605.mcf\_s(base, peak)  
625.x264\_s(base, peak) 657.xz\_s(base, peak)

Intel(R) C Compiler for applications running on Intel(R) 64, Version 2021.1  
NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C | 600.perlbench\_s(peak)  
-----

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,  
Version 19.1.1.217 Build 20200306  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
C++ | 620.omnetpp\_s(base, peak) 623.xalancbmk\_s(base, peak)  
631.deepsjeng\_s(base, peak) 641.leela\_s(base, peak)

Intel(R) C++ Compiler for applications running on Intel(R) 64, Version 2021.1  
NextGen Build 20200304  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----

=====  
Fortran | 648.exchange2\_s(base, peak)  
-----

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)  
64, Version 19.1.1.217 Build 20200306  
Copyright (C) 1985-2020 Intel Corporation. All rights reserved.  
-----



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Inspur Corporation

SPECspeed®2017\_int\_base = 10.8

Inspur NF8260M5 (Intel Xeon Platinum 8260)

SPECspeed®2017\_int\_peak = 11.0

CPU2017 License: 3358

Test Sponsor: Inspur Corporation

Tested by: Inspur Corporation

Test Date: Sep-2020

Hardware Availability: Apr-2019

Software Availability: Apr-2020

## Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

## Base Portability Flags

```
600.perlbench_s: -DSPEC_LP64 -DSPEC_LINUX_X64
602.gcc_s: -DSPEC_LP64
605.mcf_s: -DSPEC_LP64
620.omnetpp_s: -DSPEC_LP64
623.xalancbmk_s: -DSPEC_LP64 -DSPEC_LINUX
625.x264_s: -DSPEC_LP64
631.deepsjeng_s: -DSPEC_LP64
641.leela_s: -DSPEC_LP64
648.exchange2_s: -DSPEC_LP64
657.xz_s: -DSPEC_LP64
```

## Base Optimization Flags

C benchmarks:

```
-m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries -Wl,-z,muldefs
-xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse -funroll-loops
-fuse-ld=gold -qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

C++ benchmarks:

```
-m64 -qnextgen -Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -O3 -ffast-math -flto -mfpmath=sse
-funroll-loops -fuse-ld=gold -qopt-mem-layout-trans=4
-L/usr/local/IntelCompiler19/compilers_and_libraries_2020.1.217/linux/compiler/lib/intel64_lin
-lqkmalloc
```

Fortran benchmarks:

```
-m64 -Wl,-plugin-opt=-x86-branches-within-32B-boundaries -xCORE-AVX512
-O3 -ipo -no-prec-div -qopt-mem-layout-trans=4
-nostandard-realloc-lhs -align array32byte
```

(Continued on next page)





# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Inspur Corporation

SPECspeed®2017\_int\_base = 10.8

Inspur NF8260M5 (Intel Xeon Platinum 8260)

SPECspeed®2017\_int\_peak = 11.0

CPU2017 License: 3358

Test Sponsor: Inspur Corporation

Tested by: Inspur Corporation

Test Date: Sep-2020

Hardware Availability: Apr-2019

Software Availability: Apr-2020

## Base Optimization Flags (Continued)

Fortran benchmarks (continued):

-mbranches-within-32B-boundaries

## Peak Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

## Peak Portability Flags

600.perlbench\_s: -DSPEC\_LP64 -DSPEC\_LINUX\_X64

602.gcc\_s: -DSPEC\_LP64(\*) -DSPEC\_LP64

605.mcf\_s: -DSPEC\_LP64

620.omnetpp\_s: -DSPEC\_LP64

623.xalancbmk\_s: -DSPEC\_LP64 -DSPEC\_LINUX

625.x264\_s: -DSPEC\_LP64

631.deepsjeng\_s: -DSPEC\_LP64

641.leela\_s: -DSPEC\_LP64

648.exchange2\_s: -DSPEC\_LP64

657.xz\_s: -DSPEC\_LP64

(\*) Indicates a portability flag that was found in a non-portability variable.

## Peak Optimization Flags

C benchmarks:

600.perlbench\_s: -w1, -z, muldefs -prof-gen(pass 1) -prof-use(pass 2)

-xCORE-AVX512 -ipo -O3 -no-prec-div

-qopt-mem-layout-trans=4 -fno-strict-overflow

-mbranches-within-32B-boundaries

-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc

(Continued on next page)



# SPEC CPU®2017 Integer Speed Result

Copyright 2017-2020 Standard Performance Evaluation Corporation

Inspur Corporation

SPECspeed®2017\_int\_base = 10.8

Inspur NF8260M5 (Intel Xeon Platinum 8260)

SPECspeed®2017\_int\_peak = 11.0

CPU2017 License: 3358

Test Sponsor: Inspur Corporation

Tested by: Inspur Corporation

Test Date: Sep-2020

Hardware Availability: Apr-2019

Software Availability: Apr-2020

## Peak Optimization Flags (Continued)

```
602.gcc_s: -m64 -qnextgen -std=c11 -fuse-ld=gold
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -fprofile-generate(pass 1)
-fprofile-use=default.profdata(pass 2) -xCORE-AVX512 -flto
-Ofast(pass 1) -O3 -ffast-math -qopt-mem-layout-trans=4
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

605.mcf\_s: basepeak = yes

```
625.x264_s: -m64 -qnextgen -std=c11
-Wl,-plugin-opt=-x86-branches-within-32B-boundaries
-Wl,-z,muldefs -xCORE-AVX512 -flto -O3 -ffast-math
-fuse-ld=gold -qopt-mem-layout-trans=4 -fno-alias
-L/usr/local/jemalloc64-5.0.1/lib -ljemalloc
```

657.xz\_s: basepeak = yes

C++ benchmarks:

620.omnetpp\_s: basepeak = yes

623.xalancbmk\_s: basepeak = yes

631.deepsjeng\_s: basepeak = yes

641.leela\_s: basepeak = yes

Fortran benchmarks:

648.exchange2\_s: basepeak = yes

The flags files that were used to format this result can be browsed at

[http://www.spec.org/cpu2017/flags/Intel-ic19.1ul-official-linux64\\_revA.html](http://www.spec.org/cpu2017/flags/Intel-ic19.1ul-official-linux64_revA.html)

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V1.9.html>

You can also download the XML flags sources by saving the following links:

[http://www.spec.org/cpu2017/flags/Intel-ic19.1ul-official-linux64\\_revA.xml](http://www.spec.org/cpu2017/flags/Intel-ic19.1ul-official-linux64_revA.xml)

<http://www.spec.org/cpu2017/flags/Inspur-Platform-Settings-V1.9.xml>

SPEC CPU and SPECspeed are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester. For other inquiries, please contact [info@spec.org](mailto:info@spec.org).

Tested with SPEC CPU®2017 v1.1.0 on 2018-06-22 07:15:32-0400.

Report generated on 2020-10-27 16:15:26 by CPU2017 PDF formatter v6255.

Originally published on 2020-10-27.