



SPEC® CPU2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Fujitsu

PRIMERGY RX4770 M5, Intel Xeon Platinum 8280L,
2.70GHz

SPECspeed2017_fp_base = 228

SPECspeed2017_fp_peak = Not Run

CPU2017 License: 19

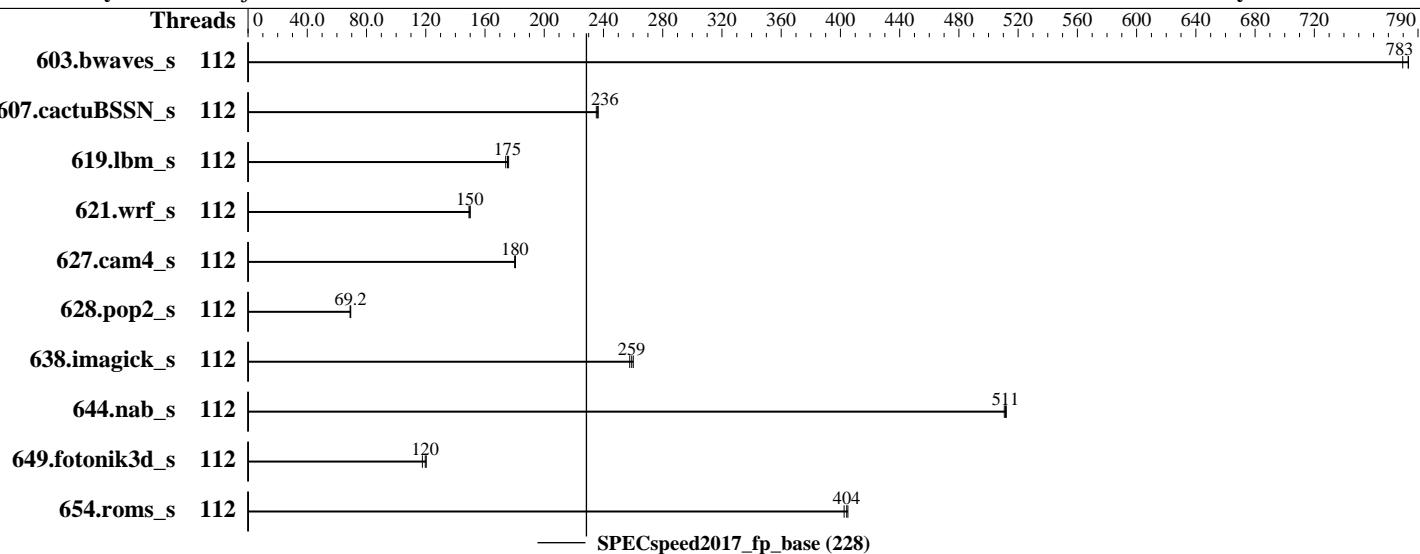
Test Date: Apr-2019

Test Sponsor: Fujitsu

Hardware Availability: May-2019

Tested by: Fujitsu

Software Availability: Feb-2019



Hardware

CPU Name: Intel Xeon Platinum 8280L
Max MHz.: 4000
Nominal: 2700
Enabled: 112 cores, 4 chips
Orderable: 2,4 chips
Cache L1: 32 KB I + 32 KB D on chip per core
L2: 1 MB I+D on chip per core
L3: 38.5 MB I+D on chip per chip
Other: None
Memory: 1536 GB (48 x 32 GB 2Rx4 PC4-2933Y-R)
Storage: 1 x SATA M.2 SSD, 480GB
Other: None

Software

OS: Red Hat Enterprise Linux Server release 7.6 (Maipo)
Compiler: 3.10.0-957.el7.x86_64
C/C++: Version 19.0.1.144 of Intel C/C++ Compiler for Linux;
Fortran: Version 19.0.1.144 of Intel Fortran Compiler for Linux
Parallel: Yes
Firmware: Fujitsu BIOS Version V5.0.0.14 R1.8.0 for D3753-C1x. Released Jun-2019 tested as V5.0.0.14 R1.3.0 for D3753-C1x Mar-2019
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other: None



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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	112	75.3	784	75.7	780	75.3	783							
607.cactuBSSN_s	112	70.5	236	70.9	235	70.6	236							
619.lbm_s	112	29.8	176	29.9	175			30.1	174					
621.wrf_s	112	88.1	150	88.7	149	88.3	150							
627.cam4_s	112	49.1	181	49.2	180			49.2	180					
628.pop2_s	112	171	69.3	172	68.9	172	69.2							
638.imagick_s	112	56.0	258	55.5	260	55.7	259							
644.nab_s	112	34.1	512	34.2	511	34.2	511							
649.fotonik3d_s	112	77.4	118	76.2	120			75.8	120					
654.roms_s	112	39.0	404	39.1	403	38.9	405							
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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Kernel Boot Parameter set with : nohz_full=1-111

General Notes

Environment variables set by runcpu before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/Benchmark/speccpu2017/lib/intel64"
OMP_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i9-7900X CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.5

Transparent Huge Pages disabled by default

```
echo never > /sys/kernel/mm/transparent_hugepage/enabled
echo never > /sys/kernel/mm/transparent_hugepage/defrag
```

Prior to runcpu invocation

Filesystem page cache synced and cleared with:
sync; echo 3> /proc/sys/vm/drop_caches

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown)
is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1)
is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2)
is mitigated in the system as tested and documented.



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Platform Notes

BIOS configuration:

Adjacent Cache Line Prefetch = Disabled

Energy Performance = Performance

Fan Control = Full

Hyper-Threading = Disabled

Intel Virtualization Technology = Disabled

Override OS Energy Performance = Enabled

Patrol Scrub = Disabled

Stale AtoS = Disabled

Sub NUMA Clustering = Disabled

WR CRC feature Control = Disabled

Sysinfo program /home/Benchmark/speccpu2017/bin/sysinfo

Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9
running on localhost.localdomain Tue Apr 16 00:58:13 2019

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Platinum 8280L CPU @ 2.70GHz

4 "physical id"s (chips)

112 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 28

siblings : 28

physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30

physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30

physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30

physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24 25 26 27
28 29 30

From lscpu:

Architecture: x86_64

CPU op-mode(s): 32-bit, 64-bit

Byte Order: Little Endian

CPU(s): 112

On-line CPU(s) list: 0-111

Thread(s) per core: 1

Core(s) per socket: 28

Socket(s): 4

NUMA node(s): 4

Vendor ID: GenuineIntel

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Platform Notes (Continued)

CPU family:	6
Model :	85
Model name:	Intel(R) Xeon(R) Platinum 8280L CPU @ 2.70GHz
Stepping:	6
CPU MHz:	999.810
CPU max MHz:	4000.0000
CPU min MHz:	1000.0000
BogoMIPS:	5400.00
Virtualization:	VT-x
L1d cache:	32K
L1i cache:	32K
L2 cache:	1024K
L3 cache:	39424K
NUMA node0 CPU(s):	0-27
NUMA node1 CPU(s):	28-55
NUMA node2 CPU(s):	56-83
NUMA node3 CPU(s):	84-111
Flags:	fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov pat pse36 clflush dts acpi mmx fxsr sse sse2 ss ht tm pbe syscall nx pdpe1gb rdtscp lm constant_tsc art arch_perfmon pebs bts rep_good nopl xtopology nonstop_tsc aperfmpfperf eagerfpu pni pclmulqdq dtes64 monitor ds_cpl vmx smx est tm2 ssse3 sdbg fma cx16 xtpr pdcm pcid dca sse4_1 sse4_2 x2apic movbe popcnt tsc_deadline_timer aes xsave avx f16c rdrand lahf_lm abm 3dnowprefetch epb cat_l3 cdp_l3 intel_pt ssbd mba ibrs ibpb stibp ibrs_enhanced tpr_shadow vnmi flexpriority ept vpid fsgsbase tsc_adjust bmi1 hle avx2 smep bmi2 erms invpcid rtm cqmq mpx rdt_a avx512f avx512dq rdseed adx smap clflushopt clwb avx512cd avx512bw avx512vl xsaveopt xsavexc xgetbv1 cqmq_llc cqmq_occup_llc cqmq_mbm_total cqmq_mbm_local dtherm ida arat pln pts hwp hwp_act_window hwp_epp hwp_pkg_req pku ospke avx512_vnni spec_ctrl intel_stibp flush_ll1d arch_capabilities

```
/proc/cpuinfo cache data
cache size : 39424 KB
```

```
From numactl --hardware  WARNING: a numactl 'node' might or might not correspond to a
physical chip.
```

```
available: 4 nodes (0-3)
node 0 cpus: 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27
node 0 size: 391762 MB
node 0 free: 382663 MB
node 1 cpus: 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52
53 54 55
node 1 size: 393216 MB
node 1 free: 384384 MB
node 2 cpus: 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80
81 82 83
node 2 size: 393216 MB
node 2 free: 383702 MB
```

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Platform Notes (Continued)

```
node 3 cpus: 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100 101 102 103 104 105  
106 107 108 109 110 111  
node 3 size: 393216 MB  
node 3 free: 384477 MB  
node distances:  
node 0 1 2 3  
0: 10 21 21 21  
1: 21 10 21 21  
2: 21 21 10 21  
3: 21 21 21 10
```

```
From /proc/meminfo  
MemTotal: 1583563460 kB  
HugePages_Total: 0  
Hugepagesize: 2048 kB
```

```
From /etc/*release* /etc/*version*
```

```
os-release:  
NAME="Red Hat Enterprise Linux Server"  
VERSION="7.6 (Maipo)"  
ID="rhel"  
ID_LIKE="fedora"  
VARIANT="Server"  
VARIANT_ID="server"  
VERSION_ID="7.6"  
PRETTY_NAME="Red Hat Enterprise Linux Server 7.6 (Maipo)"  
redhat-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)  
system-release: Red Hat Enterprise Linux Server release 7.6 (Maipo)  
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.6:ga:server
```

```
uname -a:  
Linux localhost.localdomain 3.10.0-957.el7.x86_64 #1 SMP Thu Oct 4 20:48:51 UTC 2018  
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

```
CVE-2017-5754 (Meltdown): Not affected  
CVE-2017-5753 (Spectre variant 1): Mitigation: Load fences, __user pointer sanitization  
CVE-2017-5715 (Spectre variant 2): Mitigation: Enhanced IBRS
```

run-level 3 Apr 15 23:36

```
SPEC is set to: /home/Benchmark/speccpu2017  
Filesystem Type Size Used Avail Use% Mounted on  
/dev/mapper/rhel-home xfs 392G 7.0G 385G 2% /home
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret

(Continued on next page)



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Platform Notes (Continued)

this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS FUJITSU // American Megatrends Inc. V5.0.0.14 R1.3.0 for D3753-C1x
03/15/2019

Memory:

48x Micron 36ASF4G72PZ-2G9E2 32 GB 2 rank 2933, configured at 2934

(End of data from sysinfo program)

Compiler Version Notes

=====

CC 619.lbm_s(base) 638.imagick_s(base) 644.nab_s(base)

=====

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====

=====

FC 607.cactuBSSN_s(base)

=====

Intel(R) C++ Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

Intel(R) C Intel(R) 64 Compiler for applications running on Intel(R) 64,
Version 19.0.1.144 Build 20181018

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Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018

Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====

=====

FC 603.bwaves_s(base) 649.fotonik3d_s(base) 654.roms_s(base)

=====

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)
64, Version 19.0.1.144 Build 20181018
Copyright (C) 1985-2018 Intel Corporation. All rights reserved.

=====

=====

CC 621.wrf_s(base) 627.cam4_s(base) 628.pop2_s(base)

=====

Intel(R) Fortran Intel(R) 64 Compiler for applications running on Intel(R)

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Base Compiler Invocation

C benchmarks:

icc -m64 -std=c11

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

ifort -m64 icc -m64 -std=c11

Benchmarks using Fortran, C, and C++:

icpc -m64 icc -m64 -std=c11 ifort -m64

Base Portability Flags

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=4 -qopenmp -DSPEC_OPENMP

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Base Optimization Flags (Continued)

Fortran benchmarks:

```
-DSPEC_OPENMP -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -fopenmp  
-nostandard-realloc-lhs
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP  
-nostandard-realloc-lhs
```

Benchmarks using Fortran, C, and C++:

```
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch  
-ffinite-math-only -qopt-mem-layout-trans=4 -fopenmp -DSPEC_OPENMP  
-nostandard-realloc-lhs
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2019-04-02.html>

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-CSL-RevC.2019-04-30.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2019-04-02.xml>

<http://www.spec.org/cpu2017/flags/Fujitsu-Platform-Settings-V1.0-CSL-RevC.2019-04-30.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

Tested with SPEC CPU2017 v1.0.5 on 2019-04-15 11:58:12-0400.

Report generated on 2019-05-23 11:54:45 by CPU2017 PDF formatter v6067.

Originally published on 2019-04-30.