



SPEC® CPU2017 Floating Point Speed Result

Copyright 2017-2019 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen10

(3.10 GHz, AMD EPYC 7371)

SPECspeed2017_fp_base = 70.8

SPECspeed2017_fp_peak = 70.9

CPU2017 License: 3

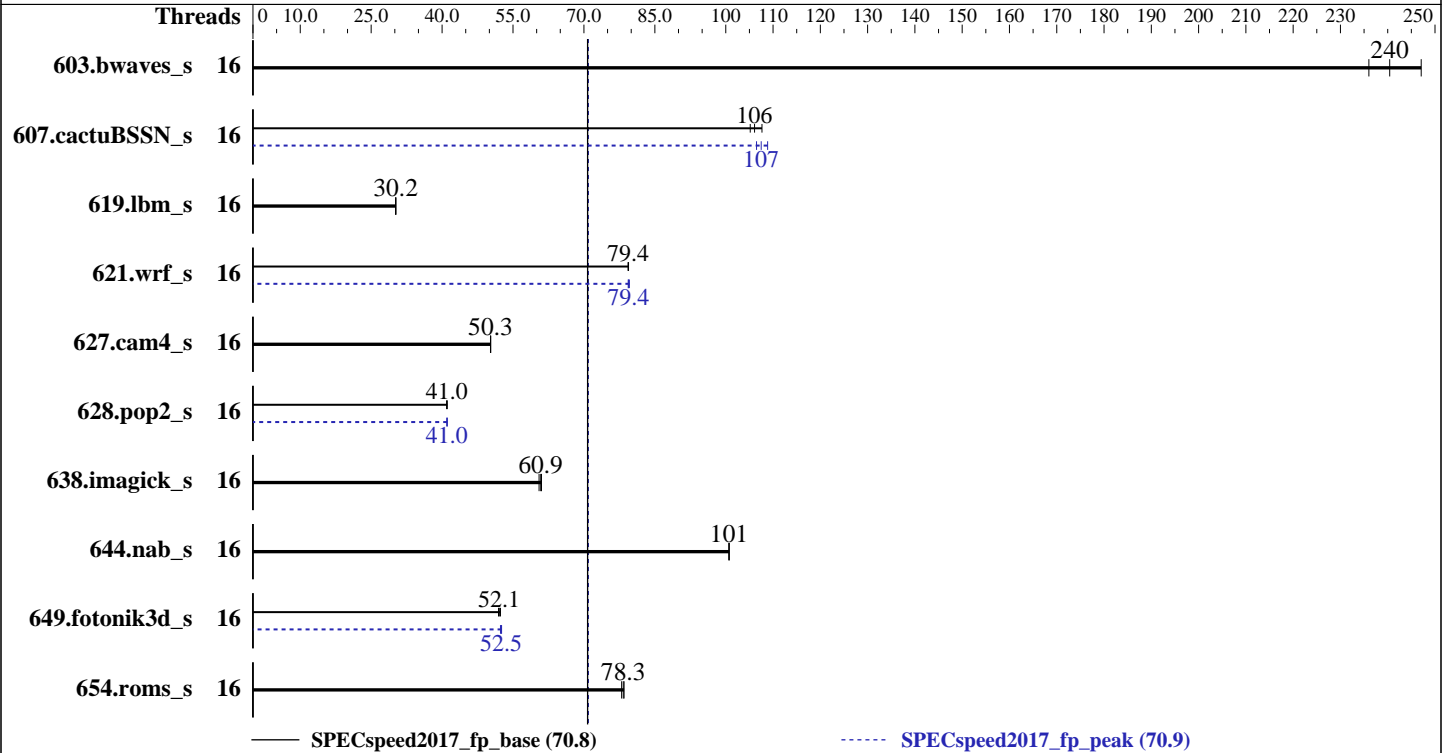
Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2019

Hardware Availability: Apr-2019

Software Availability: Jul-2018



Hardware

CPU Name: AMD EPYC 7371
 Max MHz.: 3800
 Nominal: 3100
 Enabled: 16 cores, 1 chip
 Orderable: 1 chip
 Cache L1: 64 KB I + 32 KB D on chip per core
 L2: 512 KB I+D on chip per core
 L3: 64 MB I+D on chip per chip, 8 MB shared / 2 cores
 Other: None
 Memory: 512 GB (8 x 64 GB 4Rx4 PC4-2666V-L)
 Storage: 1 x 400 GB SAS SSD RAID 0
 Other: None

Software

OS: SUSE linux Enterprise Server 12 (x86_64) SP3
 Kernel version 4.4.132-94.33-default
 Compiler: C/C++: Version 1.2.1 of AOCC
 Fortran: Version 4.8.2 of GCC
 Parallel: Yes
 Firmware: HPE BIOS Version A41 10/02/2018 released Oct-2018
 File System: btrfs
 System State: Run level 3 (multi-user)
 Base Pointers: 64-bit
 Peak Pointers: 64-bit
 Other: jemalloc memory allocator library V5.1.0



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Results Table

Benchmark	Base							Peak						
	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Threads	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
603.bwaves_s	16	239	247	250	236	<u>245</u>	<u>240</u>	16	239	247	250	236	<u>245</u>	<u>240</u>
607.cactuBSSN_s	16	155	108	159	105	<u>157</u>	<u>106</u>	16	153	109	<u>155</u>	<u>107</u>	157	107
619.lbm_s	16	<u>173</u>	<u>30.2</u>	173	30.2	174	30.2	16	<u>173</u>	<u>30.2</u>	173	30.2	174	30.2
621.wrf_s	16	167	79.4	<u>167</u>	<u>79.4</u>	166	79.4	16	167	79.4	<u>167</u>	<u>79.4</u>	166	79.6
627.cam4_s	16	176	50.3	<u>176</u>	<u>50.3</u>	176	50.2	16	176	50.3	<u>176</u>	<u>50.3</u>	176	50.2
628.pop2_s	16	<u>289</u>	<u>41.0</u>	290	41.0	289	41.1	16	289	41.1	<u>289</u>	<u>41.0</u>	289	41.0
638.imagick_s	16	<u>237</u>	<u>60.9</u>	236	61.0	238	60.5	16	<u>237</u>	<u>60.9</u>	236	61.0	238	60.5
644.nab_s	16	173	101	<u>174</u>	<u>101</u>	174	101	16	173	101	<u>174</u>	<u>101</u>	174	101
649.fotonik3d_s	16	174	52.3	176	51.9	<u>175</u>	<u>52.1</u>	16	<u>174</u>	<u>52.5</u>	173	52.6	174	52.3
654.roms_s	16	201	78.5	<u>201</u>	<u>78.3</u>	202	78.0	16	201	78.5	<u>201</u>	<u>78.3</u>	202	78.0

SPECspeed2017_fp_base = 70.8

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Notes

The AMD64 AOCC Compiler Suite is available at <http://developer.amd.com/amd-aocc/>

The AOCC Gold Linker plugin was installed and used for the link stage.

The AOCC Fortran Plugin version 1.2 was used to leverage AOCC optimizers with gfortran. It is available here: <http://developer.amd.com/amd-aocc/>

jemalloc: configured and built with GCC v4.8.5 in RHEL v7.2 under default conditions.

jemalloc uses environment variable MALLOC_CONF with values narenas and lg_chunk:
narenas: sets the maximum number of arenas to use for automatic multiplexing of threads and arenas.

lg_chunk: set the virtual memory chunk size (log base 2). For example, lg_chunk:21 sets the default chunk size to 2^21 = 2MiB.

Operating System Notes

'ulimit -s unlimited' was used to set environment stack size
'ulimit -l 2097152' was used to set environment locked pages in memory limit

runspec command invoked through numactl i.e.:
numactl --interleave=all runspec <etc>

Set dirty_ratio=8 to limit dirty cache to 8% of memory
Set swappiness=1 to swap only if necessary

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Operating System Notes (Continued)

Set zone_reclaim_mode=1 to free local node memory and avoid remote memory sync then drop_caches=3 to reset caches before invoking runcpu

dirty_ratio, swappiness, zone_reclaim_mode and drop_caches were all set using privileged echo (e.g. echo 1 > /proc/sys/vm/swappiness).

Transparent huge pages were enabled for this run (OS default)

General Notes

Environment variables set by runcpu before the start of the run:

GOMP_CPU_AFFINITY = "0-31"

LD_LIBRARY_PATH = "/home/cpu2017/amd1806-speed-libs-revA/64:/home/cpu2017/amd1806-speed-libs-revA/32:"

OMP_PROC_BIND = "true"

OMP_STACKSIZE = "192M"

OMP_WAIT_POLICY = "active"

Binaries were compiled on a system with 2x AMD EPYC 7601 CPU + 512GB Memory using RHEL 7.4 NA: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

Yes: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

jemalloc: configured and built with GCC v4.8.2 in RHEL v7.2 under default conditions.

jemalloc: sources available from jemalloc.net or <https://github.com/jemalloc/jemalloc/releases>

Platform Notes

BIOS Configuration

AMD SMT Option set to Disabled

Thermal Configuration set to Maximum Cooling

Performance Determinism set to Power Deterministic

Workload Power and Utilization Monitoring set to Disabled

Minimum Processor Idle Power core C-State set to C6 State

Sysinfo program /home/cpu2017/bin/sysinfo

Rev: r5974 of 2018-05-19 9bcde8f2999c33d61f64985e45859ea9

running on linux-qlr8 Thu Feb 7 20:11:00 2019

SUT (System Under Test) info as seen by some common utilities.

For more information on this section, see

<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : AMD EPYC 7371 16-Core Processor

1 "physical id"s (chips)

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Platform Notes (Continued)

16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 16

siblings : 16

physical 0: cores 0 1 8 9 16 17 24 25 32 33 40 41 48 49 56 57

From lscpu:

```
Architecture:          x86_64
CPU op-mode(s):        32-bit, 64-bit
Byte Order:             Little Endian
CPU(s):                 16
On-line CPU(s) list:   0-15
Thread(s) per core:     1
Core(s) per socket:    16
Socket(s):              1
NUMA node(s):          4
Vendor ID:              AuthenticAMD
CPU family:             23
Model:                  1
Model name:             AMD EPYC 7371 16-Core Processor
Stepping:               2
CPU MHz:                3100.000
CPU max MHz:            3100.0000
CPU min MHz:            2500.0000
BogoMIPS:               6188.22
Virtualization:        AMD-V
L1d cache:              32K
L1i cache:              64K
L2 cache:               512K
L3 cache:               8192K
NUMA node0 CPU(s):     0-3
NUMA node1 CPU(s):     4-7
NUMA node2 CPU(s):     8-11
NUMA node3 CPU(s):     12-15
```

```
Flags:                  fpu vme de pse tsc msr pae mce cx8 apic sep mtrr pge mca cmov
pat pse36 clflush mmx fxsr sse sse2 ht syscall nx mmxext fxsr_opt pdpe1gb rdtscp lm
constant_tsc rep_good nopl nonstop_tsc extd_apicid amd_dcm aperfmperf eagerfpu pni
pclmulqdq monitor ssse3 fma cx16 sse4_1 sse4_2 movbe popcnt aes xsave avx f16c
rdrand lahf_lm cmp_legacy svm extapic cr8_legacy abm sse4a misalignsse 3dnowprefetch
osvw skinit wdt tce topoext perfctr_core perfctr_nb bpext perfctr_l2 mwaitx arat cpb
hw_pstate ssbd retpoline retpoline_amd npt lbrv svm_lock nrip_save tsc_scale
vmcb_clean flushbyasid decodeassists pausefilter pfthreshold vmmcall avic fsgsbase
bmi1 avx2 smep bmi2 rdseed adx smap clflushopt sha_ni xsaveopt xsavec xgetbv1 clzero
irperf ibpb overflow_recov succor smca
```

/proc/cpuinfo cache data

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Platform Notes (Continued)

cache size : 512 KB

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

available: 4 nodes (0-3)

node 0 cpus: 0 1 2 3

node 0 size: 128841 MB

node 0 free: 128654 MB

node 1 cpus: 4 5 6 7

node 1 size: 129022 MB

node 1 free: 128822 MB

node 2 cpus: 8 9 10 11

node 2 size: 129022 MB

node 2 free: 128916 MB

node 3 cpus: 12 13 14 15

node 3 size: 129022 MB

node 3 free: 128902 MB

node distances:

node 0 1 2 3

0: 10 16 16 16

1: 16 10 16 16

2: 16 16 10 16

3: 16 16 16 10

From /proc/meminfo

MemTotal: 528291400 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

/usr/bin/lsb_release -d

SUSE Linux Enterprise Server 12 SP3

From /etc/*release* /etc/*version*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86_64)

VERSION = 12

PATCHLEVEL = 3

This file is deprecated and will be removed in a future service pack or release.

Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12-SP3"

VERSION_ID="12.3"

PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"

ID="sles"

ANSI_COLOR="0;32"

CPE_NAME="cpe:/o:suse:sles:12:sp3"

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Platform Notes (Continued)

uname -a:

```
Linux linux-qlr8 4.4.132-94.33-default #1 SMP Tue May 29 20:09:56 UTC 2018 (76aae3b)
x86_64 x86_64 x86_64 GNU/Linux
```

Kernel self-reported vulnerability status:

```
CVE-2017-5754 (Meltdown): Not affected
CVE-2017-5753 (Spectre variant 1): Mitigation: __user pointer sanitization
CVE-2017-5715 (Spectre variant 2): Mitigation: Full AMD retpoline + IBPB
```

run-level 3 Feb 7 14:36

SPEC is set to: /home/cpu2017

```
Filesystem Type Size Used Avail Use% Mounted on
/dev/sda4 xfs 331G 4.4G 326G 2% /home
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE A41 10/02/2018

Memory:

```
8x UNKNOWN NOT AVAILABLE
8x UNKNOWN NOT AVAILABLE 64 GB 4 rank 2666
```

(End of data from sysinfo program)

Compiler Version Notes

=====
CC 619.lbm_s(base, peak) 638.imagick_s(base, peak) 644.nab_s(base, peak)
=====

```
AOCC.LLVM.1.2.1.B29.2018_05_14 clang version 6.0.0 (CLANG:
b6b3d31d6df08fb7da935a28842b39b7b3c2c55b) (llvm/cpu/llvm
18855c80ed252fc4ba4ac41e2086627ef2bddd04) (based on LLVM
AOCC.LLVM.1.2.1.B29.2018_05_14)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /root/work/compilers/aoccl.2.1/AOCC-1.2.1-Compiler/bin
```

=====
FC 607.cactuBSSN_s(base, peak)
=====

```
AOCC.LLVM.1.2.1.B29.2018_05_14 clang version 6.0.0 (CLANG:
```

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Compiler Version Notes (Continued)

```

b6b3d31d6df08fb7da935a28842b39b7b3c2c55b) (llvm/cpu/llvm
18855c80ed252fc4ba4ac41e2086627ef2bddd04) (based on LLVM
AOCC.LLVM.1.2.1.B29.2018_05_14)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /root/work/compilers/aoccl.2.1/AOCC-1.2.1-Compiler/bin
AOCC.LLVM.1.2.1.B29.2018_05_14 clang version 6.0.0 (CLANG:
b6b3d31d6df08fb7da935a28842b39b7b3c2c55b) (llvm/cpu/llvm
18855c80ed252fc4ba4ac41e2086627ef2bddd04) (based on LLVM
AOCC.LLVM.1.2.1.B29.2018_05_14)
Target: x86_64-unknown-linux-gnu
Thread model: posix
InstalledDir: /root/work/compilers/aoccl.2.1/AOCC-1.2.1-Compiler/bin
GNU Fortran (GCC) 4.8.2
Copyright (C) 2013 Free Software Foundation, Inc.
GNU Fortran comes with NO WARRANTY, to the extent permitted by law.
You may redistribute copies of GNU Fortran
under the terms of the GNU General Public License.
For more information about these matters, see the file named COPYING

```

```

=====
FC 603.bwaves_s(base, peak) 649.fotonik3d_s(base, peak) 654.roms_s(base,
peak)
-----

```

```

GNU Fortran (GCC) 4.8.2
Copyright (C) 2013 Free Software Foundation, Inc.
GNU Fortran comes with NO WARRANTY, to the extent permitted by law.
You may redistribute copies of GNU Fortran
under the terms of the GNU General Public License.
For more information about these matters, see the file named COPYING

```

```

=====
CC 621.wrf_s(base, peak) 627.cam4_s(base, peak) 628.pop2_s(base, peak)
-----

```

```

GNU Fortran (GCC) 4.8.2
Copyright (C) 2013 Free Software Foundation, Inc.
GNU Fortran comes with NO WARRANTY, to the extent permitted by law.
You may redistribute copies of GNU Fortran
under the terms of the GNU General Public License.
For more information about these matters, see the file named COPYING
AOCC.LLVM.1.2.1.B29.2018_05_14 clang version 6.0.0 (CLANG:
b6b3d31d6df08fb7da935a28842b39b7b3c2c55b) (llvm/cpu/llvm
18855c80ed252fc4ba4ac41e2086627ef2bddd04) (based on LLVM
AOCC.LLVM.1.2.1.B29.2018_05_14)
Target: x86_64-unknown-linux-gnu

```

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Compiler Version Notes (Continued)

Thread model: posix

InstalledDir: /root/work/compilers/aoccl.2.1/AOCC-1.2.1-Compiler/bin

Base Compiler Invocation

C benchmarks:

clang

Fortran benchmarks:

clang gfortran

Benchmarks using both Fortran and C:

clang gfortran

Benchmarks using Fortran, C, and C++:

clang++ clang gfortran

Base Portability Flags

603.bwaves_s: -DSPEC_LP64

607.cactuBSSN_s: -DSPEC_LP64

619.lbm_s: -DSPEC_LP64

621.wrf_s: -DSPEC_CASE_FLAG -fconvert=big-endian -DSPEC_LP64

627.cam4_s: -DSPEC_CASE_FLAG -DSPEC_LP64

628.pop2_s: -DSPEC_CASE_FLAG -fconvert=big-endian -DSPEC_LP64

638.imagick_s: -DSPEC_LP64

644.nab_s: -DSPEC_LP64

649.fotonik3d_s: -DSPEC_LP64

654.roms_s: -DSPEC_LP64

Base Optimization Flags

C benchmarks:

-flto -fuse-ld=lld -Wl,-mllvm -Wl,-function-specialize -O3

-ffast-math -march=znver1 -fstruct-layout=3

-mllvm -unroll-threshold=50 -fremap-arrays -mno-avx2

-mllvm -inline-threshold=1000 -flv-function-specialization

-mllvm -enable-gvn-hoist -mllvm -function-specialize -z muldefs

-lamdlibm -DSPEC_OPENMP -fopenmp -fopenmp=libomp -lomp -ljemalloc

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Base Optimization Flags (Continued)

Fortran benchmarks:

```
-flto -fuse-ld=lld -Wl,-mllvm -Wl,-function-specialize -O3
-funroll-loops -ffast-math -z muldefs -lamdlibm -fplugin=dragonegg.so
-fplugin-arg-dragonegg-llvm-option=-inline-threshold:1000 -DSPEC_OPENMP
-fopenmp -fopenmp=libomp -lomp -ljemalloc -lgfortran
```

Benchmarks using both Fortran and C:

```
-flto -fuse-ld=lld -Wl,-mllvm -Wl,-function-specialize -O3
-ffast-math -march=znver1 -fstruct-layout=3
-mllvm -unroll-threshold=50 -fremap-arrays -mno-avx2
-mllvm -inline-threshold=1000 -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -function-specialize -funroll-loops
-z muldefs -lamdlibm -fplugin=dragonegg.so
-fplugin-arg-dragonegg-llvm-option=-inline-threshold:1000 -DSPEC_OPENMP
-fopenmp -fopenmp=libomp -lomp -ljemalloc -lgfortran
```

Benchmarks using Fortran, C, and C++:

```
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -flto -fuse-ld=lld
-Wl,-mllvm -Wl,-function-specialize -O3 -ffast-math -march=znver1
-fstruct-layout=3 -mllvm -unroll-threshold=50 -fremap-arrays -mno-avx2
-mllvm -inline-threshold=1000 -flv-function-specialization
-mllvm -enable-gvn-hoist -mllvm -function-specialize
-mllvm -unroll-threshold=100 -mllvm -enable-partial-unswitch
-funroll-loops -z muldefs -lamdlibm -fplugin=dragonegg.so
-fplugin-arg-dragonegg-llvm-option=-inline-threshold:1000 -DSPEC_OPENMP
-fopenmp -fopenmp=libomp -lomp -ljemalloc
```

Base Other Flags

C benchmarks:

```
-Wno-return-type -DUSE_OPENMP
```

Fortran benchmarks:

```
-DUSE_OPENMP -Wno-return-type
```

Benchmarks using both Fortran and C:

```
-DUSE_OPENMP -Wno-return-type
```

Benchmarks using Fortran, C, and C++:

```
-Wno-return-type -DUSE_OPENMP
```



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Fortran benchmarks:

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Benchmarks using both Fortran and C:

clang gfortran

Benchmarks using Fortran, C, and C++:

clang++ clang gfortran

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

619.lbm_s: basepeak = yes

638.imagick_s: basepeak = yes

644.nab_s: basepeak = yes

Fortran benchmarks:

603.bwaves_s: basepeak = yes

649.fotonik3d_s: -flto -fuse-ld=lld -Wl,-mllvm -Wl,-function-specialize
-O3 -funroll-loops -ffast-math -z muldefs -lamdlibm
-fplugin=dragonegg.so
-fplugin-arg-dragonegg-llvm-option=-inline-threshold:1000
-DSPEC_OPENMP -fopenmp -fopenmp=libomp -lomp -ljemalloc
-lgfortran

654.roms_s: basepeak = yes

Benchmarks using both Fortran and C:

(Continued on next page)



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Peak Optimization Flags (Continued)

```
621.wrf_s: -flto -fuse-ld=lld -Wl,-mllvm -Wl,-function-specialize
-Ofast -march=znver1 -mno-sse4a -fstruct-layout=5
-mllvm -vectorize-memory-aggressively -mno-avx2
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-flv-function-specialization
-mllvm -enable-vectorize-compares -O3 -funroll-loops
-ffast-math -z muldefs -lamdlibm -fplugin=dragonegg.so
-fplugin-arg-dragonegg-llvm-option=-inline-threshold:1000
-DSPEC_OPENMP -fopenmp -fopenmp=libomp -lomp -ljemalloc
-lgfortran
```

627.cam4_s: basepeak = yes

628.pop2_s: Same as 621.wrf_s

Benchmarks using Fortran, C, and C++:

```
-Wl,-mllvm -Wl,-x86-use-vzeroupper=false -flto -fuse-ld=lld
-Wl,-mllvm -Wl,-function-specialize -Ofast -march=znver1 -mno-sse4a
-fstruct-layout=5 -mllvm -vectorize-memory-aggressively -mno-avx2
-mllvm -unroll-threshold=50 -fremap-arrays
-mllvm -inline-threshold=1000 -mllvm -enable-gvn-hoist
-flv-function-specialization -mllvm -enable-vectorize-compares
-mllvm -unroll-threshold=100 -O3 -funroll-loops -ffast-math
-z muldefs -lamdlibm -fplugin=dragonegg.so
-fplugin-arg-dragonegg-llvm-option=-inline-threshold:1000 -DSPEC_OPENMP
-fopenmp -fopenmp=libomp -lomp -ljemalloc
```

Peak Other Flags

C benchmarks:

-Wno-return-type -DUSE_OPENMP

Fortran benchmarks:

-DUSE_OPENMP -Wno-return-type

Benchmarks using both Fortran and C:

-DUSE_OPENMP -Wno-return-type

Benchmarks using Fortran, C, and C++:

-Wno-return-type -DUSE_OPENMP



SPEC CPU2017 Floating Point Speed Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL325 Gen10

(3.10 GHz, AMD EPYC 7371)

SPECspeed2017_fp_base = 70.8

SPECspeed2017_fp_peak = 70.9

CPU2017 License: 3

Test Sponsor: HPE

Tested by: HPE

Test Date: Feb-2019

Hardware Availability: Apr-2019

Software Availability: Jul-2018

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/aocc100-flags-revC-I.2018-11-13.html>

<http://www.spec.org/cpu2017/flags/gcc.2017-11-20.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-V1.2-EPYC-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/aocc100-flags-revC-I.2018-11-13.xml>

<http://www.spec.org/cpu2017/flags/gcc.2017-11-20.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-AMD-V1.2-EPYC-revD.xml>

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For questions about this result, please contact the tester. For other inquiries, please contact info@spec.org.

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