



# SPEC® CPU2017 Floating Point Speed Result

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## Hewlett Packard Enterprise

(Test Sponsor: HPE)

### Synergy 480 Gen10

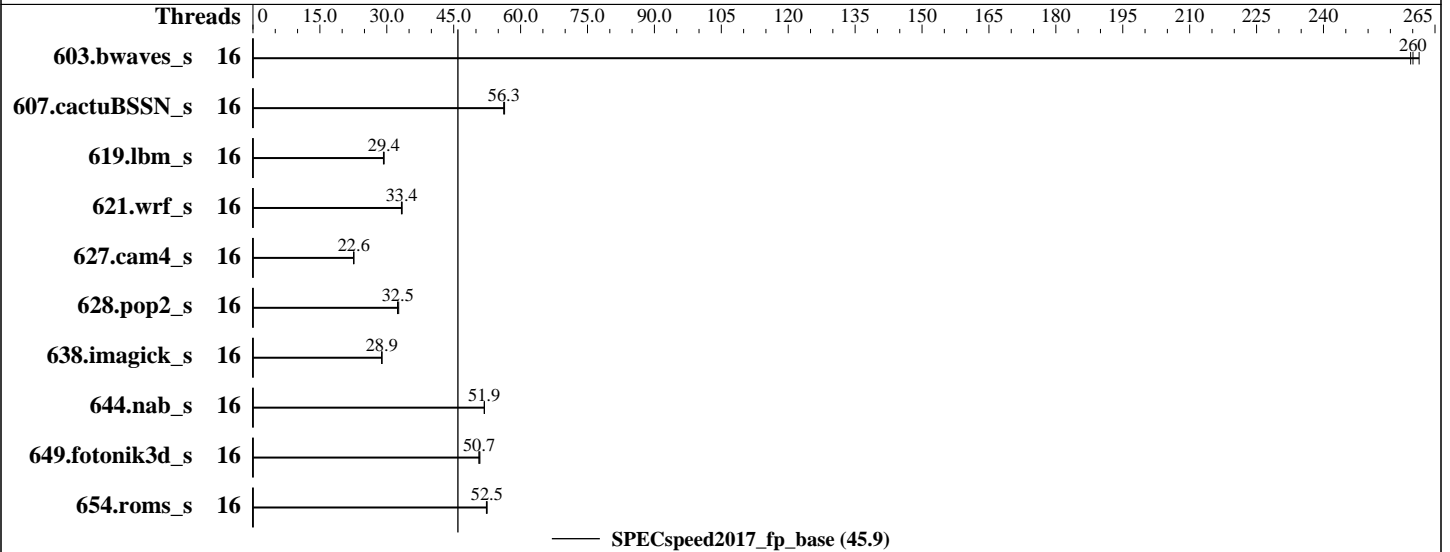
(1.70 GHz, Intel Xeon Bronze 3106)

SPECspeed2017\_fp\_base = 45.9

SPECspeed2017\_fp\_peak = Not Run

**CPU2017 License:** 3  
**Test Sponsor:** HPE  
**Tested by:** HPE

**Test Date:** Dec-2017  
**Hardware Availability:** Nov-2017  
**Software Availability:** Sep-2017



### Hardware

CPU Name: Intel Xeon Bronze 3106  
Max MHz.: 1700  
Nominal: 1700  
Enabled: 16 cores, 2 chips  
Orderable: 1, 2 chip(s)  
Cache L1: 32 KB I + 32 KB D on chip per core  
L2: 1 MB I+D on chip per core  
L3: 11 MB I+D on chip per chip  
Other: None  
Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R, running at 2133)  
Storage: 1 x 480 GB SATA SSD, RAID 0  
Other: None

### Software

OS: Red Hat Enterprise Linux Server release 7.3 (Maipo)  
Kernel 3.10.0-514.el7.x86\_64  
Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux;  
Fortran: Version 18.0.0.128 of Intel Fortran Compiler for Linux  
Parallel: Yes  
Firmware: HPE BIOS Version I42 released Nov-2017 (tested with I42 11/14/2017)  
File System: xfs  
System State: Run level 3 (multi-user)  
Base Pointers: 64-bit  
Peak Pointers: Not Applicable  
Other: None



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## Results Table

| Benchmark       | Base    |                   |                    |                   |                    |                   |                    | Peak    |         |       |         |       |         |       |
|-----------------|---------|-------------------|--------------------|-------------------|--------------------|-------------------|--------------------|---------|---------|-------|---------|-------|---------|-------|
|                 | Threads | Seconds           | Ratio              | Seconds           | Ratio              | Seconds           | Ratio              | Threads | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 603.bwaves_s    | 16      | 226               | 261                | <b><u>227</u></b> | <b><u>260</u></b>  | 227               | 260                |         |         |       |         |       |         |       |
| 607.cactuBSSN_s | 16      | 296               | 56.2               | <b><u>296</u></b> | <b><u>56.3</u></b> | 296               | 56.4               |         |         |       |         |       |         |       |
| 619.lbm_s       | 16      | 179               | 29.3               | 178               | 29.4               | <b><u>178</u></b> | <b><u>29.4</u></b> |         |         |       |         |       |         |       |
| 621.wrf_s       | 16      | 397               | 33.3               | 396               | 33.4               | <b><u>396</u></b> | <b><u>33.4</u></b> |         |         |       |         |       |         |       |
| 627.cam4_s      | 16      | 393               | 22.6               | 392               | 22.6               | <b><u>392</u></b> | <b><u>22.6</u></b> |         |         |       |         |       |         |       |
| 628.pop2_s      | 16      | 366               | 32.4               | 363               | 32.7               | <b><u>366</u></b> | <b><u>32.5</u></b> |         |         |       |         |       |         |       |
| 638.imagick_s   | 16      | 500               | 28.9               | 499               | 28.9               | <b><u>500</u></b> | <b><u>28.9</u></b> |         |         |       |         |       |         |       |
| 644.nab_s       | 16      | 337               | 51.9               | <b><u>337</u></b> | <b><u>51.9</u></b> | 337               | 51.9               |         |         |       |         |       |         |       |
| 649.fotonik3d_s | 16      | <b><u>180</u></b> | <b><u>50.7</u></b> | 179               | 50.9               | 180               | 50.7               |         |         |       |         |       |         |       |
| 654.roms_s      | 16      | <b><u>300</u></b> | <b><u>52.5</u></b> | 301               | 52.3               | 300               | 52.5               |         |         |       |         |       |         |       |

SPECspeed2017\_fp\_base = 45.9

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Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
Transparent Huge Pages enabled by default  
Filesystem page cache cleared with:  
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop\_caches' prior to run  
irqbalance disabled with "systemctl stop irqbalance"  
tuned profile set with "tuned-adm profile throughput-performance"

## General Notes

Environment variables set by runcpu before the start of the run:  
KMP\_AFFINITY = "granularity=core,compact"  
LD\_LIBRARY\_PATH = "/home/cpu2017/lib/ia32:/home/cpu2017/lib/intel64:/home/cpu2017/je5.0.1-32:/home/cpu2017/je5.0.1-64"  
OMP\_STACKSIZE = "192M"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.4

## Platform Notes

BIOS Configuration:  
Thermal Configuration set to Maximum Cooling  
LLC Prefetch set to Enabled  
LLC Dead Line Allocation set to Disabled  
Memory Patrol Scrubbing set to Disabled  
Workload Profile set to General Peak Frequency Compute  
Energy/Performance Bias set to Maximum Performance  
Workload Profile set to Custom

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## Platform Notes (Continued)

NUMA Group Size Optimization set to Flat  
Sysinfo program /home/cpu2017/bin/sysinfo  
Rev: r5797 of 2017-06-14 96c45e4568ad54c135fd618bcc091c0f  
running on SY480\_M3\_RHEL Sat Dec 9 15:14:47 2017

SUT (System Under Test) info as seen by some common utilities.  
For more information on this section, see  
<https://www.spec.org/cpu2017/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name      : Intel(R) Xeon(R) Bronze 3106 CPU @ 1.70GHz
 2 "physical id"s (chips)
 16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following
excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
cpu cores      : 8
siblings       : 8
physical 0:    : cores 0 1 2 3 4 5 6 7
physical 1:    : cores 0 1 2 3 4 5 6 7
```

From lscpu:

```
Architecture:    x86_64
CPU op-mode(s):  32-bit, 64-bit
Byte Order:      Little Endian
CPU(s):          16
On-line CPU(s) list:  0-15
Thread(s) per core:  1
Core(s) per socket:  8
Socket(s):       2
NUMA node(s):    2
Vendor ID:       GenuineIntel
CPU family:      6
CPU model:       85
Model name:      Intel(R) Xeon(R) Bronze 3106 CPU @ 1.70GHz
Stepping:        4
CPU MHz:         1700.000
BogoMIPS:        3405.48
Virtualization:  VT-x
L1d cache:       32K
L1i cache:       32K
L2 cache:        1024K
L3 cache:        11264K
NUMA node0 CPU(s):  0-3,8-11
NUMA node1 CPU(s):  4-7,12-15
```

```
/proc/cpuinfo cache data
cache size : 11264 KB
```

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## Platform Notes (Continued)

From numactl --hardware WARNING: a numactl 'node' might or might not correspond to a physical chip.

```
From /proc/meminfo
MemTotal:      395933088 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server
```

```
uname -a:
Linux SY480_M3_RHEL 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13 EDT 2016 x86_64
x86_64 x86_64 GNU/Linux
```

```
run-level 3 Dec 9 15:10
```

```
SPEC is set to: /home/cpu2017
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   392G   25G  368G   7% /home
```

Additional information from dmidecode follows. WARNING: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS HPE I42 11/14/2017
Memory:
24x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666, configured at 2133
```

(End of data from sysinfo program)



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## Compiler Version Notes

=====  
CC 619.lbm\_s(base) 638.imagick\_s(base) 644.nab\_s(base)  
-----

icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
FC 607.cactuBSSN\_s(base)  
-----

icpc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
FC 603.bwaves\_s(base) 649.fotonik3d\_s(base) 654.roms\_s(base)  
-----

ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

=====  
CC 621.wrf\_s(base) 627.cam4\_s(base) 628.pop2\_s(base)  
-----

ifort (IFORT) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
icc (ICC) 18.0.0 20170811  
Copyright (C) 1985-2017 Intel Corporation. All rights reserved.  
-----

## Base Compiler Invocation

C benchmarks:

icc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

ifort icc

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## Base Compiler Invocation (Continued)

Benchmarks using Fortran, C, and C++:

icpc icc ifort

## Base Portability Flags

```

603.bwaves_s: -DSPEC_LP64
607.cactuBSSN_s: -DSPEC_LP64
619.lbm_s: -DSPEC_LP64
621.wrf_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
627.cam4_s: -DSPEC_LP64 -DSPEC_CASE_FLAG
628.pop2_s: -DSPEC_LP64 -DSPEC_CASE_FLAG -convert big_endian
-assume byterecl
638.imagick_s: -DSPEC_LP64
644.nab_s: -DSPEC_LP64
649.fotonik3d_s: -DSPEC_LP64
654.roms_s: -DSPEC_LP64

```

## Base Optimization Flags

C benchmarks:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP

```

Fortran benchmarks:

```

-DSPEC_OPENMP -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-ffinite-math-only -qopt-mem-layout-trans=3 -qopenmp
-nostandard-realloc-lhs -align array32byte

```

Benchmarks using both Fortran and C:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

```

Benchmarks using Fortran, C, and C++:

```

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -ffinite-math-only
-qopt-mem-layout-trans=3 -qopenmp -DSPEC_OPENMP
-nostandard-realloc-lhs -align array32byte

```



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## Base Other Flags

C benchmarks:

-m64 -std=c11

Fortran benchmarks:

-m64

Benchmarks using both Fortran and C:

-m64 -std=c11

Benchmarks using Fortran, C, and C++:

-m64 -std=c11

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.html>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2017/flags/Intel-ic18.0-official-linux64.2017-10-19.xml>

<http://www.spec.org/cpu2017/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml>

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Tested with SPEC CPU2017 v1.0.2 on 2017-12-09 04:44:46-0500.

Report generated on 2018-10-31 17:13:46 by CPU2017 PDF formatter v6067.

Originally published on 2018-01-14.