



SPEC® CINT2006 Result

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Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL560 Gen10
(2.40 GHz, Intel Xeon Gold 5115)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 1960

CPU2006 license: 3

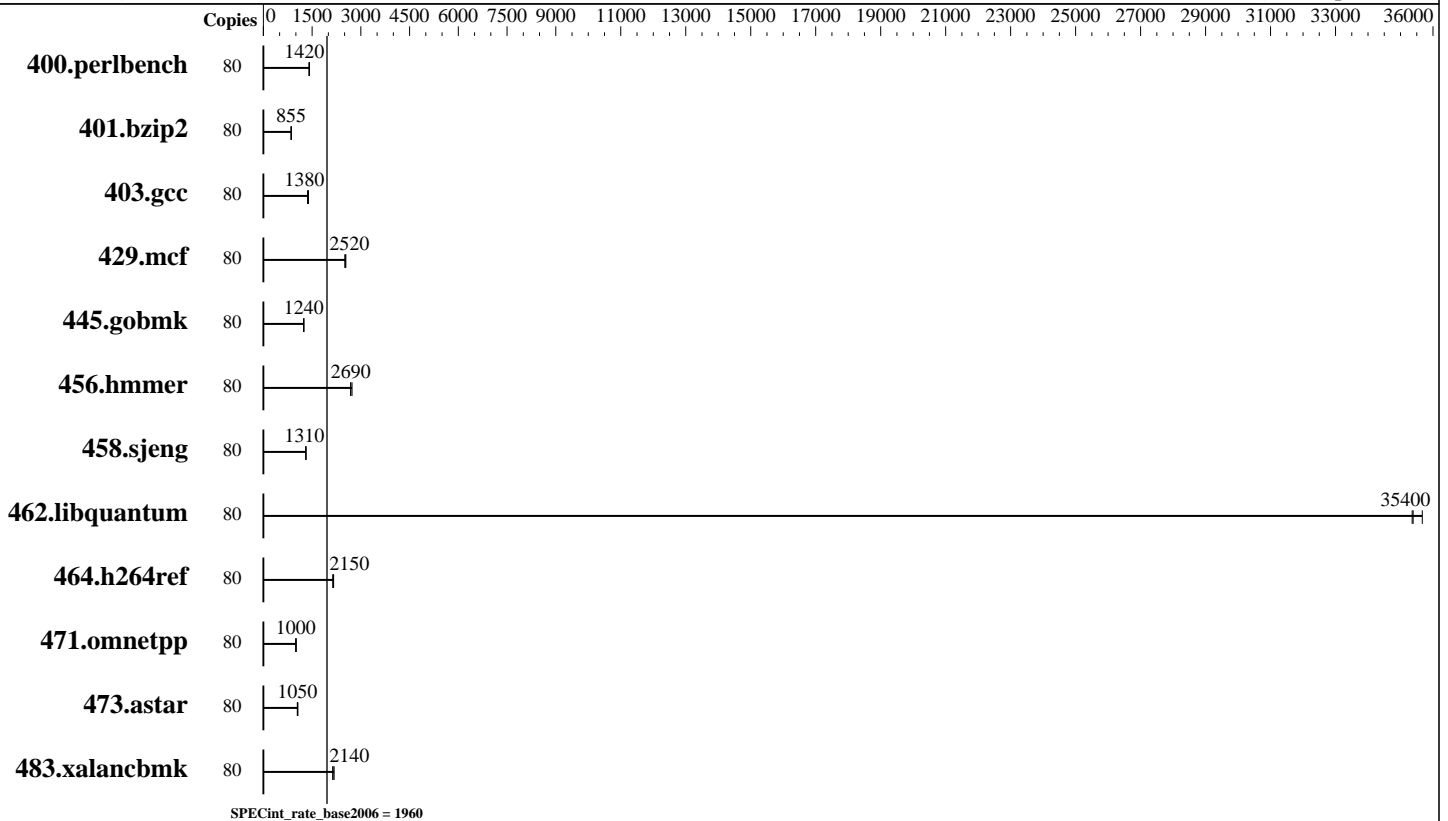
Test sponsor: HPE

Tested by: HPE

Test date: Dec-2017

Hardware Availability: Oct-2017

Software Availability: Sep-2017



Hardware

CPU Name: Intel Xeon Gold 5115
CPU Characteristics: Intel Turbo Boost Technology up to 3.20 GHz
CPU MHz: 2400
FPU: Integrated
CPU(s) enabled: 40 cores, 4 chips, 10 cores/chip, 2 threads/core
CPU(s) orderable: 1, 2, 4 chip(s)
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 1 MB I+D on chip per core
L3 Cache: 13.75 MB I+D on chip per chip
Other Cache: None
Memory: 384 GB (48 x 8 GB 2Rx8 PC4-2666V-R, running at 2400)
Disk Subsystem: 1 x 960 GB SATA SSD, RAID 0
Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12 (x86_64) SP2
 Kernel 4.4.21-68-default
Compiler: C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux
Auto Parallel: No
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: Microquill SmartHeap V10.2



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	80	558	1400	<u>551</u>	<u>1420</u>	551	1420							
401.bzip2	80	<u>903</u>	<u>855</u>	897	861	905	853							
403.gcc	80	474	1360	<u>465</u>	<u>1380</u>	464	1390							
429.mcf	80	287	2540	<u>289</u>	<u>2520</u>	291	2510							
445.gobmk	80	675	1240	675	1240	<u>675</u>	<u>1240</u>							
456.hammer	80	<u>277</u>	<u>2690</u>	278	2690	274	2730							
458.sjeng	80	740	1310	739	1310	<u>740</u>	<u>1310</u>							
462.libquantum	80	46.5	35700	<u>46.8</u>	<u>35400</u>	46.9	35400							
464.h264ref	80	<u>824</u>	<u>2150</u>	829	2140	819	2160							
471.omnetpp	80	494	1010	<u>499</u>	<u>1000</u>	503	995							
473.astar	80	528	1060	<u>535</u>	<u>1050</u>	538	1040							
483.xalancbmk	80	253	2180	<u>257</u>	<u>2140</u>	259	2130							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
  shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>
irqbalance disabled with "systemctl stop irqbalance"
tuned profile set with "tuned-adm profile throughput-performance"
VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"
```

Platform Notes

```
BIOS Configuration:
  Thermal Configuration set to Maximum Cooling
  Memory Patrol Scrubbing set to Disabled
  LLC Prefetch set to Enabled
  LLC Dead Line Allocation set to Disabled
  Workload Profile set to General Throughput Compute
  Minimum Processor Idle Power Core C-State set to C1E State
Sysinfo program /home/cpu2006/config/sysinfo.rev6993
```

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Platform Notes (Continued)

Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-y57o Tue Dec 12 12:52:18 2017

This section contains SUT (System Under Test) info as seen by
some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 5115 CPU @ 2.40GHz

4 "physical id"s (chips)

80 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)

cpu cores : 10

siblings : 20

physical 0: cores 0 1 2 3 4 8 9 10 11 12

physical 1: cores 0 1 2 3 4 8 9 10 11 12

physical 2: cores 0 1 2 3 4 8 9 10 11 12

physical 3: cores 0 1 2 3 4 8 9 10 11 12

cache size : 14080 KB

From /proc/meminfo

MemTotal: 395922384 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

From /etc/*release* /etc/*version*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86_64)

VERSION = 12

PATCHLEVEL = 2

This file is deprecated and will be removed in a future service pack or
release.

Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12-SP2"

VERSION_ID="12.2"

PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"

ID="sles"

ANSI_COLOR="0;32"

CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:

Linux linux-y57o 4.4.21-68-default #1 SMP Tue Oct 18 18:19:37 UTC 2016
(63cf368) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Dec 12 12:42

SPEC is set to: /home/cpu2006

Filesystem Type Size Used Avail Use% Mounted on
Continued on next page

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Platform Notes (Continued)

/dev/sdc4 xfs 852G 37G 816G 5% /home
Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U34 09/29/2017

Memory:

48x UNKNOWN NOT AVAILABLE 8 GB 2 rank 2666 MHz, configured at 2400 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

No: The test sponsor attests, as of date of publication, that CVE-2017-5754 (Meltdown) is mitigated in the system as tested and documented.

No: The test sponsor attests, as of date of publication, that CVE-2017-5753 (Spectre variant 1) is mitigated in the system as tested and documented.

No: The test sponsor attests, as of date of publication, that CVE-2017-5715 (Spectre variant 2) is mitigated in the system as tested and documented.

This benchmark result is intended to provide perspective on past performance using the historical hardware and/or software described on this result page.

The system as described on this result page was formerly generally available. At the time of this publication, it may not be shipping, and/or may not be supported, and/or may fail to meet other tests of General Availability described in the SPEC OSG Policy document, <http://www.spec.org/osg/policy.htm>.

This measured result may not be representative of the result that would be measured were this benchmark run with hardware and software available as of the publication date.

Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers_and_libraries_2018.0.082/linux/lib/ia32

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Base Compiler Invocation (Continued)

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers_and_libraries_2018.0.082/linux/lib/ia32

Base Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
 401.bzip2: -D_FILE_OFFSET_BITS=64
 403.gcc: -D_FILE_OFFSET_BITS=64
 429.mcf: -D_FILE_OFFSET_BITS=64
 445.gobmk: -D_FILE_OFFSET_BITS=64
 456.hmmer: -D_FILE_OFFSET_BITS=64
 458.sjeng: -D_FILE_OFFSET_BITS=64
 462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
 464.h264ref: -D_FILE_OFFSET_BITS=64
 471.omnetpp: -D_FILE_OFFSET_BITS=64
 473.astar: -D_FILE_OFFSET_BITS=64
 483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -Wl,-z,muldefs
-L/home/cpu2006/sh10.2 -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revH.xml>



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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