# SPEC® CINT2006 Result
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**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL360 Gen10
(3.60 GHz, Intel Xeon Platinum 8156)

$SPECint®\_rate2006 =$ Not Run

$SPECint\_rate\_base2006 =$ 576

| | | |
|---|---|---|
| **CPU2006 license:** 3 | **Test date:** | Nov-2017 |
| **Test sponsor:** HPE | **Hardware Availability:** | Oct-2017 |
| **Tested by:** HPE | **Software Availability:** | Sep-2017 |



SPECint_rate_base2006 = 576

## Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon Platinum 8156 |
| CPU Characteristics: | Intel Turbo Boost Technology up to 3.70 GHz |
| CPU MHz: | 3600 |
| FPU: | Integrated |
| CPU(s) enabled: | 8 cores, 2 chips, 4 cores/chip, 2 threads/core |
| CPU(s) orderable: | 1, 2 chip(s) |
| Primary Cache: | 32 KB I + 32 KB D on chip per core |
| Secondary Cache: | 1 MB I+D on chip per core |
| L3 Cache: | 16.5 MB I+D on chip per chip |
| Other Cache: | None |
| Memory: | 192 GB (24 x 8 GB 2Rx8 PC4-2666V-R) |
| Disk Subsystem: | 1 x 400 GB SATA SSD, RAID 0 |
| Other Hardware: | None |

## Software

| | |
|---|---|
| Operating System: | SUSE Linux Enterprise Server 12 (x86_64) SP2 Kernel 4.4.21-68-default |
| Compiler: | C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux |
| Auto Parallel: | No |
| File System: | xfs |
| System State: | Run level 3 (multi-user) |
| Base Pointers: | 32-bit |
| Peak Pointers: | Not Applicable |
| Other Software: | Microquill SmartHeap V10.2 |

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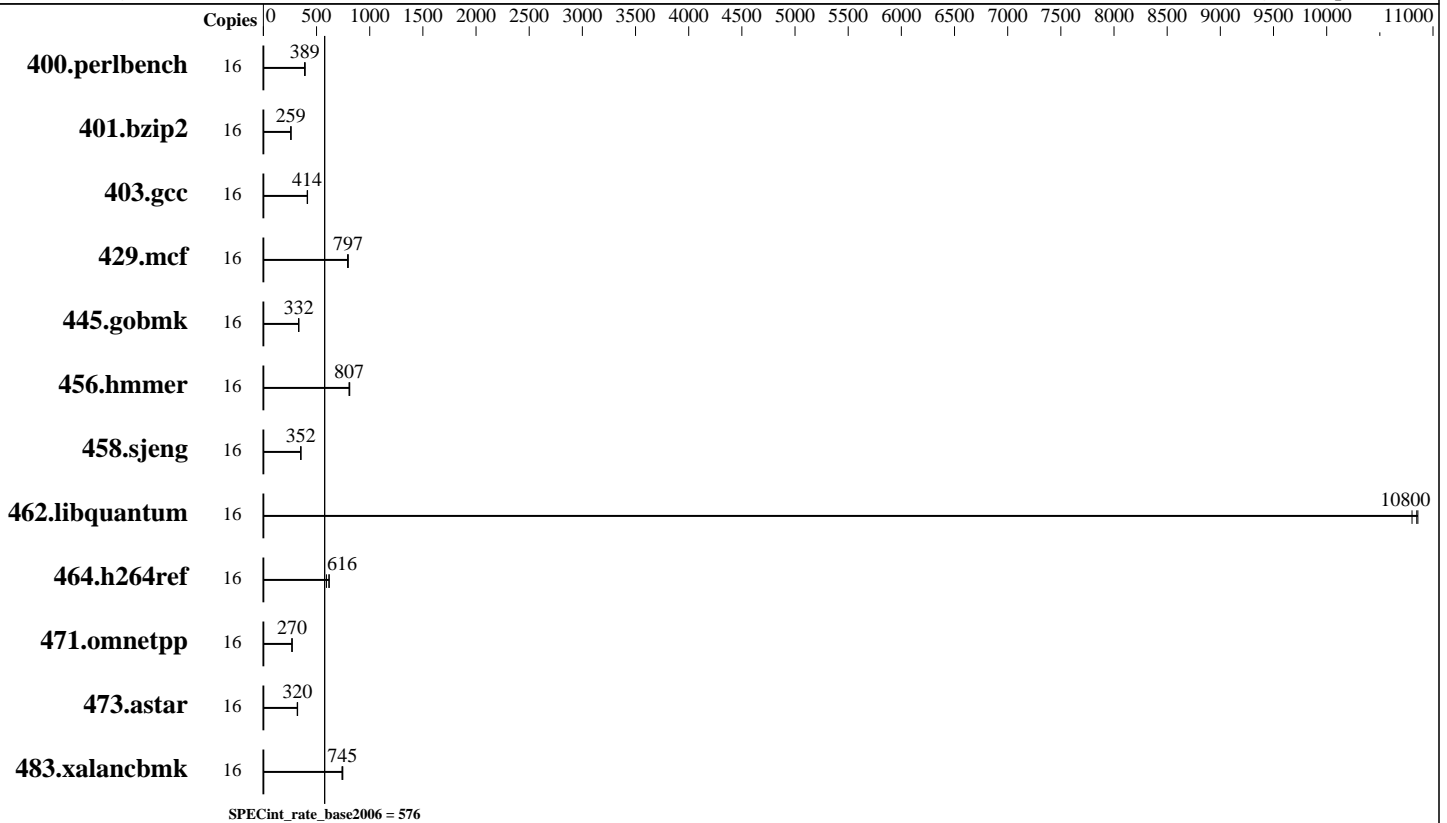SPECint_rate_base2006 =   576

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## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 16 | **402** | **389** | 403 | 388 | 398 | 393 | | | | | | | |
| 401.bzip2 | 16 | **597** | **259** | 598 | 258 | 595 | 259 | | | | | | | |
| 403.gcc | 16 | 310 | 415 | 312 | 413 | **311** | **414** | | | | | | | |
| 429.mcf | 16 | 184 | 792 | **183** | **797** | 183 | 797 | | | | | | | |
| 445.gobmk | 16 | 505 | 332 | **505** | **332** | 505 | 332 | | | | | | | |
| 456.hmmer | 16 | 185 | 807 | **185** | **807** | 184 | 812 | | | | | | | |
| 458.sjeng | 16 | 550 | 352 | **550** | **352** | 550 | 352 | | | | | | | |
| 462.libquantum | 16 | 30.7 | 10800 | 30.5 | 10900 | **30.6** | **10800** | | | | | | | |
| 464.h264ref | 16 | **575** | **616** | 571 | 620 | 597 | 593 | | | | | | | |
| 471.omnetpp | 16 | 370 | 270 | 373 | 268 | **371** | **270** | | | | | | | |
| 473.astar | 16 | 352 | 319 | 349 | 322 | **351** | **320** | | | | | | | |
| 483.xalancbmk | 16 | 148 | 746 | **148** | **745** | 149 | 739 | | | | | | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
 shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
 numactl --interleave=all runspec <etc>
irqbalance disabled with "service irqbalance stop"
tuned profile set wtih "tuned-adm profile throughput-performance"
VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"

## Platform Notes

BIOS Configuration:
 Thermal Configuration set to Maximum Cooling
 LLC Prefetch set to Enabled
 LLC Dead Line Allocation set to Disabled
 Memory Patrol Scrubbing set to Disabled
 Workload Profile set to General Throughput Compute
  Minimum Processor Idle Power Core C-State set to C1E
 Sysinfo program /home/cpu2006/config/sysinfo.rev6993

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## Platform Notes (Continued)

```
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on linux-rugf Wed Nov 15 00:49:18 2017

This section contains SUT (System Under Test) info as seen by
some common utilities.  To remove or add to this section, see:
  http://www.spec.org/cpu2006/Docs/config.html#sysinfo

From /proc/cpuinfo
   model name : Intel(R) Xeon(R) Platinum 8156 CPU @ 3.60GHz
      2 "physical id"s (chips)
      16 "processors"
   cores, siblings (Caution: counting these is hw and system dependent.  The
   following excerpts from /proc/cpuinfo might not be reliable.  Use with
   caution.)
      cpu cores : 4
      siblings  : 8
      physical 0: cores 1 2 5 11
      physical 1: cores 1 5 9 13
   cache size : 16896 KB

From /proc/meminfo
   MemTotal:        197749764 kB
   HugePages_Total:         0
   Hugepagesize:         2048 kB

From /etc/*release* /etc/*version*
   SuSE-release:
      SUSE Linux Enterprise Server 12 (x86_64)
      VERSION = 12
      PATCHLEVEL = 2
      # This file is deprecated and will be removed in a future service pack or
      release.
      # Please check /etc/os-release for details about this release.
   os-release:
      NAME="SLES"
      VERSION="12-SP2"
      VERSION_ID="12.2"
      PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
      ID="sles"
      ANSI_COLOR="0;32"
      CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
   Linux linux-rugf 4.4.21-68-default #1 SMP Tue Oct 18 18:19:37 UTC 2016
   (63cf368) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Nov 14 01:22

SPEC is set to: /home/cpu2006
   Filesystem      Type  Size  Used Avail Use% Mounted on
   /dev/nvme0n1p4 xfs   331G   83G  248G  26% /home
Additional information from dmidecode:
```

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## Platform Notes (Continued)

```
    Warning: Use caution when you interpret this section. The 'dmidecode' program
    reads system data which is "intended to allow hardware to be accurately
    determined", but the intent may not be met, as there are frequent changes to
    hardware, firmware, and the "DMTF SMBIOS" standard.

    BIOS HPE U32 09/29/2017
    Memory:
    24x UNKNOWN NOT AVAILABLE 8 GB 2 rank 2666 MHz

  (End of data from sysinfo program)
```

## General Notes

```
Environment variables set by runspec before the start of the run:
LD_LIBRARY_PATH = "/home/cpu2006/libs/32:/home/cpu2006/libs/64:/home/cpu2006/sh10.2"

 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.2
```

## Base Compiler Invocation

C benchmarks:
```
    icc -m32 -L/opt/intel/compilers_and_libraries_2018.0.082/linux/lib/ia32
```

C++ benchmarks:
```
    icpc -m32 -L/opt/intel/compilers_and_libraries_2018.0.082/linux/lib/ia32
```

## Base Portability Flags

```
    400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
        401.bzip2: -D_FILE_OFFSET_BITS=64
         403.gcc: -D_FILE_OFFSET_BITS=64
         429.mcf: -D_FILE_OFFSET_BITS=64
       445.gobmk: -D_FILE_OFFSET_BITS=64
       456.hmmer: -D_FILE_OFFSET_BITS=64
        458.sjeng: -D_FILE_OFFSET_BITS=64
    462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
      464.h264ref: -D_FILE_OFFSET_BITS=64
      471.omnetpp: -D_FILE_OFFSET_BITS=64
        473.astar: -D_FILE_OFFSET_BITS=64
    483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```

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# Base Optimization Flags

C benchmarks:

```
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -Wl,-z,muldefs
-L/home/cpu2006/sh10.2 -lsmartheap
```

# Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revG.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revG.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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