



# SPEC® CINT2006 Result

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**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL560 Gen10  
(2.10 GHz, Intel Xeon Platinum 8176)

SPECint®2006 = **Not Run**

SPECint\_base2006 = **80.0**

CPU2006 license: 3

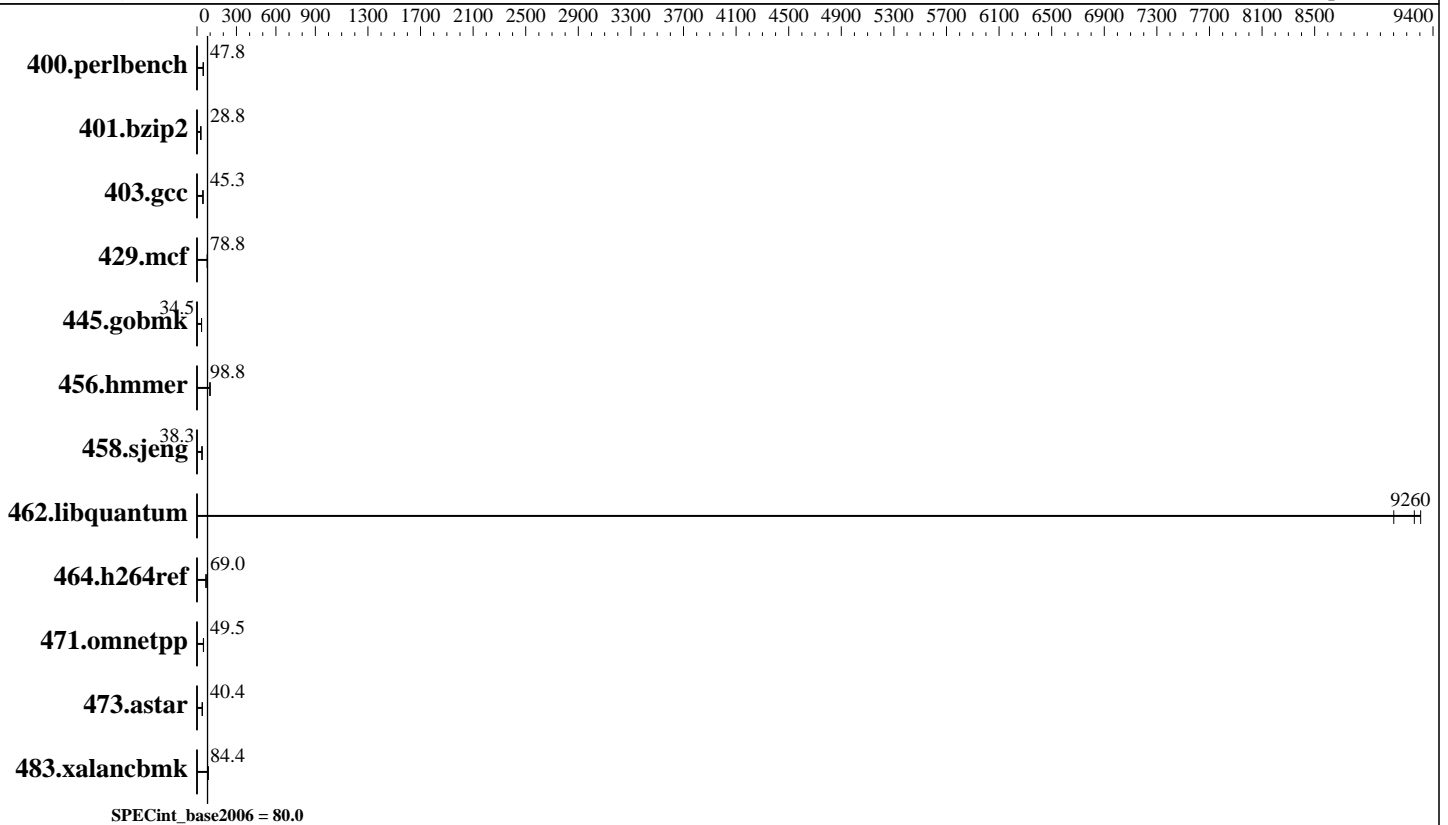
Test sponsor: HPE

Tested by: HPE

Test date: Nov-2017

Hardware Availability: Oct-2017

Software Availability: Apr-2017



## Hardware

CPU Name: Intel Xeon Platinum 8176  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.80 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 112 cores, 4 chips, 28 cores/chip  
 CPU(s) orderable: 1, 2, 4 chip(s)  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 38.5 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 768 GB (48 x 16 GB 2Rx8 PC4-2666V-R)  
 Disk Subsystem: 1 x 480 GB SATA SSD, RAID 0  
 Other Hardware: None

## Software

Operating System: Red Hat Enterprise Linux Server release 7.3 (Maipo)  
 Kernel 3.10.0-514.el7.x86\_64  
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: Not Applicable  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	<b>204</b>	<b>47.8</b>	204	47.8	205	47.7						
401.bzip2	335	28.8	333	28.9	<b>335</b>	<b>28.8</b>						
403.gcc	178	45.3	177	45.4	<b>178</b>	<b>45.3</b>						
429.mcf	115	79.5	118	77.6	<b>116</b>	<b>78.8</b>						
445.gobmk	<b>304</b>	<b>34.5</b>	304	34.5	304	34.5						
456.hammer	94.5	98.7	94.2	99.0	<b>94.4</b>	<b>98.8</b>						
458.sjeng	316	38.3	316	38.3	<b>316</b>	<b>38.3</b>						
462.libquantum	<b>2.24</b>	<b>9260</b>	2.28	9100	2.23	9310						
464.h264ref	<b>321</b>	<b>69.0</b>	323	68.5	320	69.1						
471.omnetpp	129	48.5	126	49.7	<b>126</b>	<b>49.5</b>						
473.astar	174	40.4	<b>174</b>	<b>40.4</b>	174	40.3						
483.xalancbmk	81.7	84.4	<b>81.8</b>	<b>84.4</b>	81.8	84.3						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"  
 Transparent Huge Pages enabled by default  
 Filesystem page cache cleared with:  
 shell invocation of 'sync; echo 3 > /proc/sys/vm/drop\_caches' prior to run  
 IRQ balance service was stop using "service irqbalance stop"  
 Tuned-adm profile was set to Throughput-Performance

## Platform Notes

BIOS Configuration:  
 Intel Hyperthreading set to Disabled  
 Thermal Configuration set to Maximum Cooling  
 LLC Prefetch set to Enabled  
 LLC Dead Line Allocation set to Disabled  
 Memory Patrol Scrubbing set to Disabled  
 Workload Profile set to General Peak Frequency Compute  
 Energy/Performance Bias set to Maximum Performance  
 Workload Profile set to Custom  
 NUMA Group Size Optimization set to Flat  
 Sysinfo program /cpu2006/config/sysinfo.rev6993  
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
 running on DL560-Gen10 Fri Nov 17 11:35:59 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

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## Platform Notes (Continued)

From /proc/cpuinfo

```

model name      : Intel(R) Xeon(R) Platinum 8176 CPU @ 2.10GHz
 4 "physical id"s (chips)
 112 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores    : 28
  siblings     : 28
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
 25 26 27 28 29 30
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
 25 26 27 28 29 30
  physical 2: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
 25 26 27 28 29 30
  physical 3: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14 16 17 18 19 20 21 22 24
 25 26 27 28 29 30
 cache size    : 39424 KB

```

From /proc/meminfo

```

MemTotal:      792066452 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

From /etc/\*release\* /etc/\*version\*

```

os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server

```

uname -a:

```

Linux DL560-Gen10 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13 EDT 2016
x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Nov 17 11:33

SPEC is set to: /cpu2006

```

Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sdal        xfs      447G      41G  407G   9% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to

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## Platform Notes (Continued)

hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U34 09/29/2017

Memory:

48x UNKNOWN NOT AVAILABLE 16 GB 2 rank 2666 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,compact"

LD\_LIBRARY\_PATH = "/cpu2006/lib/ia32:/cpu2006/lib/intel64:/cpu2006/sh10.2"

OMP\_NUM\_THREADS = "112"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.2

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64  
401.bzip2: -DSPEC\_CPU\_LP64  
403.gcc: -DSPEC\_CPU\_LP64  
429.mcf: -DSPEC\_CPU\_LP64  
445.gobmk: -DSPEC\_CPU\_LP64  
456.hmmmer: -DSPEC\_CPU\_LP64  
458.sjeng: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
464.h264ref: -DSPEC\_CPU\_LP64  
471.omnetpp: -DSPEC\_CPU\_LP64  
473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch  
-auto-p32

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## Base Optimization Flags (Continued)

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-Wl,-z,muldefs -L/shl0.2 -lsmartheap64
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revG.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.

Report generated on Tue Dec 12 17:06:52 2017 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 12 December 2017.