



SPEC® CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen10
(3.50 GHz, Intel Xeon Gold 6144)

SPECint®2006 = Not Run

SPECint_base2006 = 82.6

CPU2006 license: 3

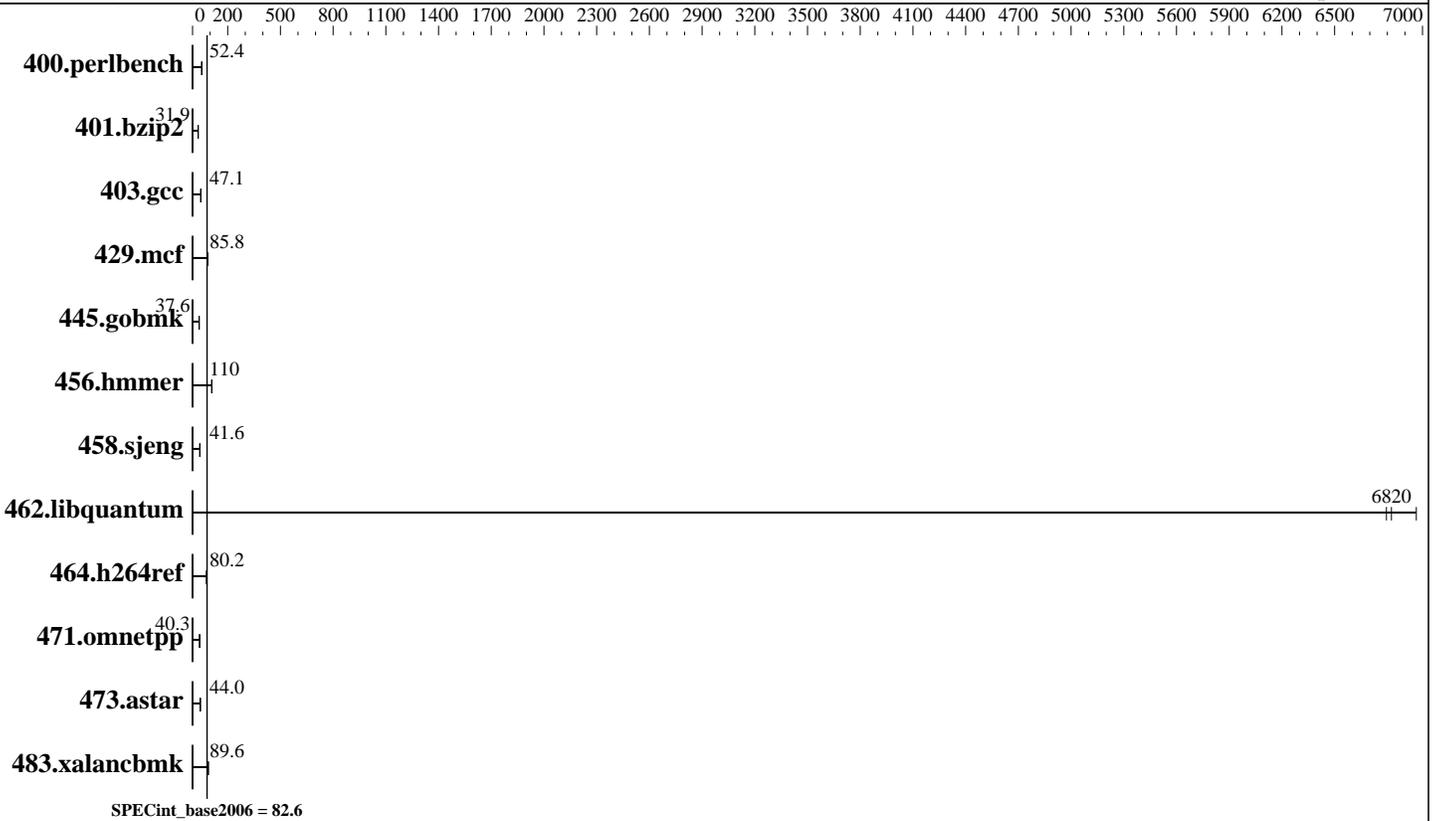
Test sponsor: HPE

Tested by: HPE

Test date: Nov-2017

Hardware Availability: Oct-2017

Software Availability: Apr-2017



Hardware

CPU Name: Intel Xeon Gold 6144
CPU Characteristics: Intel Turbo Boost Technology up to 4.20 GHz
CPU MHz: 3500
FPU: Integrated
CPU(s) enabled: 16 cores, 2 chips, 8 cores/chip
CPU(s) orderable: 1, 2 chip(s)
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 1 MB I+D on chip per core
L3 Cache: 24.75 MB I+D on chip per chip
Other Cache: None
Memory: 192 GB (24 x 8 GB 2Rx8 PC4-2666V-R)
Disk Subsystem: 1 x 480 GB SATA SSD, RAID 0
Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.3 (Maipo), Kernel 3.10.0-514.el7.x86_64
Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux
Auto Parallel: Yes
File System: xfs
System State: Run level 3 (multi-user)
Base Pointers: 32/64-bit
Peak Pointers: Not Applicable
Other Software: Microquill SmartHeap V10.2



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen10

(3.50 GHz, Intel Xeon Gold 6144)

SPECint2006 =

Not Run

SPECint_base2006 =

82.6

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Nov-2017

Hardware Availability: Oct-2017

Software Availability: Apr-2017

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	185	52.7	186	52.4	186	52.4						
401.bzip2	302	31.9	303	31.8	303	31.9						
403.gcc	171	47.1	171	47.2	171	47.0						
429.mcf	106	85.8	105	86.8	107	85.0						
445.gobmk	279	37.6	280	37.4	279	37.6						
456.hmmer	85.1	110	85.3	109	85.1	110						
458.sjeng	291	41.6	291	41.6	291	41.6						
462.libquantum	3.05	6790	2.97	6960	3.04	6820						
464.h264ref	276	80.2	275	80.4	278	79.6						
471.omnetpp	154	40.6	155	40.3	156	40.0						
473.astar	161	43.7	159	44.1	159	44.0						
483.xalancbmk	78.0	88.5	77.0	89.6	77.0	89.6						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
IRQ balance service was stop using "service irqbalance stop"
Tuned-adm profile was set to Throughput-Performance

Platform Notes

BIOS Configuration:

Intel Hyperthreading set to Disabled
Thermal Configuration set to Maximum Cooling
LLC Prefetch set to Enabled
LLC Dead Line Allocation set to Disabled
Memory Patrol Scrubbing set to Disabled
Workload Profile set to General Peak Frequency Compute
Energy/Performance Bias set to Maximum Performance
Workload Profile set to Custom
NUMA Group Size Optimization set to Flat
Sysinfo program /home/specuser/cpu2006/config/sysinfo.rev6993
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
running on dl360gen10rhel173 Wed Nov 22 12:58:32 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen10
(3.50 GHz, Intel Xeon Gold 6144)

SPECint2006 =

Not Run

SPECint_base2006 =

82.6

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Nov-2017

Hardware Availability: Oct-2017

Software Availability: Apr-2017

Platform Notes (Continued)

```

From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6144 CPU @ 3.50GHz
 2 "physical id"s (chips)
 16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 8
  siblings  : 8
  physical 0: cores 0 2 3 9 16 19 26 27
  physical 1: cores 0 2 3 9 16 19 26 27
cache size : 25344 KB

```

```

From /proc/meminfo
MemTotal:      197574320 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

```

From /etc/*release* /etc/*version*
os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server

```

```

uname -a:
Linux dl360gen10rhel73 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13 EDT
2016 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Nov 22 10:53

```

SPEC is set to: /home/specuser/cpu2006
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/mapper/rhel-home xfs   392G   37G  356G  10% /home

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U32 09/29/2017

Memory:
24x UNKNOWN NOT AVAILABLE 8 GB 2 rank 2666 MHz

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen10
(3.50 GHz, Intel Xeon Gold 6144)

SPECint2006 = Not Run

SPECint_base2006 = 82.6

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Nov-2017

Hardware Availability: Oct-2017

Software Availability: Apr-2017

Platform Notes (Continued)

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/specuser/cpu2006/lib/ia32:/home/specuser/cpu2006/lib/intel64:/home/specuser/cpu2006/sh10.2"

OMP_NUM_THREADS = "16"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
-auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Hewlett Packard Enterprise

(Test Sponsor: HPE)

ProLiant DL360 Gen10
(3.50 GHz, Intel Xeon Gold 6144)

SPECint2006 = Not Run

SPECint_base2006 = 82.6

CPU2006 license: 3

Test sponsor: HPE

Tested by: HPE

Test date: Nov-2017

Hardware Availability: Oct-2017

Software Availability: Apr-2017

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revG.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revG.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Tue Dec 12 17:06:52 2017 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 12 December 2017.