



# SPEC® CFP2006 Result

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Hewlett Packard Enterprise  
(Test Sponsor: HPE)

ProLiant DL360 Gen10  
(3.20 GHz, Intel Xeon Gold 6146)

**SPECfp®\_rate2006 = Not Run**

**SPECfp\_rate\_base2006 = 1240**

**CPU2006 license:** 3

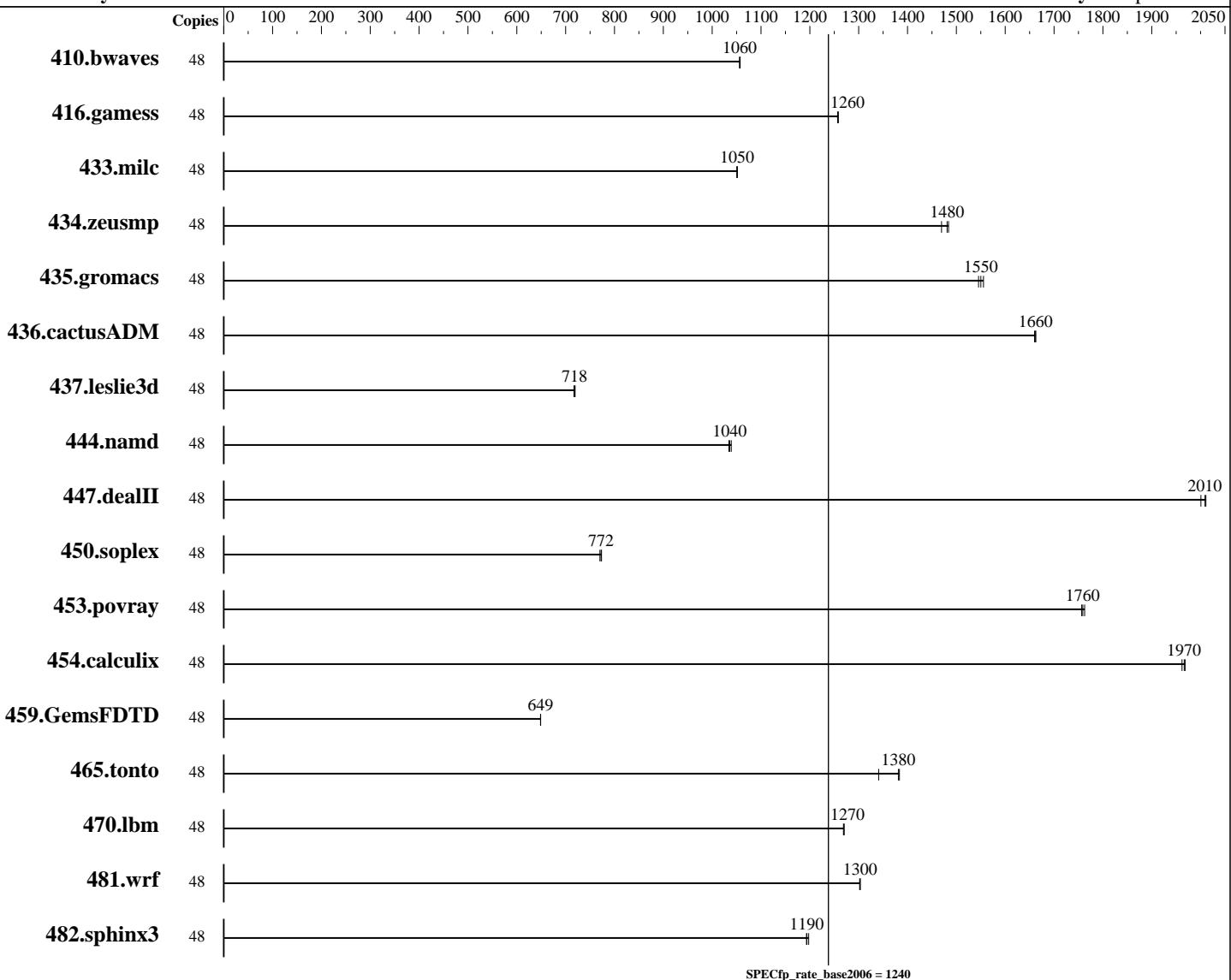
**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Nov-2017

**Hardware Availability:** Oct-2017

**Software Availability:** Apr-2017



## Hardware

CPU Name: Intel Xeon Gold 6146  
CPU Characteristics: Intel Turbo Boost Technology up to 4.20 GHz  
CPU MHz: 3200  
FPU: Integrated  
CPU(s) enabled: 24 cores, 2 chips, 12 cores/chip, 2 threads/core  
CPU(s) orderable: 1, 2 chip(s)  
Primary Cache: 32 KB I + 32 KB D on chip per core  
Secondary Cache: 1 MB I+D on chip per core

## Software

Operating System: SUSE Linux Enterprise Server 12 (x86\_64) SP2  
Compiler: Kernel 4.4.21-69-default  
C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;  
Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux  
Auto Parallel: No  
File System: xfs  
System State: Run level 3 (multi-user)

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L3 Cache: 24.75 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 192 GB (24 x 8 GB 2Rx8 PC4-2666V-R)  
 Disk Subsystem: 1 x 480 GB SATA SSD, RAID 0  
 Other Hardware: None

Base Pointers: 32/64-bit  
 Peak Pointers: Not Applicable  
 Other Software: None

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	48	<b>617</b>	<b>1060</b>	617	1060	618	1060							
416.gamess	48	748	1260	747	1260	<b>747</b>	<b>1260</b>							
433.milc	48	419	1050	419	1050	<b>419</b>	<b>1050</b>							
434.zeusmp	48	<b>295</b>	<b>1480</b>	294	1480	297	1470							
435.gromacs	48	220	1560	<b>221</b>	<b>1550</b>	222	1550							
436.cactusADM	48	346	1660	<b>345</b>	<b>1660</b>	345	1660							
437.leslie3d	48	629	717	<b>628</b>	<b>718</b>	627	719							
444.namd	48	372	1030	370	1040	<b>372</b>	<b>1040</b>							
447.dealII	48	273	2010	<b>273</b>	<b>2010</b>	274	2000							
450.soplex	48	518	773	520	770	<b>518</b>	<b>772</b>							
453.povray	48	145	1760	<b>145</b>	<b>1760</b>	145	1760							
454.calculix	48	201	1970	<b>201</b>	<b>1970</b>	202	1960							
459.GemsFDTD	48	785	649	785	649	<b>785</b>	<b>649</b>							
465.tonto	48	352	1340	341	1380	<b>342</b>	<b>1380</b>							
470.lbm	48	520	1270	<b>519</b>	<b>1270</b>	519	1270							
481.wrf	48	412	1300	411	1300	<b>412</b>	<b>1300</b>							
482.sphinx3	48	784	1190	781	1200	<b>783</b>	<b>1190</b>							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop\_caches' prior to run runspec command invoked through numactl i.e.:

numactl --interleave=all runspec <etc>

irqbalance disabled with "service irqbalance stop"

tuned profile set with "tuned-adm profile throughput-performance"

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## Operating System Notes (Continued)

VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty\_ratio"  
Numa balancing was disabled using "echo 0 > /proc/sys/kernel numa\_balancing"

## Platform Notes

BIOS Configuration:

Thermal Configuration set to Maximum Cooling  
LLC Prefetch set to Enabled  
LLC Dead Line Allocation set to Disabled  
Memory Patrol Scrubbing set to Disabled  
Workload Profile set to General Throughput Compute  
Minimum Processor Idle Power Core C-State set to C1E  
Sysinfo program /home/specuser/cpu2006/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on linux-lvuc Sat Nov 11 01:06:32 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
    model name : Intel(R) Xeon(R) Gold 6146 CPU @ 3.20GHz
        2 "physical id"s (chips)
        48 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
    cpu cores : 12
    siblings : 24
    physical 0: cores 0 1 2 3 4 8 9 11 17 18 19 20
    physical 1: cores 0 1 2 3 8 9 10 11 18 19 24 27
    cache size : 25344 KB
```

```
From /proc/meminfo
    MemTotal:       197552060 kB
    HugePages_Total:      0
    Hugepagesize:     2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
    SUSE Linux Enterprise Server 12 (x86_64)
    VERSION = 12
    PATCHLEVEL = 2
    # This file is deprecated and will be removed in a future service pack or
    release.
    # Please check /etc/os-release for details about this release.
os-release:
    NAME="SLES"
    VERSION="12-SP2"
    VERSION_ID="12.2"
    PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
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```



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## Platform Notes (Continued)

```
ID="sles"  
ANSI_COLOR="0;32"  
CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

```
uname -a:  
Linux linux-lvuc 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016  
(9464f67) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Nov 10 20:25
```

```
SPEC is set to: /home/specuser/cpu2006  
Filesystem      Type  Size  Used  Avail Use% Mounted on  
/dev/sda4        xfs   405G  143G  263G  36% /home  
Additional information from dmidecode:
```

```
Warning: Use caution when you interpret this section. The 'dmidecode' program  
reads system data which is "intended to allow hardware to be accurately  
determined", but the intent may not be met, as there are frequent changes to  
hardware, firmware, and the "DMTF SMBIOS" standard.
```

```
BIOS HPE U32 09/29/2017
```

```
Memory:
```

```
24x UNKNOWN NOT AVAILABLE 8 GB 2 rank 2666 MHz
```

```
(End of data from sysinfo program)
```

## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/home/specuser/cpu2006/lib/ia32:/home/specuser/cpu2006/lib/intel64:/home/specuser/cpu2006/sh10.2"
```

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM  
memory using Redhat Enterprise Linux 7.2

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Benchmarks using both Fortran and C:

```
icc -m64 ifort -m64
```



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## Base Portability Flags

```

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
    433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
    444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
    465.tonto: -DSPEC_CPU_LP64
    470.lbm: -DSPEC_CPU_LP64
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

```

## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

Fortran benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
```

Benchmarks using both Fortran and C:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revF.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revF.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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