



# SPEC® CINT2006 Result

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**Hewlett Packard Enterprise**

(Test Sponsor: HPE)

ProLiant DL360 Gen10  
(3.20 GHz, Intel Xeon Gold 6134)

**SPECint®\_rate2006 = Not Run**

**SPECint\_rate\_base2006 = 1080**

**CPU2006 license:** 3

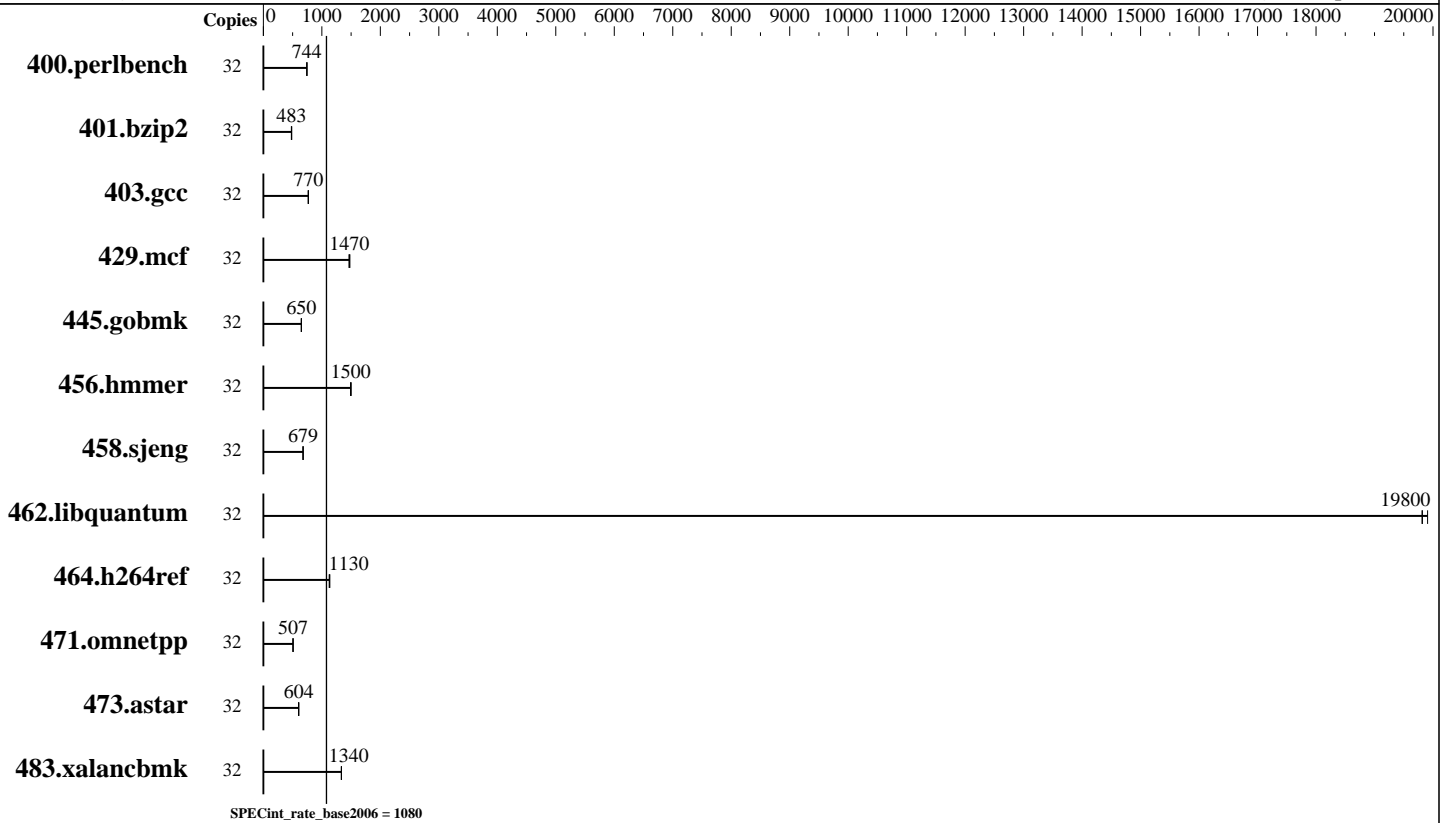
**Test sponsor:** HPE

**Tested by:** HPE

**Test date:** Oct-2017

**Hardware Availability:** Oct-2017

**Software Availability:** Sep-2017



## Hardware

**CPU Name:** Intel Xeon Gold 6134  
**CPU Characteristics:** Intel Turbo Boost Technology up to 3.70 GHz  
**CPU MHz:** 3200  
**FPU:** Integrated  
**CPU(s) enabled:** 16 cores, 2 chips, 8 cores/chip, 2 threads/core  
**CPU(s) orderable:** 1, 2 chip(s)  
**Primary Cache:** 32 KB I + 32 KB D on chip per core  
**Secondary Cache:** 1 MB I+D on chip per core  
**L3 Cache:** 24.75 MB I+D on chip per chip  
**Other Cache:** None  
**Memory:** 192 GB (24 x 8 GB 2Rx8 PC4-2666V-R)  
**Disk Subsystem:** 1 x 480 GB SATA SSD, RAID 0  
**Other Hardware:** None

## Software

**Operating System:** SUSE Linux Enterprise Server 12 (x86\_64) SP2  
 Kernel 4.4.21-69-default  
**Compiler:** C/C++: Version 18.0.0.128 of Intel C/C++ Compiler for Linux  
**Auto Parallel:** No  
**File System:** xfs  
**System State:** Run level 3 (multi-user)  
**Base Pointers:** 32-bit  
**Peak Pointers:** Not Applicable  
**Other Software:** Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
400.perlbench	32	420	745	<b>420</b>	<b>744</b>	421	742									
401.bzip2	32	<b>640</b>	<b>483</b>	645	479	638	484									
403.gcc	32	336	766	334	771	<b>335</b>	<b>770</b>									
429.mcf	32	<b>198</b>	<b>1470</b>	199	1460	197	1480									
445.gobmk	32	518	648	516	651	<b>516</b>	<b>650</b>									
456.hammer	32	<b>199</b>	<b>1500</b>	200	1490	199	1500									
458.sjeng	32	570	679	<b>570</b>	<b>679</b>	570	679									
462.libquantum	32	33.3	19900	<b>33.5</b>	<b>19800</b>	33.5	19800									
464.h264ref	32	624	1130	629	1130	<b>625</b>	<b>1130</b>									
471.omnetpp	32	394	508	<b>394</b>	<b>507</b>	395	507									
473.astar	32	<b>372</b>	<b>604</b>	374	601	371	606									
483.xalancbmk	32	165	1340	166	1330	<b>165</b>	<b>1340</b>									

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

```
Stack size set to unlimited using "ulimit -s unlimited"
Transparent Huge Pages enabled by default
Filesystem page cache cleared with:
  shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
runspec command invoked through numactl i.e.:
  numactl --interleave=all runspec <etc>
irqbalance disabled with "service irqbalance stop"
tuned profile set with "tuned-adm profile throughput-performance"
VM Dirty ratio was set to 40 using "echo 40 > /proc/sys/vm/dirty_ratio"
Numa balancing was disabled using "echo 0 > /proc/sys/kernel/numa_balancing"
```

## Platform Notes

```
BIOS Configuration:
Thermal Configuration set to Maximum Cooling
Memory Patrol Scrubbing set to Disabled
LLC Prefetcher set to Enabled
LLC Dead Line Allocation set to Disabled
Workload Pofile set to General Throughput Compute
Minimum Processor Idle Power Core C-State set to C1E
```

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## Platform Notes (Continued)

Sysinfo program /cpu2006/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on linux-lvuc Thu Oct 26 08:42:59 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Gold 6134 CPU @ 3.20GHz
 2 "physical id"s (chips)
 32 "processors"
```

cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

```
cpu cores : 8
siblings   : 16
physical 0: cores 0 2 3 9 16 19 26 27
physical 1: cores 4 8 9 11 16 18 19 25
cache size : 25344 KB
```

From /proc/meminfo

```
MemTotal:      197554684 kB
HugePages_Total: 0
Hugepagesize:   2048 kB
```

From /etc/\*release\* /etc/\*version\*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
```

os-release:

```
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="/o:suse:sles:12:sp2"
```

uname -a:

```
Linux linux-lvuc 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Oct 26 08:39

SPEC is set to: /cpu2006

```
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sda4        xfs       405G  137G  269G  34% /home
```

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## Platform Notes (Continued)

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS HPE U32 09/29/2017

Memory:

24x UNKNOWN NOT AVAILABLE 8 GB 2 rank 2666 MHz

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

LD\_LIBRARY\_PATH = "/cpu2006/libs/32:/cpu2006/libs/64:/cpu2006/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

## Base Compiler Invocation

C benchmarks:

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2018.0.082/linux/lib/ia32

C++ benchmarks:

icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2018.0.082/linux/lib/ia32

## Base Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -D\_FILE\_OFFSET\_BITS=64  
403.gcc: -D\_FILE\_OFFSET\_BITS=64  
429.mcf: -D\_FILE\_OFFSET\_BITS=64  
445.gobmk: -D\_FILE\_OFFSET\_BITS=64  
456.hmmer: -D\_FILE\_OFFSET\_BITS=64  
458.sjeng: -D\_FILE\_OFFSET\_BITS=64  
462.libquantum: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX  
464.h264ref: -D\_FILE\_OFFSET\_BITS=64  
471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
473.astar: -D\_FILE\_OFFSET\_BITS=64  
483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:

```
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/cpu2006/sh10.2 -lsmartheap
```

## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/HPE-Platform-Flags-Intel-V1.2-SKX-revD.xml>

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For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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