SPEC® CINT2006 Result
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Cisco Systems

Cisco UCS B200 M5 (Intel Xeon Gold 6130, 2.10GHz)

$SPECint®2006 =$ Not Run

$SPECint\_base2006 =$ 68.8

CPU2006 license: 9019	**Test date:** Sep-2017
Test sponsor: Cisco Systems	**Hardware Availability:** Aug-2017
Tested by: Cisco Systems	**Software Availability:** Apr-2017

0 200 400 600 800 1100 1400 1700 2000 2300 2600 2900 3200 3500 3800 4100 4400 4700 5000 5300 5600 5900 6200 6500 6900

- 400.perlbench 44.9
- 401.bzip2 27.4
- 403.gcc 33.9
- 429.mcf 70.4
- 445.gobmk 31.9
- 456.hmmer 89.6
- 458.sjeng 34.0
- 462.libquantum 6700
- 464.h264ref 60.9
- 471.omnetpp 31.5
- 473.astar 37.3
- 483.xalancbmk 81.5

SPECint_base2006 = 68.8

Hardware

CPU Name:	Intel Xeon Gold 6130
CPU Characteristics:	Intel Turbo Boost Technology up to 3.70 GHz
CPU MHz:	2100
FPU:	Integrated
CPU(s) enabled:	32 cores, 2 chips, 16 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	22 MB I+D on chip per chip
Other Cache:	None
Memory:	768 GB (24 x 32 GB 2Rx4 PC4-2666V-R)
Disk Subsystem:	1 x 300 GB SAS SSD
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 12 SP2 (x86_64) 4.4.21-69-default
Compiler:	C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux
Auto Parallel:	Yes
File System:	xfs
System State:	Run level 3 (multi-user)
Base Pointers:	32/64-bit
Peak Pointers:	Not Applicable
Other Software:	Microquill SmartHeap V10.2

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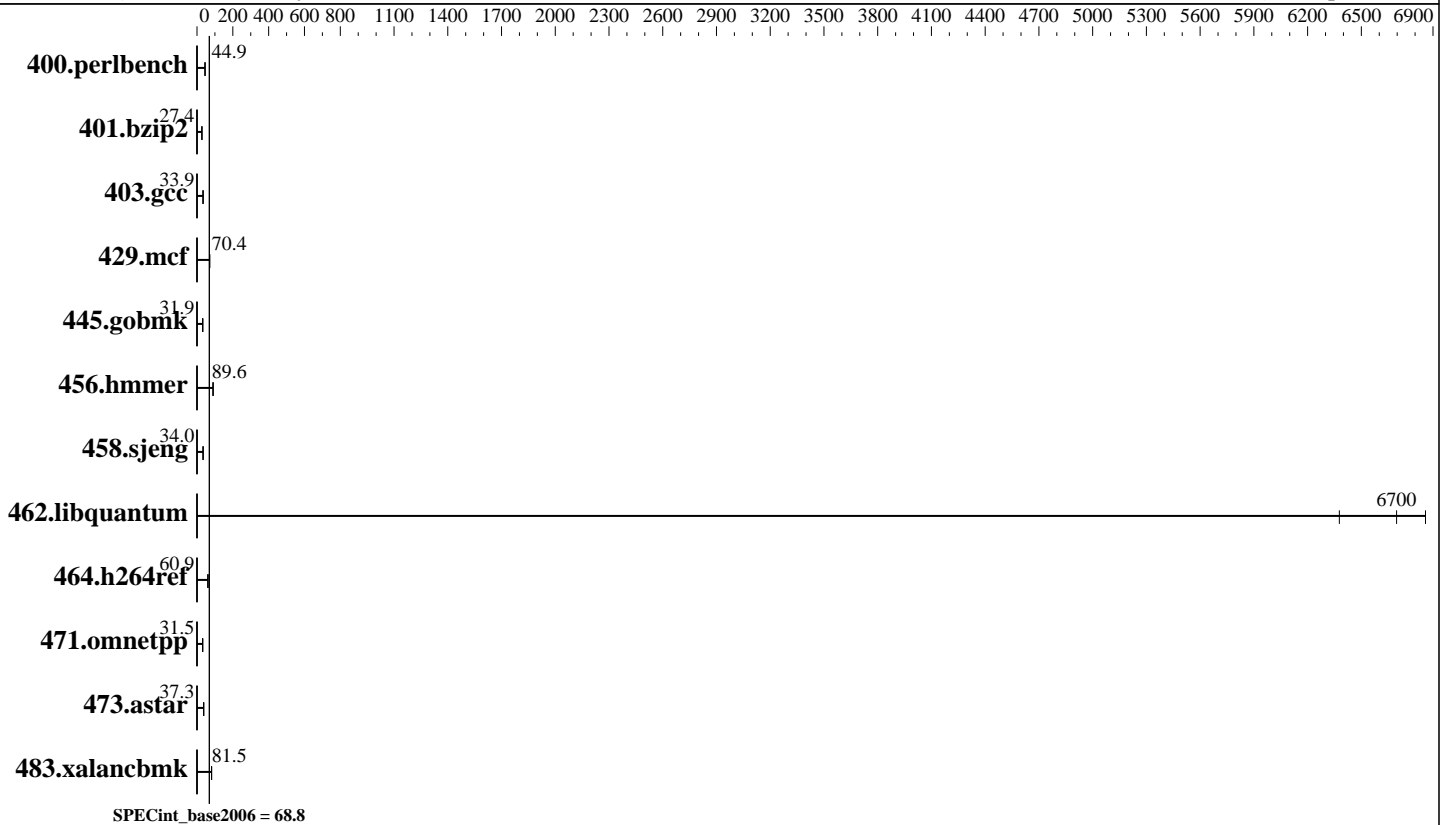
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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**218**	**44.9**	218	44.7	218	44.9						
401.bzip2	353	27.3	**352**	**27.4**	352	27.4						
403.gcc	**237**	**33.9**	237	33.9	237	33.9						
429.mcf	**130**	**70.4**	128	71.1	130	70.3						
445.gobmk	**328**	**31.9**	328	31.9	328	31.9						
456.hmmer	**104**	**89.6**	103	90.2	104	89.4						
458.sjeng	357	33.9	355	34.1	**356**	**34.0**						
462.libquantum	3.25	6380	**3.09**	**6700**	3.02	6860						
464.h264ref	361	61.3	**363**	**60.9**	364	60.8						
471.omnetpp	200	31.2	**198**	**31.5**	194	32.3						
473.astar	188	37.2	188	37.4	**188**	**37.3**						
483.xalancbmk	84.6	81.5	**84.6**	**81.5**	85.1	81.1						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

```
    BIOS Settings:
    Intel HyperThreading Technology set to Disabled
    CPU performance set to Enterprise
    Power Performance Tuning set to OS
    SNC set to Disabled
    IMC Interleaving set to Auto
    Patrol Scrub set to Disabled
     Sysinfo program /home/cpu2006-1.2/config/sysinfo.rev6993
     Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
     running on linux Mon Sep 11 03:51:17 2017

     This section contains SUT (System Under Test) info as seen by
     some common utilities.  To remove or add to this section, see:
       http://www.spec.org/cpu2006/Docs/config.html#sysinfo

     From /proc/cpuinfo
        model name : Intel(R) Xeon(R) Gold 6130 CPU @ 2.10GHz
           2 "physical id"s (chips)
           32 "processors"
        cores, siblings (Caution: counting these is hw and system dependent.  The
        following excerpts from /proc/cpuinfo might not be reliable.  Use with
        caution.)
           cpu cores : 16
```

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Platform Notes (Continued)

```
     siblings  : 16
     physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
     physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
   cache size : 22528 KB

From /proc/meminfo
   MemTotal:       791029252 kB
   HugePages_Total:        0
   Hugepagesize:        2048 kB

From /etc/*release* /etc/*version*
   SuSE-release:
     SUSE Linux Enterprise Server 12 (x86_64)
     VERSION = 12
     PATCHLEVEL = 2
     # This file is deprecated and will be removed in a future service pack or
     release.
     # Please check /etc/os-release for details about this release.
   os-release:
     NAME="SLES"
     VERSION="12-SP2"
     VERSION_ID="12.2"
     PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
     ID="sles"
     ANSI_COLOR="0;32"
     CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:
   Linux linux 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016 (9464f67)
   x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Jan 1 19:26

SPEC is set to: /home/cpu2006-1.2
   Filesystem      Type   Size   Used Avail Use% Mounted on
   /dev/sda1       xfs    280G    22G  258G   8% /
Additional information from dmidecode:

   Warning: Use caution when you interpret this section. The 'dmidecode' program
   reads system data which is "intended to allow hardware to be accurately
   determined", but the intent may not be met, as there are frequent changes to
   hardware, firmware, and the "DMTF SMBIOS" standard.

  BIOS Cisco Systems, Inc. B200M5.3.2.1d.5.0727171353 07/27/2017
  Memory:
   24x 0xCE00 M393A4K40BB2-CTD 32 GB 2 rank 2666 MHz

(End of data from sysinfo program)
```

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General Notes

```
Environment variables set by runspec before the start of the run:
KMP_AFFINITY = "granularity=fine,compact"
LD_LIBRARY_PATH = "/home/cpu2006-1.2/lib/ia32:/home/cpu2006-1.2/lib/intel64:/home/cpu2006-1.2/sh10.2"
OMP_NUM_THREADS = "32"

 Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
 memory using Redhat Enterprise Linux 7.2
 Transparent Huge Pages enabled with:
 echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

Base Compiler Invocation

C benchmarks:
```
    icc -m64
```

C++ benchmarks:
```
    icpc -m64
```

Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
       401.bzip2: -DSPEC_CPU_LP64
         403.gcc: -DSPEC_CPU_LP64
         429.mcf: -DSPEC_CPU_LP64
       445.gobmk: -DSPEC_CPU_LP64
       456.hmmer: -DSPEC_CPU_LP64
        458.sjeng: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
      464.h264ref: -DSPEC_CPU_LP64
      471.omnetpp: -DSPEC_CPU_LP64
        473.astar: -DSPEC_CPU_LP64
    483.xalancbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
    -xCORE-AVX512 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
    -auto-p32
```

C++ benchmarks:
```
    -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
    -Wl,-z,muldefs -L/sh10.2 -lsmartheap64
```

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Base Other Flags

C benchmarks:

>   403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml
http://www.spec.org/cpu2006/flags/Cisco-Platform-Settings-V1.2-revH.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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