



SPEC[®] CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECint[®]2006 = **78.0**

ThinkSystem SR850
(2.10 GHz, Intel Xeon Gold 6130)

SPECint_base2006 = **73.9**

CPU2006 license: 9017

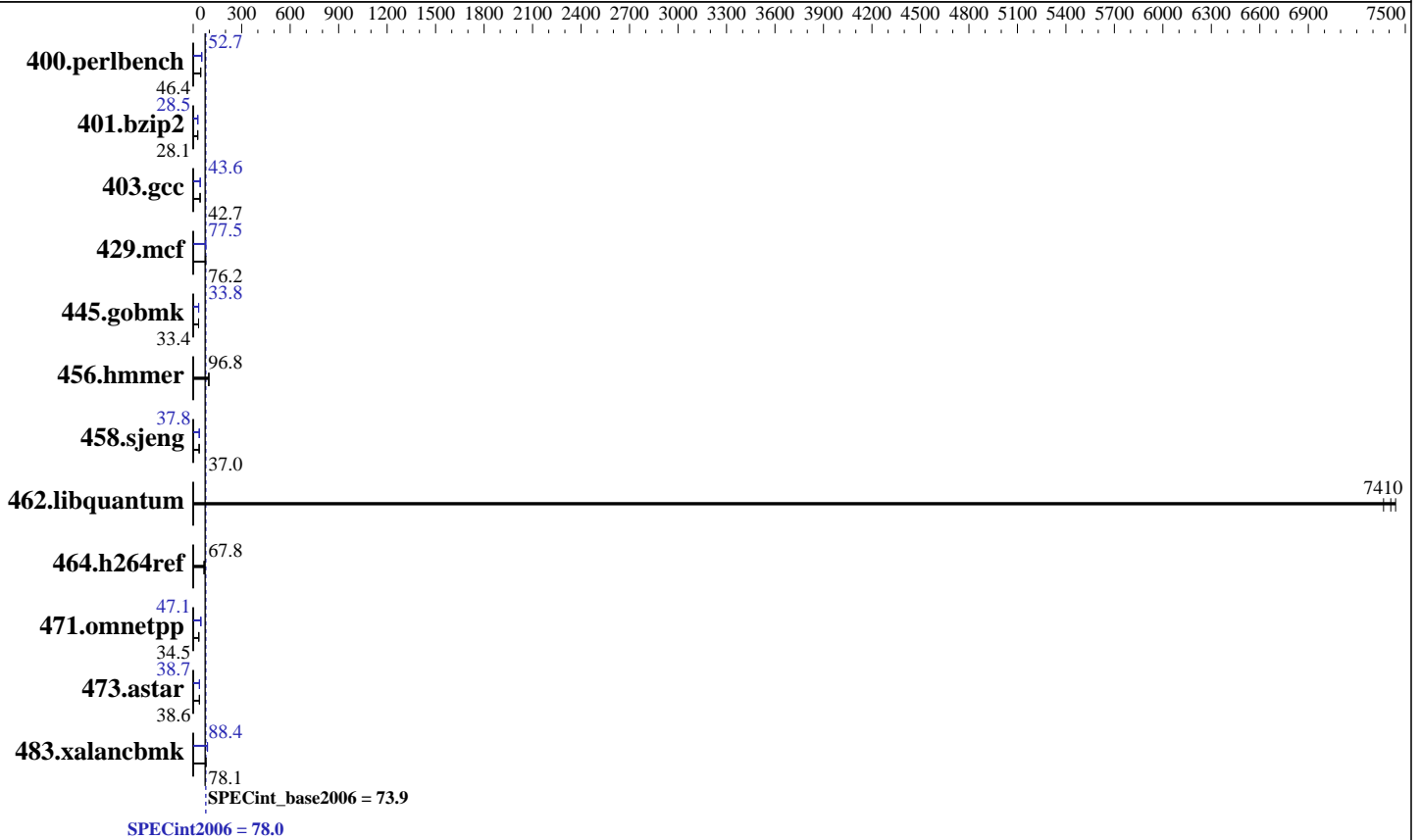
Test date: Sep-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Apr-2017



Hardware

CPU Name: Intel Xeon Gold 6130
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
 CPU MHz: 2100
 FPU: Integrated
 CPU(s) enabled: 64 cores, 4 chips, 16 cores/chip
 CPU(s) orderable: 2,4 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core
 L3 Cache: 22 MB I+D on chip per chip
 Other Cache: None
 Memory: 1536 GB (48 x 32 GB 2Rx4 PC4-2666V-R)
 Disk Subsystem: 1 x 800 GB SATA SSD
 Other Hardware: None

Software

Operating System: Red Hat Enterprise Linux Server release 7.3 (Maipo)
 Kernel 3.10.0-514.el7.x86_64
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 32/64-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.2



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR850
(2.10 GHz, Intel Xeon Gold 6130)

SPECint2006 = **78.0**

SPECint_base2006 = **73.9**

CPU2006 license: 9017

Test sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test date: Sep-2017

Hardware Availability: Aug-2017

Software Availability: Apr-2017

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	210	46.5	<u>211</u>	<u>46.4</u>	211	46.3	<u>185</u>	<u>52.7</u>	186	52.6	185	52.8
401.bzip2	343	28.1	<u>343</u>	<u>28.1</u>	344	28.1	339	28.5	339	28.5	<u>339</u>	<u>28.5</u>
403.gcc	188	42.7	189	42.7	<u>188</u>	<u>42.7</u>	184	43.6	185	43.6	<u>185</u>	<u>43.6</u>
429.mcf	121	75.1	116	78.4	<u>120</u>	<u>76.2</u>	118	77.0	<u>118</u>	<u>77.5</u>	118	77.5
445.gobmk	314	33.4	313	33.5	<u>314</u>	<u>33.4</u>	310	33.8	<u>310</u>	<u>33.8</u>	310	33.8
456.hammer	<u>96.4</u>	<u>96.8</u>	96.5	96.7	96.3	96.8	<u>96.4</u>	<u>96.8</u>	96.5	96.7	96.3	96.8
458.sjeng	327	37.0	<u>327</u>	<u>37.0</u>	327	37.0	320	37.8	<u>320</u>	<u>37.8</u>	320	37.8
462.libquantum	2.81	7370	<u>2.80</u>	<u>7410</u>	2.78	7440	2.81	7370	<u>2.80</u>	<u>7410</u>	2.78	7440
464.h264ref	327	67.7	<u>327</u>	<u>67.8</u>	327	67.8	327	67.7	<u>327</u>	<u>67.8</u>	327	67.8
471.omnetpp	181	34.5	<u>181</u>	<u>34.5</u>	179	35.0	132	47.3	<u>133</u>	<u>47.1</u>	133	47.0
473.astar	<u>182</u>	<u>38.6</u>	182	38.6	182	38.6	<u>181</u>	<u>38.7</u>	181	38.7	181	38.8
483.xalancbmk	<u>88.3</u>	<u>78.1</u>	88.5	77.9	87.4	79.0	78.1	88.4	78.1	88.4	<u>78.1</u>	<u>88.4</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS Configuration:

Choose Operating Mode set to Maximum Performance
 LLC dead line alloc set to Disable
 Hyper-Threading set to Disable
 Sysinfo program /home/cpu2006-1.2-ic17.0u3/config/sysinfo.rev6993
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
 running on electron-rhel7.3 Tue Sep 12 01:24:14 2017

This section contains SUT (System Under Test) info as seen by
 some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Gold 6130 CPU @ 2.10GHz
 4 "physical id"s (chips)
 64 "processors"

cores, siblings (Caution: counting these is hw and system dependent. The
 following excerpts from /proc/cpuinfo might not be reliable. Use with
 caution.)

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECint2006 = 78.0

ThinkSystem SR850
(2.10 GHz, Intel Xeon Gold 6130)

SPECint_base2006 = 73.9

CPU2006 license: 9017

Test date: Sep-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Apr-2017

Platform Notes (Continued)

```

cpu cores : 16
siblings  : 16
physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 2: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
physical 3: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
cache size : 22528 KB

```

From /proc/meminfo

```

MemTotal:      1584978820 kB
HugePages_Total:      0
Hugepagesize:    2048 kB

```

From /etc/*release* /etc/*version*

```

os-release:
NAME="Red Hat Enterprise Linux Server"
VERSION="7.3 (Maipo)"
ID="rhel"
ID_LIKE="fedora"
VERSION_ID="7.3"
PRETTY_NAME="Red Hat Enterprise Linux Server 7.3 (Maipo)"
ANSI_COLOR="0;31"
CPE_NAME="cpe:/o:redhat:enterprise_linux:7.3:GA:server"
redhat-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release: Red Hat Enterprise Linux Server release 7.3 (Maipo)
system-release-cpe: cpe:/o:redhat:enterprise_linux:7.3:ga:server

```

uname -a:

```

Linux electron-rhel7.3 3.10.0-514.el7.x86_64 #1 SMP Wed Oct 19 11:24:13 EDT
2016 x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Sep 12 01:23

SPEC is set to: /home/cpu2006-1.2-ic17.0u3

```

Filesystem      Type      Size      Used Avail Use% Mounted on
/dev/sda4        xfs       688G      5.1G  683G   1% /home

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Lenovo -[TEE113J-1.00]- 06/03/2017

Memory:

48x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666 MHz

(End of data from sysinfo program)



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECint2006 = 78.0

ThinkSystem SR850
(2.10 GHz, Intel Xeon Gold 6130)

SPECint_base2006 = 73.9

CPU2006 license: 9017

Test date: Sep-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Apr-2017

General Notes

Environment variables set by runspec before the start of the run:

KMP_AFFINITY = "granularity=fine,compact"

LD_LIBRARY_PATH = "/home/cpu2006-1.2-ic17.0u3/lib/ia32:/home/cpu2006-1.2-ic17.0u3/lib/intel64:/home/cpu2006-1.2-ic17.0u3/sh10.2"

OMP_NUM_THREADS = "64"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM

memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default.

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run

Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
471.omnetpp: -DSPEC_CPU_LP64
473.astar: -DSPEC_CPU_LP64
483.xalanbmk: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch
-auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECint2006 = 78.0

ThinkSystem SR850
(2.10 GHz, Intel Xeon Gold 6130)

SPECint_base2006 = 73.9

CPU2006 license: 9017

Test date: Sep-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Apr-2017

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

C++ benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

473.astar: icpc -m64

Peak Portability Flags

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32

401.bzip2: -DSPEC_CPU_LP64

403.gcc: -DSPEC_CPU_LP64

429.mcf: -DSPEC_CPU_LP64

445.gobmk: -D_FILE_OFFSET_BITS=64

456.hmmmer: -DSPEC_CPU_LP64

458.sjeng: -DSPEC_CPU_LP64

462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX

464.h264ref: -DSPEC_CPU_LP64

471.omnetpp: -D_FILE_OFFSET_BITS=64

473.astar: -DSPEC_CPU_LP64

483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div -auto-ilp32 -qopt-prefetch

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

SPECint2006 = 78.0

ThinkSystem SR850
(2.10 GHz, Intel Xeon Gold 6130)

SPECint_base2006 = 73.9

CPU2006 license: 9017

Test date: Sep-2017

Test sponsor: Lenovo Global Technology

Hardware Availability: Aug-2017

Tested by: Lenovo Global Technology

Software Availability: Apr-2017

Peak Optimization Flags (Continued)

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc
-qopt-malloc-options=3 -auto-ilp32

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel
-qopt-prefetch -auto-p32

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2)

456.hmmr: basepeak = yes

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-ra-region-strategy=block
-Wl,-z,muldefs -L/sh10.2 -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-auto-p32 -Wl,-z,muldefs -L/sh10.2 -lsmartheap64

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-Wl,-z,muldefs -L/sh10.2 -lsmartheap

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.20171004.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Lenovo-Platform-Flags-V1.2-SKL-C.20171004.xml>



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Lenovo Global Technology

ThinkSystem SR850
(2.10 GHz, Intel Xeon Gold 6130)

SPECint2006 = 78.0

SPECint_base2006 = 73.9

CPU2006 license: 9017

Test sponsor: Lenovo Global Technology

Tested by: Lenovo Global Technology

Test date: Sep-2017

Hardware Availability: Aug-2017

Software Availability: Apr-2017

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.
Report generated on Wed Oct 4 12:39:31 2017 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 3 October 2017.