



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®2006 = 76.4

PowerEdge M640 (Intel Xeon Gold 6130, 2.10 GHz)

SPECint\_base2006 = 72.8

CPU2006 license: 55

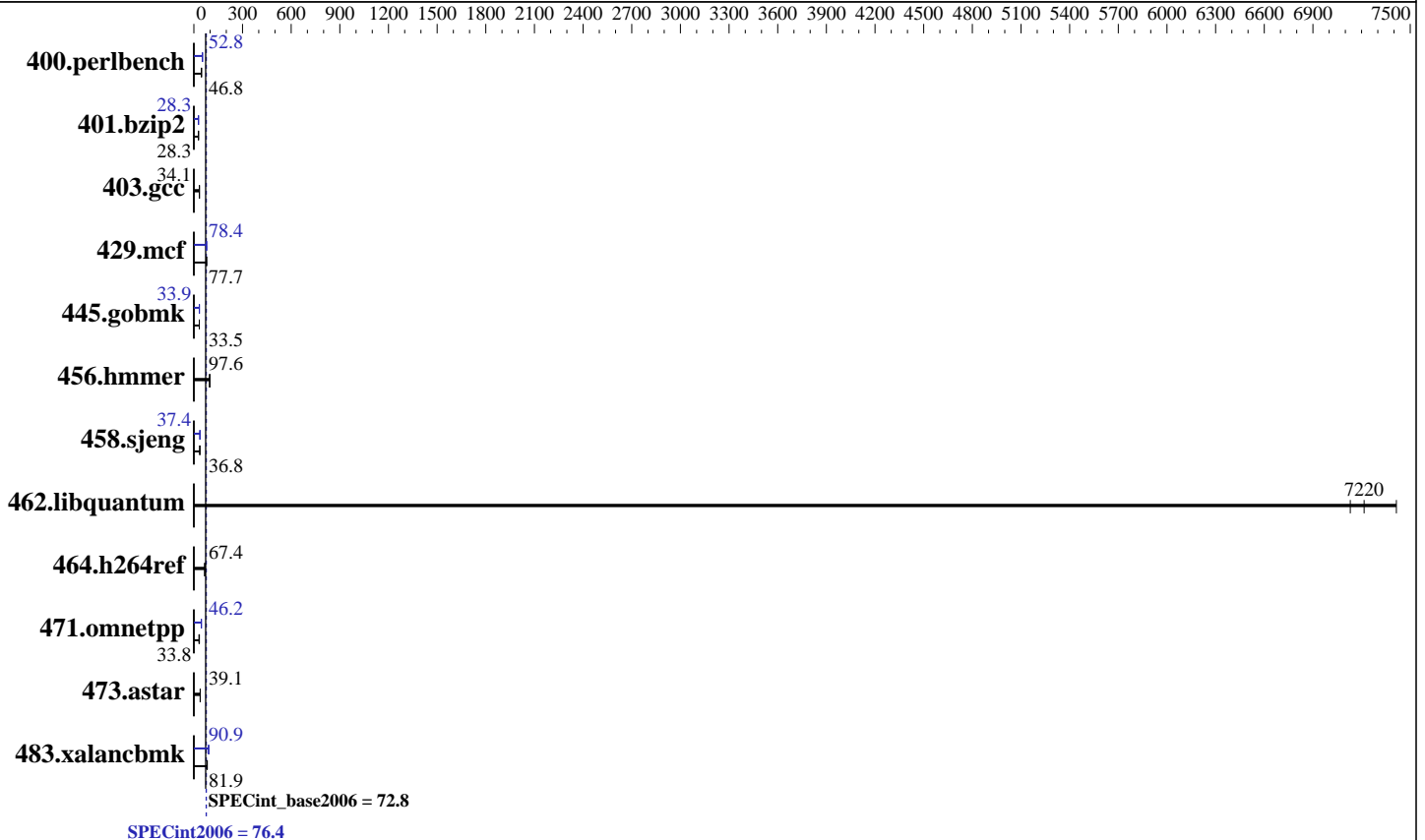
Test date: Sep-2017

Test sponsor: Dell Inc.

Hardware Availability: Sep-2017

Tested by: Dell Inc.

Software Availability: Nov-2016



## Hardware

CPU Name: Intel Xeon Gold 6130  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 2100  
 FPU: Integrated  
 CPU(s) enabled: 32 cores, 2 chips, 16 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 22 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R)  
 Disk Subsystem: 1 x 960 GB SATA SSD  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 12 SP3 (x86\_64) 4.4.70-2-default  
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux  
 Auto Parallel: Yes  
 File System: btrfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	208	46.9	<u>209</u>	<u>46.8</u>	209	46.7	184	53.2	<u>185</u>	<u>52.8</u>	185	52.8
401.bzip2	341	28.3	340	28.4	<u>340</u>	<u>28.3</u>	340	28.4	<u>340</u>	<u>28.3</u>	341	28.3
403.gcc	<u>236</u>	<u>34.1</u>	236	34.1	237	34.0	<u>236</u>	<u>34.1</u>	236	34.1	237	34.0
429.mcf	<u>117</u>	<u>77.7</u>	116	78.8	118	77.2	119	76.9	<u>116</u>	<u>78.4</u>	116	78.9
445.gobmk	313	33.5	<u>313</u>	<u>33.5</u>	312	33.6	310	33.8	310	33.9	<u>310</u>	<u>33.9</u>
456.hammer	95.5	97.7	95.7	97.5	<u>95.6</u>	<u>97.6</u>	95.5	97.7	95.7	97.5	<u>95.6</u>	<u>97.6</u>
458.sjeng	<u>329</u>	<u>36.8</u>	330	36.7	329	36.8	<u>323</u>	<u>37.4</u>	324	37.3	323	37.4
462.libquantum	2.79	7410	2.91	7130	<u>2.87</u>	<u>7220</u>	2.79	7410	2.91	7130	<u>2.87</u>	<u>7220</u>
464.h264ref	329	67.3	328	67.4	<u>328</u>	<u>67.4</u>	329	67.3	328	67.4	<u>328</u>	<u>67.4</u>
471.omnetpp	200	31.2	<u>185</u>	<u>33.8</u>	185	33.8	134	46.5	136	46.1	<u>135</u>	<u>46.2</u>
473.astar	179	39.2	<u>179</u>	<u>39.1</u>	180	39.1	179	39.2	<u>179</u>	<u>39.1</u>	180	39.1
483.xalancbmk	83.2	82.9	84.7	81.5	<u>84.3</u>	<u>81.9</u>	76.0	90.8	75.8	91.0	<u>75.9</u>	<u>90.9</u>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS settings:  
 Sub NUMA Cluster disabled  
 Virtualization Technology disabled  
 System Profile set to Custom  
 CPU Performance set to Maximum Performance  
 C States set to Autonomous  
 C1E disabled  
 Energy Efficient Turbo disabled  
 Uncore Frequency set to Dynamic  
 Energy Efficiency Policy set to Performance  
 Memory Patrol Scrub disabled  
 Logical Processor enabled  
 CPU Interconnect Bus Link Power Management disabled  
 PCI ASPM L1 Link Power Management disabled  
 Sysinfo program /root/cpu2006-1.2\_ic17u3/config/sysinfo.rev6993  
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
 running on linux-ejwa Sat Sep 2 00:01:09 2017  
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## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

```
model name : Intel(R) Xeon(R) Gold 6130 CPU @ 2.10GHz
 2 "physical id"s (chips)
 64 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)
  cpu cores : 16
  siblings  : 32
  physical 0: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
  physical 1: cores 0 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15
cache size : 22528 KB
```

From /proc/meminfo

```
MemTotal:      196682072 kB
HugePages_Total:    0
Hugepagesize:    2048 kB
```

From /etc/\*release\* /etc/\*version\*

```
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 3
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP3"
VERSION_ID="12.3"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp3"
```

uname -a:

```
Linux linux-ejwa 4.4.70-2-default #1 SMP Wed Jun 7 15:12:06 UTC 2017
(4502c76) x86_64 x86_64 x86_64 GNU/Linux
```

run-level 3 Sep 1 23:47

SPEC is set to: /root/cpu2006-1.2\_ic17u3

```
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sda3        btrfs    855G  7.0G  847G   1% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program  
Continued on next page



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## Platform Notes (Continued)

reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 1.0.0 08/10/2017

Memory:

9x 002C00B3002C 18ASF2G72PDZ-2G6D1 16 GB 2 rank 2666 MHz

3x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666 MHz

4x Not Specified Not Specified

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/root/cpu2006-1.2\_icl7u3/lib/ia32:/root/cpu2006-1.2\_icl7u3/lib/intel64:/root/cpu2006-1.2\_icl7u3/sh10.2"

OMP\_NUM\_THREADS = "32"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default.

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop\_caches' prior to run

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -DSPEC\_CPU\_LP64

456.hmmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -DSPEC\_CPU\_LP64

473.astar: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch  
-auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

C++ benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 464.h264ref: -DSPEC\_CPU\_LP64  
 471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -qopt-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div -auto-ilp32 -qopt-prefetch

403.gcc: basepeak = yes

429.mcf: -xCORE-AVX2 -ipo -O3 -no-prec-div -parallel  
 -qopt-prefetch -auto-p32

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2)

456.hmmcr: basepeak = yes

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -qopt-ra-region-strategy=block  
 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

473.astar: basepeak = yes

483.xalancbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
 -Wl,-z,muldefs -L/sh10.2 -lsmartheap



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## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revC.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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