



# SPEC® CINT2006 Result

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Dell Inc.

SPECint®2006 = 35.6

PowerEdge FC640 (Intel Xeon Bronze 3104, 1.70 GHz)

SPECint\_base2006 = 34.5

CPU2006 license: 55

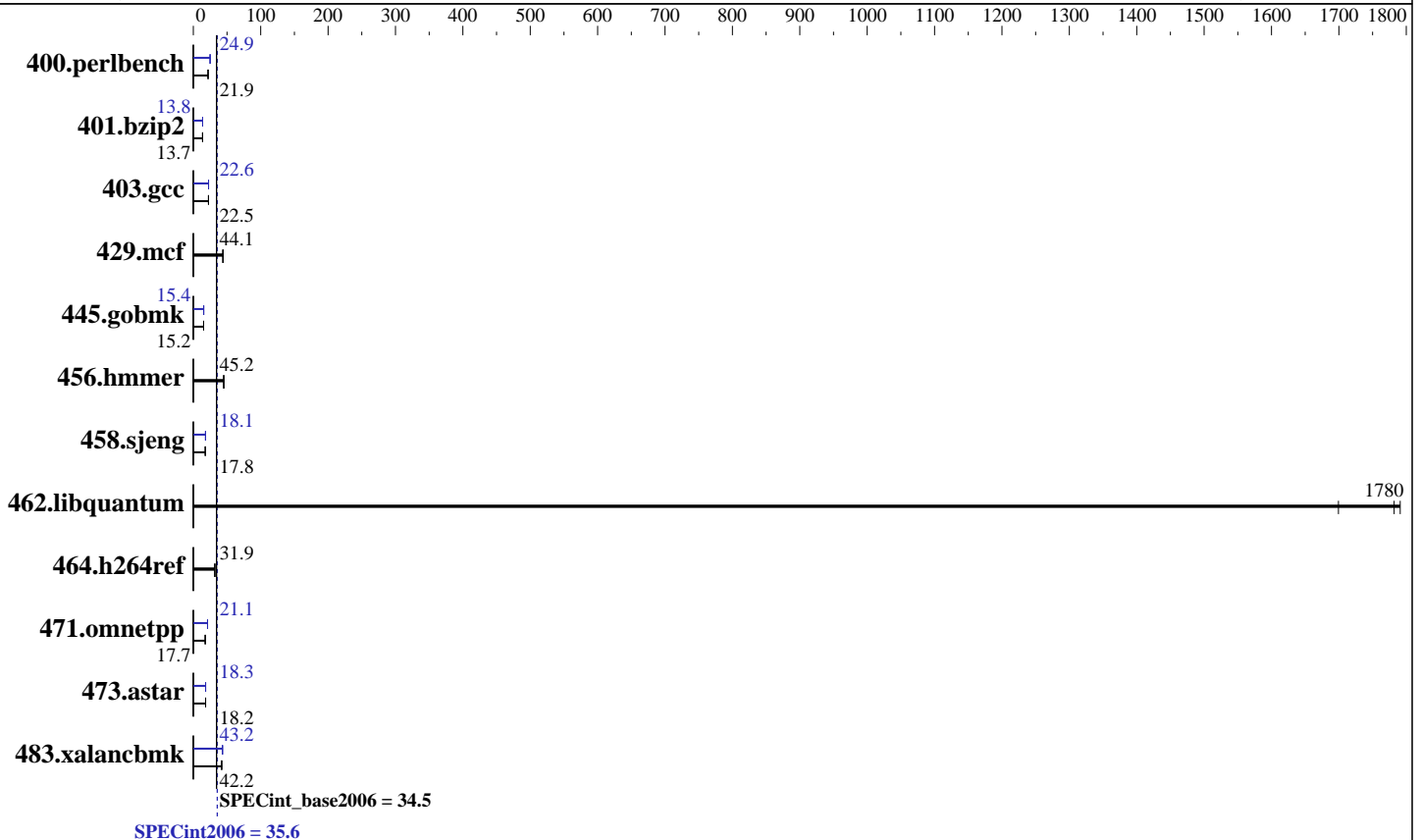
Test date: Sep-2017

Test sponsor: Dell Inc.

Hardware Availability: Sep-2017

Tested by: Dell Inc.

Software Availability: Nov-2016



## Hardware

CPU Name: Intel Xeon Bronze 3104  
 CPU Characteristics:  
 CPU MHz: 1700  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core  
 L3 Cache: 8.25 MB I+D on chip per chip  
 Other Cache: None  
 Memory: 192 GB (12 x 16 GB 2Rx8 PC4-2666V-R, running at 2133 MT/s)  
 Disk Subsystem: 1 x 960 GB SATA SSD  
 Other Hardware: None

## Software

Operating System: SUSE Linux Enterprise Server 12 SP2 (x86\_64) 4.4.16-56-default  
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux  
 Auto Parallel: Yes  
 File System: btrfs  
 System State: Run level 3 (multi-user)  
 Base Pointers: 32/64-bit  
 Peak Pointers: 32/64-bit  
 Other Software: Microquill SmartHeap V10.2



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	445	21.9	<b>447</b>	<b>21.9</b>	448	21.8	392	24.9	<b>392</b>	<b>24.9</b>	392	24.9
401.bzip2	<b>703</b>	<b>13.7</b>	704	13.7	703	13.7	699	13.8	<b>700</b>	<b>13.8</b>	700	13.8
403.gcc	358	22.5	<b>358</b>	<b>22.5</b>	357	22.5	357	22.6	355	22.7	<b>356</b>	<b>22.6</b>
429.mcf	<b>207</b>	<b>44.1</b>	207	44.1	209	43.7	<b>207</b>	<b>44.1</b>	207	44.1	209	43.7
445.gobmk	688	15.3	688	15.2	<b>688</b>	<b>15.2</b>	681	15.4	681	15.4	<b>681</b>	<b>15.4</b>
456.hammer	<b>207</b>	<b>45.2</b>	206	45.2	207	45.1	<b>207</b>	<b>45.2</b>	206	45.2	207	45.1
458.sjeng	681	17.8	682	17.7	<b>682</b>	<b>17.8</b>	<b>669</b>	<b>18.1</b>	668	18.1	669	18.1
462.libquantum	11.6	1790	<b>11.6</b>	<b>1780</b>	12.2	1700	11.6	1790	<b>11.6</b>	<b>1780</b>	12.2	1700
464.h264ref	693	32.0	694	31.9	<b>693</b>	<b>31.9</b>	693	32.0	694	31.9	<b>693</b>	<b>31.9</b>
471.omnetpp	<b>354</b>	<b>17.7</b>	351	17.8	356	17.5	<b>296</b>	<b>21.1</b>	297	21.0	296	21.1
473.astar	<b>385</b>	<b>18.2</b>	385	18.2	384	18.3	<b>384</b>	<b>18.3</b>	384	18.3	385	18.2
483.xalancbmk	165	41.8	163	42.2	<b>163</b>	<b>42.2</b>	160	43.1	<b>160</b>	<b>43.2</b>	158	43.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS settings:  
Sub NUMA Cluster disabled  
Virtualization Technology disabled  
System Profile set to Custom  
CPU Performance set to Maximum Performance  
C States set to Autonomous  
C1E disabled  
Energy Efficient Turbo disabled  
Uncore Frequency set to Dynamic  
Energy Efficiency Policy set to Performance  
Memory Patrol Scrub disabled  
CPU Interconnect Bus Link Power Management disabled  
PCI ASPM L1 Link Power Management disabled  
Sysinfo program /root/cpu2006-1.2\_ic17u3/config/sysinfo.rev6993  
Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
running on linux-u8yg Thu Sep 14 01:28:24 2017

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## Platform Notes (Continued)

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```

From /proc/cpuinfo
model name      : Intel(R) Xeon(R) Bronze 3104 CPU @ 1.70GHz
 2 "physical id"s (chips)
 12 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores    : 6
  siblings     : 6
  physical 0   : cores 0 1 2 3 4 5
  physical 1   : cores 0 1 2 3 4 5
cache size    : 8448 KB

```

```

From /proc/meminfo
MemTotal:      196687636 kB
HugePages_Total: 0
Hugepagesize:  2048 kB

```

```

From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"

```

```

uname -a:
Linux linux-u8yg 4.4.16-56-default #1 SMP Mon Aug 8 14:24:26 UTC 2016
(5b281a8) x86_64 x86_64 x86_64 GNU/Linux

```

run-level 3 Sep 12 01:58

```

SPEC is set to: /root/cpu2006-1.2_ic17u3
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sdal       btrfs    921G   18G  902G   2% /

```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately

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## Platform Notes (Continued)

determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 1.0.0 08/10/2017

Memory:

3x 002C00B3002C 18ASF2G72PDZ-2G6D1 16 GB 2 rank 2666 MHz, configured at 2133 MHz

9x 00AD00B300AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666 MHz, configured at 2133 MHz

4x Not Specified Not Specified

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

KMP\_AFFINITY = "granularity=fine,scatter"

LD\_LIBRARY\_PATH = "/root/cpu2006-1.2\_ic17u3/lib/ia32:/root/cpu2006-1.2\_ic17u3/lib/intel64:/root/cpu2006-1.2\_ic17u3/sh10.2"

OMP\_NUM\_THREADS = "6"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default.

Filesystem page cache cleared with:

shell invocation of 'sync; echo 3 > /proc/sys/vm/drop\_caches' prior to run

## Base Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks:

icpc -m64

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX\_X64

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -DSPEC\_CPU\_LP64

456.hmmer: -DSPEC\_CPU\_LP64

458.sjeng: -DSPEC\_CPU\_LP64

462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

464.h264ref: -DSPEC\_CPU\_LP64

471.omnetpp: -DSPEC\_CPU\_LP64

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## Base Portability Flags (Continued)

473.astar: -DSPEC\_CPU\_LP64  
483.xalancbmk: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -parallel -qopt-prefetch  
-auto-p32

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-Wl,-z,muldefs -L/sh10.2 -lsmartheap64

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc -m64

400.perlbench: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

445.gobmk: icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

C++ benchmarks (except as noted below):

icc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

473.astar: icpc -m64

## Peak Portability Flags

400.perlbench: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX\_IA32

401.bzip2: -DSPEC\_CPU\_LP64

403.gcc: -DSPEC\_CPU\_LP64

429.mcf: -DSPEC\_CPU\_LP64

445.gobmk: -D\_FILE\_OFFSET\_BITS=64

456.hmmer: -DSPEC\_CPU\_LP64

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## Peak Portability Flags (Continued)

458.sjeng: -DSPEC\_CPU\_LP64  
 462.libquantum: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_LINUX  
 464.h264ref: -DSPEC\_CPU\_LP64  
 471.omnetpp: -D\_FILE\_OFFSET\_BITS=64  
 473.astar: -DSPEC\_CPU\_LP64  
 483.xalancbmk: -D\_FILE\_OFFSET\_BITS=64 -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -qopt-prefetch

401.bzp2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div -auto-ilp32 -qopt-prefetch

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div -inline-calloc  
 -qopt-malloc-options=3 -auto-ilp32

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2)

456.hmmer: basepeak = yes

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -unroll4

462.libquantum: basepeak = yes

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)  
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
 -no-prec-div(pass 2) -qopt-ra-region-strategy=block  
 -Wl,-z,muldefs -L/sh10.2 -lsmartheap

473.astar: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
 -auto-p32 -Wl,-z,muldefs -L/sh10.2 -lsmartheap64

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## Peak Optimization Flags (Continued)

483.xalanbmk: -xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch  
-Wl,-z,muldefs -L/sh10.2 -lsmartheap

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revC.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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