



SPEC® CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

H3C

SPECint®_rate2006 = 2370

H3C UniServer R4900 G3 (Intel Xeon Platinum 8164)

SPECint_rate_base2006 = 2280

CPU2006 license: 9066

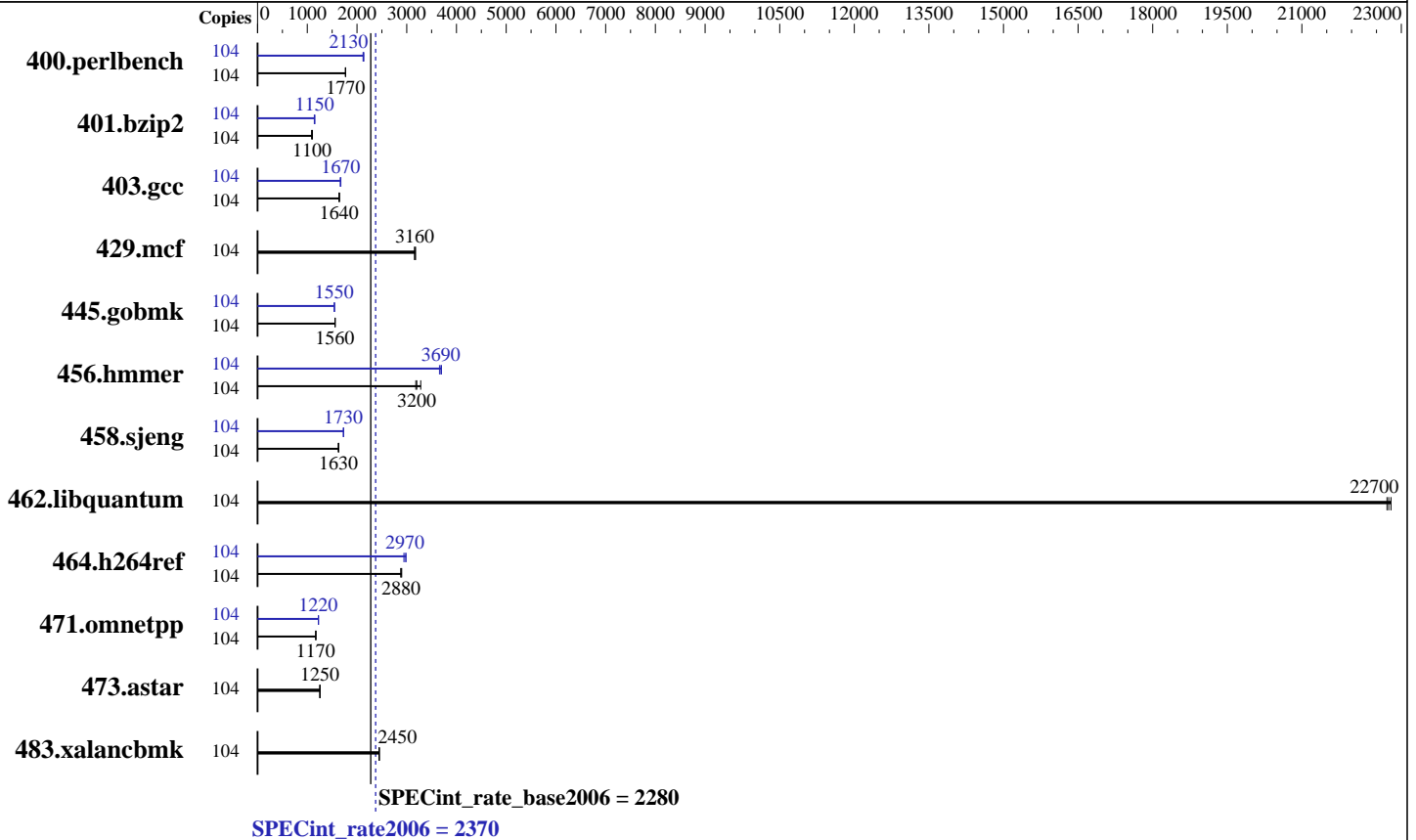
Test date: Aug-2017

Test sponsor: H3C

Hardware Availability: Jul-2017

Tested by: H3C

Software Availability: Sep-2016



Hardware

CPU Name: Intel Xeon Platinum 8164
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 52 cores, 2 chips, 26 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core
 L3 Cache: 35.75 MB I+D on chip per chip
 Other Cache: None
 Memory: 768 GB (24 x 32 GB 2Rx4 PC4-2666V-R)
 Disk Subsystem: 1 x 480 GB SATA SSD
 Other Hardware: None

Software

Operating System: SUSE Linux Enterprise Server 12 SP2
 4.4.21-69-default
 Compiler: C/C++: Version 17.0.0.098 of Intel C/C++
 Compiler for Linux
 Auto Parallel: Yes
 File System: xfs
 System State: Run level 3 (multi-user)
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: Microquill SmartHeap V10.2



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

H3C

SPECint_rate2006 = 2370

H3C UniServer R4900 G3 (Intel Xeon Platinum 8164)

SPECint_rate_base2006 = 2280

CPU2006 license: 9066

Test sponsor: H3C

Tested by: H3C

Test date: Aug-2017

Hardware Availability: Jul-2017

Software Availability: Sep-2016

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	104	574	1770	575	1770	574	1770	104	478	2130	479	2120	475	2140
401.bzip2	104	915	1100	909	1100	924	1090	104	874	1150	880	1140	872	1150
403.gcc	104	508	1650	509	1640	511	1640	104	500	1680	501	1670	504	1660
429.mcf	104	298	3180	300	3160	300	3160	104	298	3180	300	3160	300	3160
445.gobmk	104	699	1560	700	1560	700	1560	104	707	1540	706	1550	706	1550
456.hammer	104	305	3180	296	3280	303	3200	104	262	3700	263	3690	265	3660
458.sjeng	104	776	1620	772	1630	770	1630	104	727	1730	728	1730	730	1720
462.libquantum	104	94.7	22700	94.9	22700	94.5	22800	104	94.7	22700	94.9	22700	94.5	22800
464.h264ref	104	799	2880	794	2900	798	2880	104	770	2990	776	2970	782	2940
471.omnetpp	104	555	1170	555	1170	555	1170	104	531	1220	531	1220	531	1220
473.astar	104	582	1250	582	1260	583	1250	104	582	1250	582	1260	583	1250
483.xalancbmk	104	293	2450	293	2450	293	2450	104	293	2450	293	2450	293	2450

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS configuration:

Set C1E to Disable

Set SNC to Enable

Set IMC Interleaving to 1 way

Set Patrol Scrub to Disable

Set Local/remote Threshold to High

Sysinfo program /home/spec/config/sysinfo.rev6993

Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)

running on linux-57hj Fri Aug 25 19:51:08 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:

<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

From /proc/cpuinfo

model name : Intel(R) Xeon(R) Platinum 8164 CPU @ 2.00GHz

2 "physical id"s (chips)

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

<http://www.spec.org/>



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

H3C

SPECint_rate2006 = 2370

H3C UniServer R4900 G3 (Intel Xeon Platinum 8164)

SPECint_rate_base2006 = 2280

CPU2006 license: 9066

Test date: Aug-2017

Test sponsor: H3C

Hardware Availability: Jul-2017

Tested by: H3C

Software Availability: Sep-2016

Platform Notes (Continued)

104 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The following excerpts from /proc/cpuinfo might not be reliable. Use with caution.)

cpu cores : 26

siblings : 52

physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25
26 27 28 29

physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 16 17 18 19 20 21 22 24 25
26 27 28 29

cache size : 36608 KB

From /proc/meminfo

MemTotal: 790985228 kB

HugePages_Total: 0

Hugepagesize: 2048 kB

/usr/bin/lsb_release -d

SUSE Linux Enterprise Server 12 SP2

From /etc/*release* /etc/*version*

SuSE-release:

SUSE Linux Enterprise Server 12 (x86_64)

VERSION = 12

PATCHLEVEL = 2

This file is deprecated and will be removed in a future service pack or release.

Please check /etc/os-release for details about this release.

os-release:

NAME="SLES"

VERSION="12-SP2"

VERSION_ID="12.2"

PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"

ID="sles"

ANSI_COLOR="0;32"

CPE_NAME="cpe:/o:suse:sles:12:sp2"

uname -a:

Linux linux-57hj 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux

run-level 3 Aug 24 11:50

SPEC is set to: /home/spec

Filesystem Type Size Used Avail Use% Mounted on

/dev/sda3 xfs 401G 13G 389G 4% /

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

H3C

SPECint_rate2006 = 2370

H3C UniServer R4900 G3 (Intel Xeon Platinum 8164)

SPECint_rate_base2006 = 2280

CPU2006 license: 9066

Test sponsor: H3C

Tested by: H3C

Test date: Aug-2017

Hardware Availability: Jul-2017

Software Availability: Sep-2016

Platform Notes (Continued)

BIOS American Megatrends Inc. 1.00.16 07/24/2017

Memory:

24x Samsung M393A4K40BB2-CTD 32 GB 2 rank 2666 MHz

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

LD_LIBRARY_PATH = "/home/spec/libs/32:/home/spec/libs/64:/home/spec/sh10.2"

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM
memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

```
echo 1> /proc/sys/vm/drop_caches
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

C++ benchmarks:

```
icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

Base Portability Flags

```

400.perlbench: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX_IA32
401.bzip2: -D_FILE_OFFSET_BITS=64
403.gcc: -D_FILE_OFFSET_BITS=64
429.mcf: -D_FILE_OFFSET_BITS=64
445.gobmk: -D_FILE_OFFSET_BITS=64
456.hmmer: -D_FILE_OFFSET_BITS=64
458.sjeng: -D_FILE_OFFSET_BITS=64
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
464.h264ref: -D_FILE_OFFSET_BITS=64
471.omnetpp: -D_FILE_OFFSET_BITS=64
473.astar: -D_FILE_OFFSET_BITS=64
483.xalancbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

```



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

H3C

SPECint_rate2006 = 2370

H3C UniServer R4900 G3 (Intel Xeon Platinum 8164)

SPECint_rate_base2006 = 2280

CPU2006 license: 9066

Test sponsor: H3C

Tested by: H3C

Test date: Aug-2017

Hardware Availability: Jul-2017

Software Availability: Sep-2016

Base Optimization Flags

C benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3
```

C++ benchmarks:

```
-xCORE-AVX2 -ipo -O3 -no-prec-div -qopt-prefetch
-qopt-mem-layout-trans=3 -Wl,-z,muldefs -L/sh10.2 -lsmartheap
```

Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):

```
icc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

```
400.perlbench: icc -m64
```

```
401.bzip2: icc -m64
```

```
456.hmmer: icc -m64
```

```
458.sjeng: icc -m64
```

C++ benchmarks:

```
icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32
```

Peak Portability Flags

```
400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
```

```
401.bzip2: -DSPEC_CPU_LP64
```

```
403.gcc: -D_FILE_OFFSET_BITS=64
```

```
429.mcf: -D_FILE_OFFSET_BITS=64
```

```
445.gobmk: -D_FILE_OFFSET_BITS=64
```

```
456.hmmer: -DSPEC_CPU_LP64
```

```
458.sjeng: -DSPEC_CPU_LP64
```

```
462.libquantum: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX
```

```
464.h264ref: -D_FILE_OFFSET_BITS=64
```

```
471.omnetpp: -D_FILE_OFFSET_BITS=64
```

```
473.astar: -D_FILE_OFFSET_BITS=64
```

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 5



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

H3C

SPECint_rate2006 = 2370

H3C UniServer R4900 G3 (Intel Xeon Platinum 8164)

SPECint_rate_base2006 = 2280

CPU2006 license: 9066

Test sponsor: H3C

Tested by: H3C

Test date: Aug-2017

Hardware Availability: Jul-2017

Software Availability: Sep-2016

Peak Portability Flags (Continued)

483.xalanbmk: -D_FILE_OFFSET_BITS=64 -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -auto-ilp32 -qopt-mem-layout-trans=3

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-prefetch -auto-ilp32
-qopt-mem-layout-trans=3

403.gcc: -xCORE-AVX2 -ipo -O3 -no-prec-div
-qopt-mem-layout-trans=3

429.mcf: basepeak = yes

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-mem-layout-trans=3

456.hmmmer: -xCORE-AVX2 -ipo -O3 -no-prec-div -unroll2 -auto-ilp32
-qopt-mem-layout-trans=3

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4 -auto-ilp32
-qopt-mem-layout-trans=3

462.libquantum: basepeak = yes

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -qopt-mem-layout-trans=3

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX2(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2)
-qopt-ra-region-strategy=block
-qopt-mem-layout-trans=3 -Wl,-z,muldefs
-L/sh10.2 -lsmartheap

Continued on next page



SPEC CINT2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

H3C

SPECint_rate2006 = 2370

H3C UniServer R4900 G3 (Intel Xeon Platinum 8164)

SPECint_rate_base2006 = 2280

CPU2006 license: 9066

Test date: Aug-2017

Test sponsor: H3C

Hardware Availability: Jul-2017

Tested by: H3C

Software Availability: Sep-2016

Peak Optimization Flags (Continued)

473.astar: basepeak = yes

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/H3C-Platform-Settings-SKL-V1.1.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/H3C-Platform-Settings-SKL-V1.1.xml>

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.2.

Report generated on Wed Sep 20 11:01:42 2017 by SPEC CPU2006 PS/PDF formatter v6932.

Originally published on 19 September 2017.