



SPEC® CFP2006 Result

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Dell Inc.

PowerEdge C6420 (Intel Xeon Platinum 8156, 3.60 GHz)

SPECfp®_rate2006 = 520

SPECfp_rate_base2006 = 511

CPU2006 license: 55

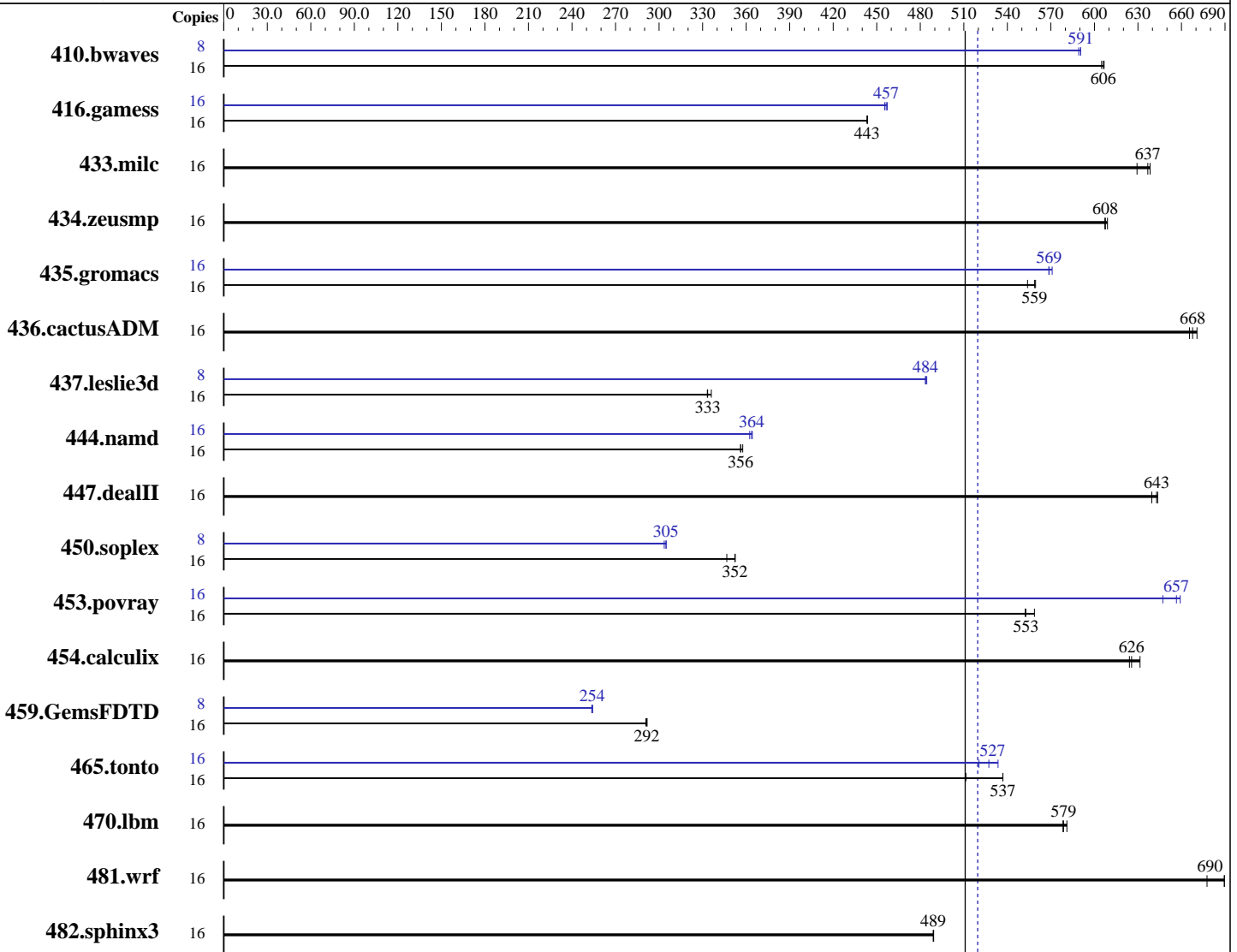
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Jul-2017

Hardware Availability: Jul-2017

Software Availability: Nov-2016



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Hardware

CPU Name: Intel Xeon Platinum 8156
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz
 CPU MHz: 3600
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 1 MB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 SP2 (x86_64) 4.4.21-69-default
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++ Compiler for Linux;
 Fortran: Version 17.0.3.191 of Intel Fortran Compiler for Linux
 Auto Parallel: Yes
 File System: btrfs
 System State: Run level 3 (multi-user)

Continued on next page



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L3 Cache: 16.5 MB I+D on chip per chip
Other Cache: None
Memory: 384 GB (12 x 32 GB 2Rx8 PC4-2666V-R)
Disk Subsystem: 1 x 960 GB SATA SSD
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	16	359	605	<u>359</u>	<u>606</u>	358	607	8	184	591	185	589	<u>184</u>	<u>591</u>
416.gamess	16	<u>707</u>	<u>443</u>	706	444	707	443	16	688	456	<u>686</u>	<u>457</u>	685	457
433.milc	16	233	629	230	638	<u>231</u>	<u>637</u>	16	233	629	230	638	<u>231</u>	<u>637</u>
434.zeusmp	16	<u>240</u>	<u>608</u>	240	607	239	609	16	<u>240</u>	<u>608</u>	240	607	239	609
435.gromacs	16	204	559	<u>204</u>	<u>559</u>	206	554	16	201	569	<u>201</u>	<u>569</u>	200	571
436.cactusADM	16	285	671	<u>286</u>	<u>668</u>	287	665	16	285	671	<u>286</u>	<u>668</u>	287	665
437.leslie3d	16	451	333	448	336	<u>451</u>	<u>333</u>	8	156	484	155	484	<u>155</u>	<u>484</u>
444.namd	16	<u>360</u>	<u>356</u>	360	356	359	358	16	<u>353</u>	<u>364</u>	354	362	352	364
447.dealII	16	284	644	286	640	<u>285</u>	<u>643</u>	16	284	644	286	640	<u>285</u>	<u>643</u>
450.soplex	16	385	347	379	352	<u>379</u>	<u>352</u>	8	219	305	220	304	<u>219</u>	<u>305</u>
453.povray	16	154	552	<u>154</u>	<u>553</u>	152	559	16	129	659	132	647	<u>130</u>	<u>657</u>
454.calculix	16	<u>211</u>	<u>626</u>	209	631	211	624	16	<u>211</u>	<u>626</u>	209	631	211	624
459.GemsFDTD	16	<u>582</u>	<u>292</u>	582	292	584	291	8	334	254	335	254	<u>334</u>	<u>254</u>
465.tonto	16	293	537	<u>293</u>	<u>537</u>	308	511	16	295	534	<u>299</u>	<u>527</u>	303	520
470.lbm	16	378	581	380	578	<u>380</u>	<u>579</u>	16	378	581	380	578	<u>380</u>	<u>579</u>
481.wrf	16	264	678	259	690	<u>259</u>	<u>690</u>	16	264	678	259	690	<u>259</u>	<u>690</u>
482.sphinx3	16	<u>638</u>	<u>489</u>	637	489	638	489	16	<u>638</u>	<u>489</u>	637	489	638	489

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
Sub NUMA Cluster enabled
Virtualization Technology disabled

Continued on next page



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Platform Notes (Continued)

System Profile set to Custom
 CPU Performance set to Maximum Performance
 C States set to autonomous
 C1E disabled
 Uncore Frequency set to Dynamic
 Energy Efficiency Policy set to Performance
 Memory Patrol Scrub disabled
 Logical Processor enabled
 CPU Interconnect Bus Link Power Management disabled
 PCI ASPM L1 Link Power Management disabled
 Sysinfo program /root/cpu2006-1.2_ic17u3/config/sysinfo.rev6993
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)
 running on linux-7lna Fri Jul 14 19:12:19 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) Platinum 8156 CPU @ 3.60GHz
 2 "physical id"s (chips)
 16 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 4
  siblings  : 8
  physical 0: cores 0 5 9 13
  physical 1: cores 1 5 9 13
cache size : 16896 KB
```

```
From /proc/meminfo
MemTotal:      394867844 kB
HugePages_Total:      0
Hugepagesize:    2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

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Platform Notes (Continued)

```
uname -a:
Linux linux-7lna 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jul 14 10:55
```

```
SPEC is set to: /root/cpu2006-1.2_ic17u3
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sda6        btrfs    272G   8.2G 263G   3% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 1.0.8 07/12/2017

Memory:

```
4x 002C00B3002C 36ASF4G72PZ-2G6D1 32 GB 2 rank 2666 MHz
4x 002C0632002C 36ASF4G72PZ-2G6D1 32 GB 2 rank 2666 MHz
4x 00AD063200AD HMA84GR7AFR4N-VK 32 GB 2 rank 2666 MHz
4x Not Specified Not Specified
```

(End of data from sysinfo program)

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/root/cpu2006-1.2_ic17u3/lib/ia32:/root/cpu2006-1.2_ic17u3/lib/intel64:/root/cpu2006-1.2_ic17u3/sh10.2"
```

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

```
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
```

```
runspec command invoked through numactl i.e.:
```

```
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Continued on next page



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Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

C++ benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

Fortran benchmarks:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:
-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32
-qopt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:
icc -m64

Continued on next page



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Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2017/linux/lib/ia32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.deallI: -DSPEC_CPU_LP64
 450.soplex: -D_FILE_OFFSET_BITS=64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
 -par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
 -no-prec-div(pass 2) -fno-alias -auto-ilp32
 -qopt-mem-layout-trans=3

Continued on next page



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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -qopt-malloc-options=3
-qopt-mem-layout-trans=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

Fortran benchmarks:

410.bwaves: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: Same as 410.bwaves

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -unroll4 -auto -inline-calloc
-qopt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)
-par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32
-qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revC.html>



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You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revC.xml>

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Tested with SPEC CPU2006 v1.2.
Report generated on Wed Aug 23 13:13:34 2017 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 22 August 2017.