



# SPEC<sup>®</sup> CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

## Dell Inc.

### SPECfp<sup>®</sup>\_rate2006 = 760

### PowerEdge R640 (Intel Xeon Gold 6128, 3.40 GHz)

### SPECfp\_rate\_base2006 = 747

CPU2006 license: 55

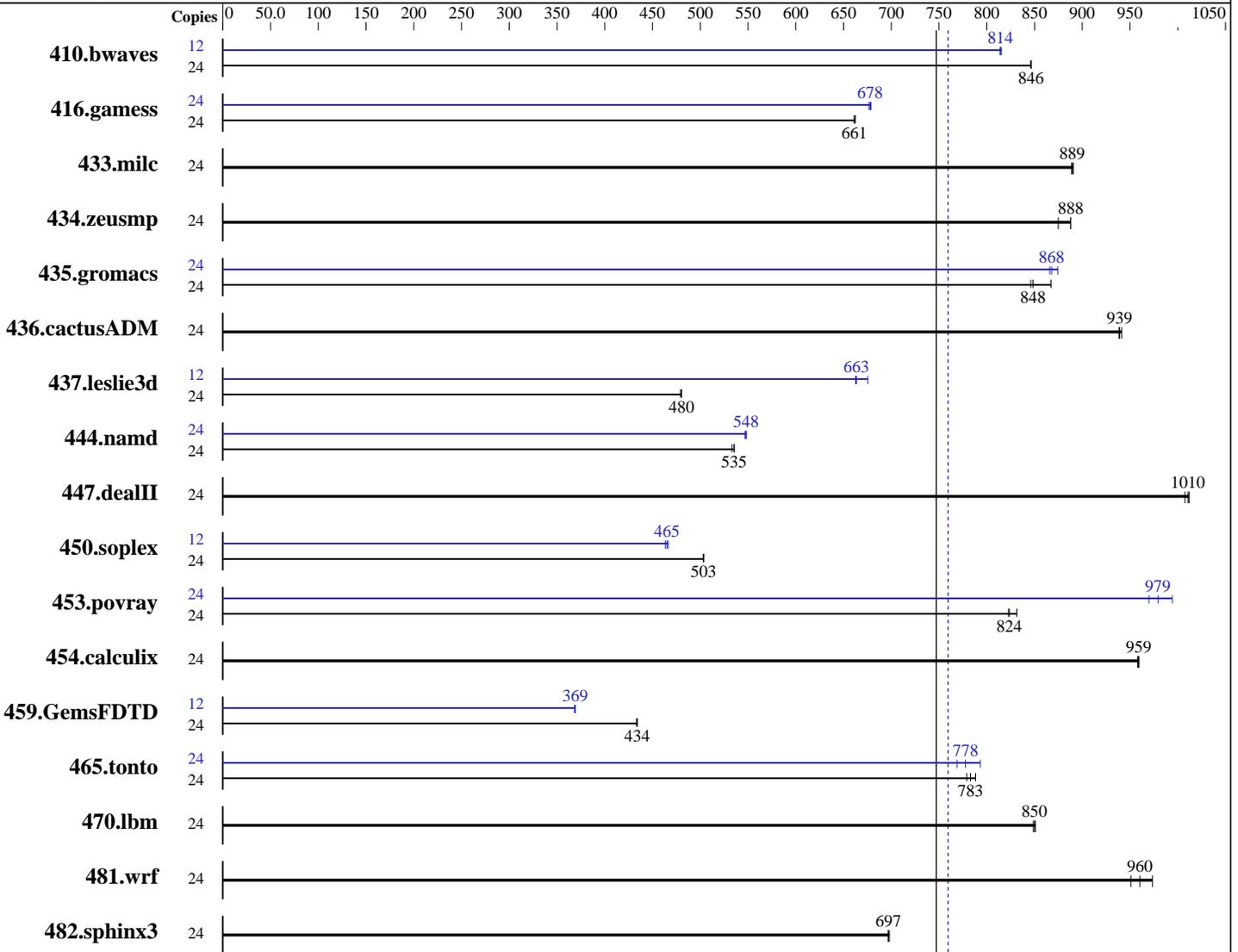
Test date: Jun-2017

Test sponsor: Dell Inc.

Hardware Availability: Jul-2017

Tested by: Dell Inc.

Software Availability: Nov-2016



SPECfp\_rate\_base2006 = 747

**SPECfp\_rate2006 = 760**

#### Hardware

CPU Name: Intel Xeon Gold 6128  
 CPU Characteristics: Intel Turbo Boost Technology up to 3.70 GHz  
 CPU MHz: 3400  
 FPU: Integrated  
 CPU(s) enabled: 12 cores, 2 chips, 6 cores/chip, 2 threads/core  
 CPU(s) orderable: 1,2 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 1 MB I+D on chip per core

Continued on next page

#### Software

Operating System: SUSE Linux Enterprise Server 12 SP2  
 4.4.21-69-default  
 Compiler: C/C++: Version 17.0.3.191 of Intel C/C++  
 Compiler for Linux;  
 Fortran: Version 17.0.3.191 of Intel Fortran  
 Compiler for Linux  
 Auto Parallel: Yes  
 File System: xfs  
 System State: Run level 3 (multi-user)

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp\_rate2006 = 760

PowerEdge R640 (Intel Xeon Gold 6128, 3.40 GHz)

SPECfp\_rate\_base2006 = 747

CPU2006 license: 55

Test date: Jun-2017

Test sponsor: Dell Inc.

Hardware Availability: Jul-2017

Tested by: Dell Inc.

Software Availability: Nov-2016

L3 Cache: 19.25 MB I+D on chip per chip  
Other Cache: None  
Memory: 384 GB (24 x 16 GB 2Rx8 PC4-2666V-R)  
Disk Subsystem: 1 x 400 GB SATA SSD  
Other Hardware: None

Base Pointers: 32/64-bit  
Peak Pointers: 32/64-bit  
Other Software: None

## Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio		
410.bwaves	24	385	846	<u>385</u>	<u>846</u>	385	846	12	200	815	<u>200</u>	<u>814</u>	200	814		
416.gamess	24	710	662	711	661	<u>710</u>	<u>661</u>	24	<u>693</u>	<u>678</u>	692	679	695	676		
433.milc	24	<u>248</u>	<u>889</u>	248	889	247	891	24	<u>248</u>	<u>889</u>	248	889	247	891		
434.zeusmp	24	250	875	246	888	<u>246</u>	<u>888</u>	24	250	875	246	888	<u>246</u>	<u>888</u>		
435.gromacs	24	<u>202</u>	<u>848</u>	198	867	203	846	24	196	874	<u>197</u>	<u>868</u>	198	866		
436.cactusADM	24	306	939	<u>305</u>	<u>939</u>	305	941	24	306	939	<u>305</u>	<u>939</u>	305	941		
437.leslie3d	24	470	480	471	479	<u>470</u>	<u>480</u>	12	170	663	<u>170</u>	<u>663</u>	167	675		
444.namd	24	359	536	361	533	<u>360</u>	<u>535</u>	24	351	548	<u>351</u>	<u>548</u>	352	547		
447.dealII	24	272	1010	<u>272</u>	<u>1010</u>	271	1010	24	272	1010	<u>272</u>	<u>1010</u>	271	1010		
450.soplex	24	397	504	398	503	<u>398</u>	<u>503</u>	12	216	463	<u>215</u>	<u>465</u>	215	466		
453.povray	24	154	831	<u>155</u>	<u>824</u>	155	823	24	<u>130</u>	<u>979</u>	128	994	132	970		
454.calculix	24	206	959	207	958	<u>206</u>	<u>959</u>	24	206	959	207	958	<u>206</u>	<u>959</u>		
459.GemsFDTD	24	587	434	<u>587</u>	<u>434</u>	588	433	12	346	368	345	369	<u>345</u>	<u>369</u>		
465.tonto	24	300	788	<u>302</u>	<u>783</u>	303	779	24	<u>304</u>	<u>778</u>	298	793	307	769		
470.lbm	24	388	849	388	851	<u>388</u>	<u>850</u>	24	388	849	388	851	<u>388</u>	<u>850</u>		
481.wrf	24	275	974	<u>279</u>	<u>960</u>	282	951	24	275	974	<u>279</u>	<u>960</u>	282	951		
482.sphinx3	24	670	698	671	697	<u>671</u>	<u>697</u>	24	670	698	671	697	<u>671</u>	<u>697</u>		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

## Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

## Platform Notes

BIOS settings:  
Sub NUMA Cluster enabled  
Virtualization Technology disabled

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp\_rate2006 = 760

PowerEdge R640 (Intel Xeon Gold 6128, 3.40 GHz)

SPECfp\_rate\_base2006 = 747

CPU2006 license: 55

Test date: Jun-2017

Test sponsor: Dell Inc.

Hardware Availability: Jul-2017

Tested by: Dell Inc.

Software Availability: Nov-2016

## Platform Notes (Continued)

System Profile set to Custom  
 CPU Performance set to Maximum Performance  
 C States set to autonomous  
 C1E disabled  
 Uncore Frequency set to Dynamic  
 Energy Efficiency Policy set to Performance  
 Memory Patrol Scrub disabled  
 Logical Processor enabled  
 CPU Interconnect Bus Link Power Management disabled  
 PCI ASPM L1 Link Power Management disabled  
 Sysinfo program /root/cpu2006-1.2\_ic17u3/config/sysinfo.rev6993  
 Revision 6993 of 2015-11-06 (b5e8d4b4eb51ed28d7f98696cbe290c1)  
 running on linux-6cje Thu Jun 1 19:04:45 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see:  
<http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) Gold 6128 CPU @ 3.40GHz
 2 "physical id"s (chips)
 24 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 6
  siblings  : 12
  physical 0: cores 0 6 9 10 11 13
  physical 1: cores 0 6 9 10 11 13
cache size : 19712 KB
```

```
From /proc/meminfo
MemTotal: 394732808 kB
HugePages_Total: 0
Hugepagesize: 2048 kB
```

```
/usr/bin/lsb_release -d
SUSE Linux Enterprise Server 12 SP2
```

```
From /etc/*release* /etc/*version*
SuSE-release:
SUSE Linux Enterprise Server 12 (x86_64)
VERSION = 12
PATCHLEVEL = 2
# This file is deprecated and will be removed in a future service pack or
release.
# Please check /etc/os-release for details about this release.
os-release:
NAME="SLES"
VERSION="12-SP2"
VERSION_ID="12.2"
PRETTY_NAME="SUSE Linux Enterprise Server 12 SP2"
```

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp\_rate2006 = 760

PowerEdge R640 (Intel Xeon Gold 6128, 3.40 GHz)

SPECfp\_rate\_base2006 = 747

CPU2006 license: 55

Test date: Jun-2017

Test sponsor: Dell Inc.

Hardware Availability: Jul-2017

Tested by: Dell Inc.

Software Availability: Nov-2016

## Platform Notes (Continued)

```
ID="sles"
ANSI_COLOR="0;32"
CPE_NAME="cpe:/o:suse:sles:12:sp2"
```

```
uname -a:
Linux linux-6cje 4.4.21-69-default #1 SMP Tue Oct 25 10:58:20 UTC 2016
(9464f67) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jun 1 11:28
```

```
SPEC is set to: /root/cpu2006-1.2_ic17u3
Filesystem      Type  Size  Used Avail Use% Mounted on
/dev/sda3        xfs   370G   15G  356G   4% /
```

Additional information from dmidecode:

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

BIOS Dell Inc. 1.0.0 05/16/2017

Memory:

```
12x 00AD063200AD HMA82GR7AFR8N-VK 16 GB 2 rank 2666 MHz
12x 00CE063200CE M393A2K43BB1-CTD 16 GB 2 rank 2666 MHz
```

(End of data from sysinfo program)

## General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/root/cpu2006-1.2_ic17u3/lib/ia32:/root/cpu2006-1.2_ic17u3/lib/intel64:/root/cpu2006-1.2_ic17u3/sh10.2"
```

Binaries compiled on a system with 1x Intel Core i7-4790 CPU + 32GB RAM memory using Redhat Enterprise Linux 7.2

Transparent Huge Pages enabled by default

Filesystem page cache cleared with:

```
shell invocation of 'sync; echo 3 > /proc/sys/vm/drop_caches' prior to run
```

runspec command invoked through numactl i.e.:

```
numactl --interleave=all runspec <etc>
```

## Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Continued on next page

Standard Performance Evaluation Corporation

info@spec.org

http://www.spec.org/

Page 4



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp\_rate2006 = 760

PowerEdge R640 (Intel Xeon Gold 6128, 3.40 GHz)

SPECfp\_rate\_base2006 = 747

CPU2006 license: 55

Test date: Jun-2017

Test sponsor: Dell Inc.

Hardware Availability: Jul-2017

Tested by: Dell Inc.

Software Availability: Nov-2016

## Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Base Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.dealII: -DSPEC\_CPU\_LP64  
 450.soplex: -DSPEC\_CPU\_LP64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Base Optimization Flags

C benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch -auto-p32  
-qopt-mem-layout-trans=3



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp\_rate2006 = 760

PowerEdge R640 (Intel Xeon Gold 6128, 3.40 GHz)

SPECfp\_rate\_base2006 = 747

CPU2006 license: 55

Test date: Jun-2017

Test sponsor: Dell Inc.

Hardware Availability: Jul-2017

Tested by: Dell Inc.

Software Availability: Nov-2016

## Peak Compiler Invocation

C benchmarks:

icc -m64

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers\_and\_libraries\_2017/linux/lib/ia32

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

## Peak Portability Flags

410.bwaves: -DSPEC\_CPU\_LP64  
 416.gamess: -DSPEC\_CPU\_LP64  
 433.milc: -DSPEC\_CPU\_LP64  
 434.zeusmp: -DSPEC\_CPU\_LP64  
 435.gromacs: -DSPEC\_CPU\_LP64 -nofor\_main  
 436.cactusADM: -DSPEC\_CPU\_LP64 -nofor\_main  
 437.leslie3d: -DSPEC\_CPU\_LP64  
 444.namd: -DSPEC\_CPU\_LP64  
 447.deallI: -DSPEC\_CPU\_LP64  
 450.soplex: -D\_FILE\_OFFSET\_BITS=64  
 453.povray: -DSPEC\_CPU\_LP64  
 454.calculix: -DSPEC\_CPU\_LP64 -nofor\_main  
 459.GemsFDTD: -DSPEC\_CPU\_LP64  
 465.tonto: -DSPEC\_CPU\_LP64  
 470.lbm: -DSPEC\_CPU\_LP64  
 481.wrf: -DSPEC\_CPU\_LP64 -DSPEC\_CPU\_CASE\_FLAG -DSPEC\_CPU\_LINUX  
 482.sphinx3: -DSPEC\_CPU\_LP64

## Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

Continued on next page



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp\_rate2006 = 760

PowerEdge R640 (Intel Xeon Gold 6128, 3.40 GHz)

SPECfp\_rate\_base2006 = 747

CPU2006 license: 55

Test date: Jun-2017

Test sponsor: Dell Inc.

Hardware Availability: Jul-2017

Tested by: Dell Inc.

Software Availability: Nov-2016

## Peak Optimization Flags (Continued)

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -fno-alias -auto-ilp32  
-qopt-mem-layout-trans=3

447.dealII: basepeak = yes

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -qopt-malloc-options=3  
-qopt-mem-layout-trans=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -qopt-mem-layout-trans=3

### Fortran benchmarks:

410.bwaves: -xCORE-AVX512 -ipo -O3 -no-prec-div -qopt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll2 -inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: Same as 410.bwaves

459.GemsFDTD: Same as 410.bwaves

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -ipo(pass 2) -O3(pass 2)  
-no-prec-div(pass 2) -unroll4 -auto -inline-calloc  
-qopt-malloc-options=3

### Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xCORE-AVX512(pass 2)  
-par-num-threads=1(pass 1) -qopt-prefetch -auto-ilp32  
-qopt-mem-layout-trans=3

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



# SPEC CFP2006 Result

Copyright 2006-2017 Standard Performance Evaluation Corporation

Dell Inc.

SPECfp\_rate2006 = 760

PowerEdge R640 (Intel Xeon Gold 6128, 3.40 GHz)

SPECfp\_rate\_base2006 = 747

CPU2006 license: 55

Test date: Jun-2017

Test sponsor: Dell Inc.

Hardware Availability: Jul-2017

Tested by: Dell Inc.

Software Availability: Nov-2016

The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revC.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic17.0-official-linux64-revF.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge14G-revC.xml>

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.  
For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

Tested with SPEC CPU2006 v1.2.  
Report generated on Wed Aug 23 13:12:40 2017 by SPEC CPU2006 PS/PDF formatter v6932.  
Originally published on 22 August 2017.