



SPEC[®] CFP2006 Result

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Dell Inc.

SPECfp[®]_rate2006 = 941

PowerEdge C6320 (Intel Xeon E5-2690 v4, 2.60 GHz)

SPECfp_rate_base2006 = 917

CPU2006 license: 55

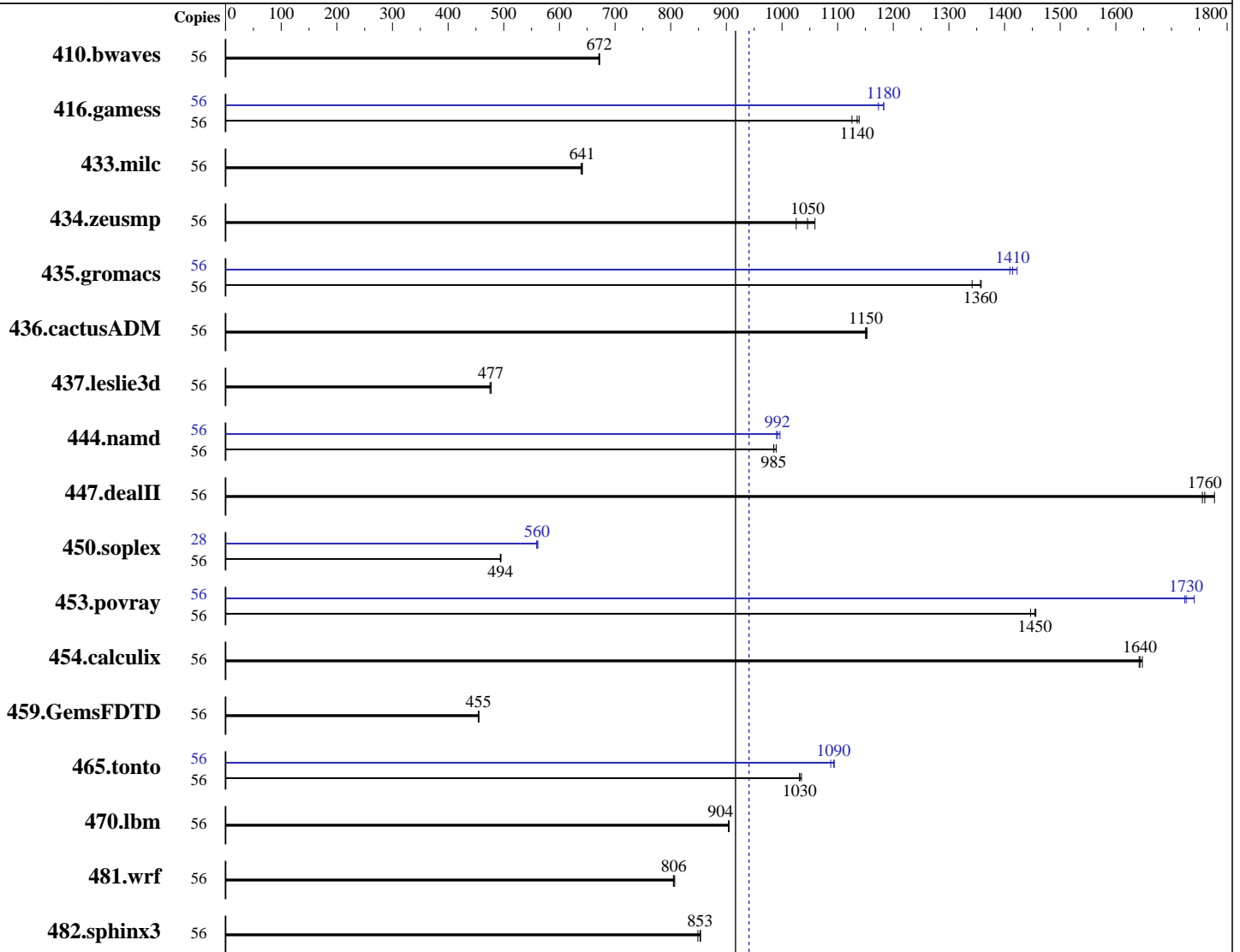
Test date: Jul-2017

Test sponsor: Dell Inc.

Hardware Availability: Jun-2016

Tested by: Dell Inc.

Software Availability: Mar-2016



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Hardware

CPU Name: Intel Xeon E5-2690 v4
 CPU Characteristics: Intel Turbo Boost Technology up to 3.50 GHz
 CPU MHz: 2600
 FPU: Integrated
 CPU(s) enabled: 28 cores, 2 chips, 14 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chip
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

Continued on next page

Software

Operating System: SUSE Linux Enterprise Server 12 SP3 (x86_64) 4.4.70-2-default
 Compiler: C/C++: Version 16.0.2.181 of Intel C++ Studio XE for Linux;
 Fortran: Version 16.0.2.181 of Intel Fortran Studio XE for Linux
 Auto Parallel: No
 File System: btrfs
 System State: Run level 3 (multi-user)

Continued on next page



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L3 Cache: 35 MB I+D on chip per chip
Other Cache: None
Memory: 256 GB (8 x 32 GB 2Rx8 PC4-2400T-R)
Disk Subsystem: 1 TB SATA 7.2K
Other Hardware: None

Base Pointers: 32/64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	56	1132	672	<u>1133</u>	<u>672</u>	1134	671	56	1132	672	<u>1133</u>	<u>672</u>	1134	671
416.gamess	56	974	1130	963	1140	<u>966</u>	<u>1140</u>	56	934	1170	<u>927</u>	<u>1180</u>	927	1180
433.milc	56	804	639	<u>802</u>	<u>641</u>	802	641	56	804	639	<u>802</u>	<u>641</u>	802	641
434.zeusmp	56	<u>487</u>	<u>1050</u>	497	1030	481	1060	56	<u>487</u>	<u>1050</u>	497	1030	481	1060
435.gromacs	56	294	1360	<u>295</u>	<u>1360</u>	298	1340	56	<u>283</u>	<u>1410</u>	281	1420	284	1410
436.cactusADM	56	<u>581</u>	<u>1150</u>	580	1150	582	1150	56	<u>581</u>	<u>1150</u>	580	1150	582	1150
437.leslie3d	56	<u>1104</u>	<u>477</u>	1102	478	1108	475	56	<u>1104</u>	<u>477</u>	1102	478	1108	475
444.namd	56	454	990	<u>456</u>	<u>985</u>	456	985	56	451	996	<u>453</u>	<u>992</u>	454	990
447.dealII	56	<u>364</u>	<u>1760</u>	360	1780	365	1760	56	<u>364</u>	<u>1760</u>	360	1780	365	1760
450.soplex	56	943	495	946	494	<u>945</u>	<u>494</u>	28	416	561	418	559	<u>417</u>	<u>560</u>
453.povray	56	205	1460	206	1450	<u>205</u>	<u>1450</u>	56	171	1740	173	1720	<u>173</u>	<u>1730</u>
454.calculix	56	280	1650	281	1640	<u>281</u>	<u>1640</u>	56	280	1650	281	1640	<u>281</u>	<u>1640</u>
459.GemsFDTD	56	1308	454	<u>1306</u>	<u>455</u>	1304	455	56	1308	454	<u>1306</u>	<u>455</u>	1304	455
465.tonto	56	532	1030	534	1030	<u>534</u>	<u>1030</u>	56	507	1090	504	1090	<u>504</u>	<u>1090</u>
470.lbm	56	850	905	<u>851</u>	<u>904</u>	851	904	56	850	905	<u>851</u>	<u>904</u>	851	904
481.wrf	56	775	807	<u>776</u>	<u>806</u>	777	805	56	775	807	<u>776</u>	<u>806</u>	777	805
482.sphinx3	56	1278	854	<u>1280</u>	<u>853</u>	1286	849	56	1278	854	<u>1280</u>	<u>853</u>	1286	849

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The numactl mechanism was used to bind copies to processors. The config file option 'submit' was used to generate numactl commands to bind each copy to a specific processor. For details, please see the config file.

Operating System Notes

Stack size set to unlimited using "ulimit -s unlimited"

Platform Notes

BIOS settings:
Snoop Mode set to Cluster on Die
Virtualization Technology disabled

Continued on next page



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Platform Notes (Continued)

System Profile set to custom
 CPU Performance set to Hardware P States
 C States set to Autonomous
 C1E disabled
 Energy Efficient Turbo disabled
 Uncore Frequency set to Dynamic
 Energy Efficiency Policy set to Balanced Performance
 Memory Patrol Scrub disabled
 Sysinfo program /root/cpu2006-1.2/config/sysinfo.rev6914
 \$Rev: 6914 \$ \$Date:: 2014-06-25 # \$ e3fbb8667b5a285932ceab81e28219e1
 running on linux-jze7 Tue Jul 11 07:00:54 2017

This section contains SUT (System Under Test) info as seen by some common utilities. To remove or add to this section, see: <http://www.spec.org/cpu2006/Docs/config.html#sysinfo>

```
From /proc/cpuinfo
model name : Intel(R) Xeon(R) CPU E5-2690 v4 @ 2.60GHz
 2 "physical id"s (chips)
 56 "processors"
cores, siblings (Caution: counting these is hw and system dependent. The
following excerpts from /proc/cpuinfo might not be reliable. Use with
caution.)
  cpu cores : 14
  siblings  : 28
  physical 0: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
  physical 1: cores 0 1 2 3 4 5 6 8 9 10 11 12 13 14
cache size : 17920 KB
```

```
From /proc/meminfo
MemTotal:      264035840 kB
HugePages_Total: 0
Hugepagesize:  2048 kB
```

```
From /etc/*release* /etc/*version*
SuSE-release:
  SUSE Linux Enterprise Server 12 (x86_64)
  VERSION = 12
  PATCHLEVEL = 3
  # This file is deprecated and will be removed in a future service pack or
  # release.
  # Please check /etc/os-release for details about this release.
os-release:
  NAME="SLES"
  VERSION="12-SP3"
  VERSION_ID="12.3"
  PRETTY_NAME="SUSE Linux Enterprise Server 12 SP3"
  ID="sles"
  ANSI_COLOR="0;32"
  CPE_NAME="cpe:/o:suse:sles:12:sp3"
```

uname -a:

Continued on next page



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Platform Notes (Continued)

```
Linux linux-jze7 4.4.70-2-default #1 SMP Wed Jun 7 15:12:06 UTC 2017
(4502c76) x86_64 x86_64 x86_64 GNU/Linux
```

```
run-level 3 Jul 10 20:31
```

```
SPEC is set to: /root/cpu2006-1.2
```

```
Filesystem      Type      Size  Used Avail Use% Mounted on
/dev/sda2        btrfs    930G  7.7G  920G   1% /
```

```
Additional information from dmidecode:
```

Warning: Use caution when you interpret this section. The 'dmidecode' program reads system data which is "intended to allow hardware to be accurately determined", but the intent may not be met, as there are frequent changes to hardware, firmware, and the "DMTF SMBIOS" standard.

```
BIOS Dell Inc. 2.5.0 12/05/2016
```

```
Memory:
```

```
6x 00AD00B300AD HMA84GR7MFR4N-UH 32 GB 2 rank 2400 MHz
2x 00CE00B300CE M393A4K40BB1-CRC 32 GB 2 rank 2400 MHz
8x Not Specified Not Specified
```

```
(End of data from sysinfo program)
```

General Notes

Environment variables set by runspec before the start of the run:

```
LD_LIBRARY_PATH = "/root/cpu2006-1.2/libs/32:/root/cpu2006-1.2/libs/64:/root/cpu2006-1.2/sh"
```

```
Binaries compiled on a system with 1x Intel Core i7-4790K CPU + 32GB
memory using RedHat EL 7.2 glibc 2.17
```

```
Transparent Huge Pages enabled with:
```

```
echo always > /sys/kernel/mm/transparent_hugepage/enabled
```

```
Filesystem page cache cleared with:
```

```
echo 1 > /proc/sys/vm/drop_caches
```

```
runspec command invoked through numactl i.e.:
```

```
numactl --interleave=all runspec <etc>
```

Base Compiler Invocation

C benchmarks:

```
icc -m64
```

C++ benchmarks:

```
icpc -m64
```

Fortran benchmarks:

```
ifort -m64
```

Continued on next page



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Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

C++ benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Fortran benchmarks:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch

Benchmarks using both Fortran and C:

-xCORE-AVX2 -ipo -O3 -no-prec-div -opt-prefetch -auto-p32
-ansi-alias -opt-mem-layout-trans=3

Peak Compiler Invocation

C benchmarks:

icc -m64

Continued on next page



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Peak Compiler Invocation (Continued)

C++ benchmarks (except as noted below):

icpc -m64

450.soplex: icpc -m32 -L/opt/intel/compilers_and_libraries_2016/linux/compiler/lib/ia32_lin

Fortran benchmarks:

ifort -m64

Benchmarks using both Fortran and C:

icc -m64 ifort -m64

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.deallI: -DSPEC_CPU_LP64
 450.soplex: -D_FILE_OFFSET_BITS=64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: basepeak = yes

482.sphinx3: basepeak = yes

C++ benchmarks:

444.namd: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
 -ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
 -par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
 -prof-use(pass 2) -fno-alias -auto-ilp32

Continued on next page



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Peak Optimization Flags (Continued)

447.dealII: basepeak = yes

450.soplex: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-malloc-options=3

453.povray: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll2
-inline-level=0 -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -prof-use(pass 2) -unroll4 -auto
-inline-calloc -opt-malloc-options=3

Benchmarks using both Fortran and C:

435.gromacs: -xCORE-AVX2(pass 2) -prof-gen:threadsafe(pass 1)
-ipo(pass 2) -O3(pass 2) -no-prec-div(pass 2)
-par-num-threads=1(pass 1) -opt-mem-layout-trans=3(pass 2)
-prof-use(pass 2) -opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes



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The flags files that were used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64-revB.html>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge13G-revE.html>

You can also download the XML flags sources by saving the following links:

<http://www.spec.org/cpu2006/flags/Intel-ic16.0-official-linux64-revB.xml>

<http://www.spec.org/cpu2006/flags/Dell-Platform-Flags-PowerEdge13G-revE.xml>

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